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# NAVAL SHIP RESEARCH AND DEVELOPMENT CENTER

Bethesda, Md. 20034



## LOW COST, HIGH ACCURACY INSTRUMENTATION TAPE RECORDER

by

Robert G. Stilwell

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## CENTRAL INSTRUMENTATION DEPARTMENT RESEARCH AND DEVELOPMENT REPORT

LOW COST, HIGH ACCURACY INSTRUMENTATION TAPE RECORDER

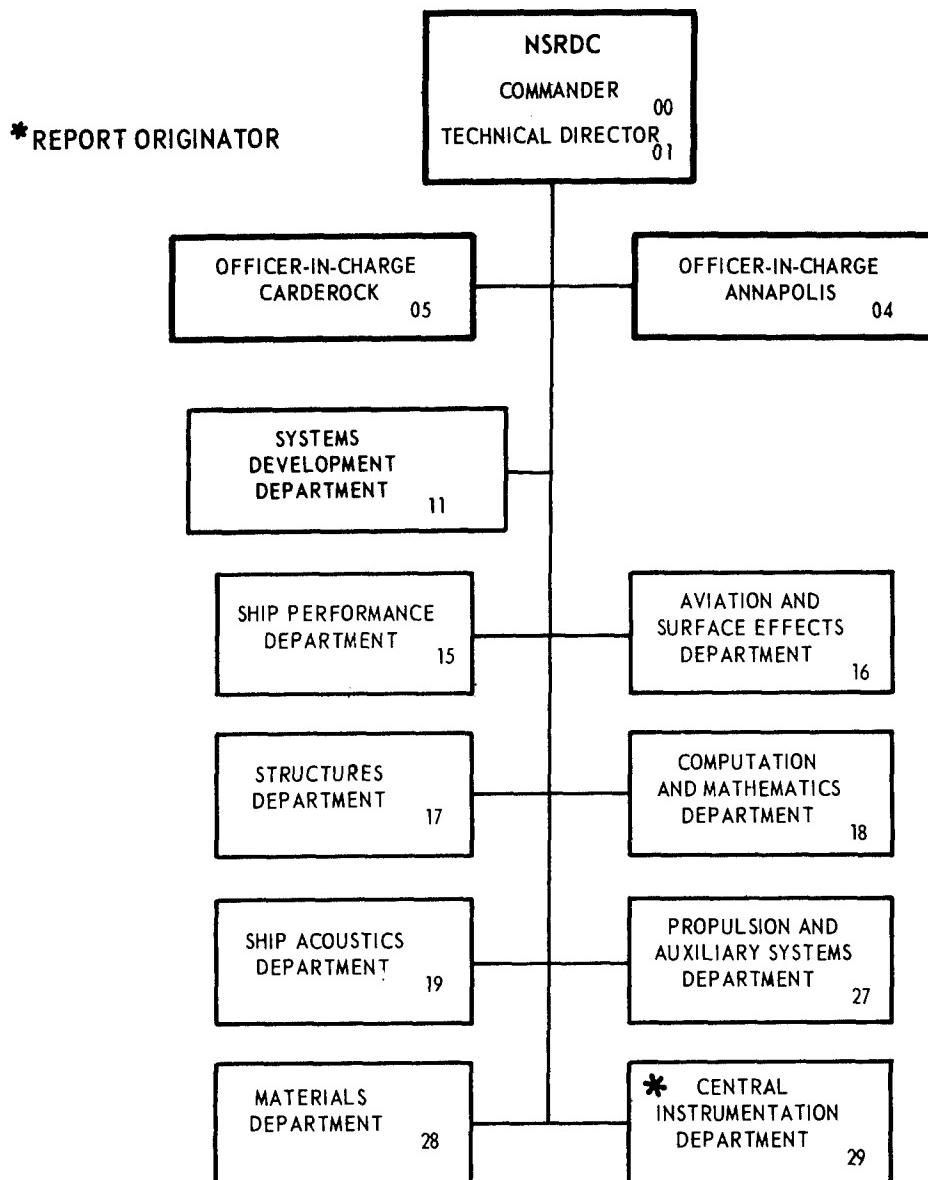
December 1972

Report 3875

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Naval Ship Research and Development Center  
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DEPARTMENT OF THE NAVY  
NAVAL SHIP RESEARCH AND DEVELOPMENT CENTER  
BETHESDA, MD. 20034

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## SUMMARY

### STATEMENT OF PROBLEM

Develop an instrumentation quality magnetic tape recorder which utilizes a new recording technique: differential pulse width modulation. Demonstrate the probable advantages: low cost, high accuracy, light weight, and ease of operation. Provide the recorder with analog outputs for field or laboratory use, and digital outputs for reproducing analog data directly into a high-speed digital computer. Use this recorder to investigate the entire data recording and retrieval process and determine overall system performance from the computer-reduced data.

### CONCLUSIONS

The recorder fulfills or exceeds each of the initial objectives. It possesses several important advantages over the modern instrumentation recorders currently being used in Navy technical programs. It is more highly accurate than any but the best (and most expensive) commercial instrumentation recorders; it is light in weight since no high-inertia components are required; and it is less costly to produce and to operate (less than one-third that of commercial recorders). Additionally it will simultaneously produce analog outputs for chart or oscilloscope display and digital outputs for playback into a digital computer without any intermediate format translations.

The equipment developed to date has demonstrated frequency response capability to 2500 Hz, sufficient for most Navy applications. This response and the resultant digital bit rate challenge modern "high-speed" digital computer capabilities.

### RECOMMENDATIONS

1. Although the additional digital bit rate required presently precludes increasing the recorder "digital bandwidth," techniques should be developed to extend the bandwidth for analog outputs.
2. Methods should be investigated for more direct access to high-speed digital computers than those used in this development. This can further reduce data reduction time, the amount of equipment involved, and hence the cost.
3. A production development program (perhaps conducted by commercial concerns) should be initiated to refine the properties of the tape recorder.

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## NOTATION AND ABBREVIATIONS

<i>A</i>	The <i>A</i> counter output count for the DPWM positive portion
AM	Amplitude Modulation
<i>B</i>	The <i>B</i> counter output count for the DPWM negative portion
bpi	Bits per inch
DMTR	Digital Magnetic Tape Recorder
DPWM	Differential Pulse Width Modulation
DSID	Differential Staggered Incremental Demodulator
DTL	Diode-Transistor Logic
FM	Frequency Modulation
<i>f<sub>c</sub></i>	DPWM average carrier frequency
<i>f<sub>m</sub></i>	Modulating frequency
<i>f<sub>r</sub></i>	Triangular wave frequency
<i>m<sub>c</sub></i>	Slope of triangular wave
<i>m<sub>m</sub></i>	Slope of modulating signal
PAM	Pulse Amplitude Modulation
PCM	Pulse Code Modulation
PLL	Phase-Locked Loop
PM	Phase Modulation
PPM	Pulse Position Modulation
PWM	Pulse Width Modulation
SYMMETRY, <i>P</i>	The portion of the DPWM sample which is positive expressed as a percentage
TTL	Transistor-Transistor Logic
<i>t<sub>s</sub></i>	Switchover time of DPWM sample
<i>V<sub>1</sub></i>	The voltage calculated from the digital data from the equation
	$V_1 = \left[ \frac{512 \cdot A}{A + B} - 256 \right] / 100$
<i>V<sub>2</sub></i>	The voltage calculated from the digital data from the equation
	$V_2 = (A - 256) / 100$

$v_c$ , $v_r$	Triangular wave voltage
$v_m$	Modulating voltage
$\sigma_1$	The standard deviation of $V_1$
$\sigma_2$	The standard deviation of $V_2$

## **ABSTRACT**

This report describes a new magnetic tape recording system for general instrumentation use. The system uses a new type of modulation format and offers excellent performance at low cost. The system concepts are explored from a subsystem or "block diagram" viewpoint, and extensions of these concepts are hypothesized. An actual prototype is described, its specifications and performance parameters given, and the results of its evaluation program presented. The history of the system from 1967 to 1971 is also included for completeness. The differential pulse width modulation (DPWM) concept is considered to represent a truly significant advance in modulation techniques.

## **ADMINISTRATIVE INFORMATION**

The work described herein was performed by the Central Instrumentation Department (Code 29) as part of the in-house Independent Exploratory Development (IED) Program of the Naval Ship Research and Development Center (NSRDC). Funding was provided under Program Element 62713N, Project FXX412, Task Area ZFXX412001.

A United States patent to protect this work has been applied for as Navy Case 50,526.

## **INTRODUCTION**

Instrumentation tape recorders suitable for recording physical phenomena are notoriously deficient. Their accuracy and precision, cost, operability, and maintainability often leave much to be desired. In addition, their output format is generally not suited for direct entry into a digital computer.

It is generally recognized that these deficiencies stem directly from the frequency modulation (FM) format used in almost all of these recorders. Accuracy in this format is directly dependent on the precision with which tape speed is maintained during recording and playback. This dependency has led to large, heavy, sophisticated, precision tape-handling mechanisms which are difficult to operate and maintain. Moreover, the accuracies obtained are not commensurate with costs.

Magnetic tape is a proven means for high-speed data entry into a digital computer, but the FM format prohibits direct data entry because demodulation and analog-to-digital conversion are required as intermediate operations. However, the Center has long recognized the advantages of introducing field-recorded data directly into a digital computer for reduction. Modern digital computers handle data at fantastically high rates, thereby providing tremendous potential for achieving economics through computer reduction and analysis of experimental data.

In 1966 a new format called differential pulse width modulation (DPWM) was devised at the Center. The DPWM format was specifically devised for machine handling. Utilization of DPWM with the magnetic tape recorder has many apparent intrinsic advantages; these include high accuracy with unsophisticated tape-handling mechanisms and direct data entry into digital computers without intermediate transcription. The DPWM concept was investigated on a small scale by using an inexpensive tape transport with new electronics. The significant results obtained were very rewarding, and the value of DPWM tapes for direct access to digital computers was confirmed. Moreover a substantial improvement in precision and accuracy over competing techniques was obtained. It was further demonstrated that the expensive mechanisms required for precision tape speed control are not required for accurate data recording. The potential for tremendous reductions in tape equipment costs was therefore established. It is estimated that utilization of the DPWM techniques can eventually reduce the Navy's capital investment in tape recording and reproducing equipment by two-thirds in addition to reducing data-handling and equipment maintenance costs and improving accuracy.

This IED program was completed in October 1971. Three prototype recorders have been constructed. The last, "Model 300," has a 2500 Hz bandwidth and can record six channels simultaneously on 1/4-in.-wide tape. A tape speed of 60 ips provides a bandwidth of 2500 Hz. This is considered sufficient for most Navy work. Performance evaluation data from Model 300 reported herein were reduced by a CDC 6700; they clearly demonstrate the superiority of the DMTR over commercial recorders in almost every respect. This document is the final report for this IED program. It can also mark the beginning of a significant reduction in the cost of gathering and reducing data, a decrease in data-handling time, and an improvement in the performance of data-handling equipment.

## HISTORY OF THE DEVELOPMENT OF THE NSRDC DIGITAL MAGNETIC TAPE RECORDER (DMTR)

### INITIAL OBJECTIVES

The initial objectives were:

1. To develop a quality instrumentation magnetic tape recorder which utilizes a new recording technique: differentiation pulse width modulation.
2. To demonstrate the anticipated advantages of low cost, high accuracy, light weight, and ease of operation.
3. To provide the recorder with analog outputs for field or laboratory use and with digital outputs for reproducing analog data directly into a high-speed digital computer.
4. To use this recorder to investigate the entire data recording and retrieval process and to utilize the computer-reduced data to evaluate overall system performance.

## **DIFFERENTIAL PULSE WIDTH MODULATION (DPWM) CONCEPT**

Originally the DPWM concept was devised as a method for overcoming certain problems which plagued other modulation formats. The most glaring of these problems was the demodulation errors that were produced in the low-pass filter of an FM demodulator. These errors are now easily avoidable due to the equally distributed energy centers of the DPWM signal, and it is felt that the concept represents a truly significant advance in modulation techniques. It was soon rationalized that application of the DPWM format to magnetic tape recording could produce revolutionary results. The true value of DPWM had thus been discovered.

## **DPWM AND MAGNETIC TAPE RECORDING**

At this stage, an IED program for the functional evaluation of this new concept was proposed and approved. An inexpensive (\$230) audio tape recorder was purchased and modified for this feasibility evaluation; only one channel of intelligence was provided. Even though the construction was crude and the bandwidth was limited to 10 Hz, this recorder demonstrated its capabilities well by recording and reproducing data to an accuracy of  $\pm 2$  percent. This first recorder further demonstrated that through the principles of phase-locked-loops (PLL's), reproduced data could be retrieved in a form suitable for direct entry into a high-speed digital computer.

## **ANALOG AND DIGITAL UTILIZATION**

During this feasibility evaluation period, several schemes were devised to retrieve the reproduced data in analog form. Excellent results were achieved. It was found that unlike the FM format, DPWM could be demodulated accurately regardless of tape speed, wow, or flutter. Thus, there existed schemes for data reproduction in either analog or digital form.

Once the feasibility of the major concepts involved had been properly demonstrated, the program proceeded into the prototype development phase.

## **THE 300 HZ MODEL DMTR**

The same inexpensive recorder that had been used in the feasibility study was again modified in order to demonstrate the great utility and superior capabilities of the DMTR. Special, yet inexpensive, multitrack heads were installed and the tape speed was doubled. This prototype provided six simultaneous data channels and the unmodulated reference track needed for digital playback. This reference track could be converted to a data track if no digital outputs were desired. Each data channel had a bandwidth of 300 Hz (-3 dB point); 45 minutes of continuous data could be collected on a reel of 1/4-in.-wide audio tape costing \$2.40.

No PLL was built for this first prototype. The PLL was used only to retrieve data in digital form. Since this capability was demonstrated previously in the feasibility model, and because a second prototype was planned, this omission was made.

## THE MODEL 300 PROTOTYPE

A second prototype was then built to demonstrate full performance capabilities. Designated Model 300, this prototype provides a bandwidth of 2500 Hz which is believed to be sufficient for most Navy applications. Six channels provide simultaneous digital data when used with the reference channel and PLL (both also provided). A switchable analog demodulator can be used to look at any data channel during either recording or playback. This final prototype was thoroughly characterized by using both the analog demodulator and a CDC 6700 digital computer. The evaluation showed that the initial premises and objectives had been realized. It is estimated that Model 300 can be duplicated in limited production for around \$5000 each (1971 dollars). Large quantity production can greatly reduce the price per unit. A detailed description of Model 300 and its subsystems is given later in this report, followed by an evaluation of its performance.

Because of the high promise demonstrated by the tape recorder, application has been made for a patent on its operation (Navy Case 50,526 filed 20 April 1971).

## DIFFERENTIAL PULSE WIDTH MODULATION (DPWM)

Because of the lack of low-frequency response of the magnetic tape recording process itself, some form of modulation scheme must be employed in any tape recording system where d.c. or quasi-steady-state response is required. Techniques employed to overcome this drawback usually fall into one of two groups. One encompasses the analog-to-digital conversion, digital-record schemes such as pulse code modulation (PCM). For instance, analog data are sampled, converted into a series of binary coded decimal (BCD) representations, and recorded on the tape. However, the PCM signal alone may not satisfy the requirements for recording directly. In such cases, the PCM is used to modulate a carrier in some fashion such as amplitude modulation (AM) or frequency modulation (FM).

If this is the case, the resultant signal falls into the second group. It includes carrier modulation techniques such as AM, FM, pulse amplitude modulation (PAM), pulse width modulation (PWM), phase modulation (PM) and pulse position modulation (PPM). Differential pulse width modulation (DPWM) and differential pulse position modulation (DPPM) are each special cases of PWM and PPM, respectively. They are interrelated in that DPPM represents the first derivative of DPWM. Both forms appear in the DMTR circuitry.

## DPWM ARCHITECTURE

Figure 1 shows a DPWM signal; note the unmodulated signal (carrier) indicated in Figure 1a. The carrier is of square waveform with constant amplitude equal to  $A$  and constant period equal to  $1/f_c$ . The frequency  $f_c$  is also referred to as the sampling rate since each cycle also represents a datum sample. The unmodulated carrier has a duty cycle of one-half or a 50 percent symmetry, i.e., the signal is positive one-half of the time. Figures 1b and 1c show that modulation upsets this symmetry. Positive modulation causes a symmetry greater than 50 percent and negative modulation a symmetry less than 50 percent. Note that the amplitude, frequency, and phase of the DPWM signal remain unchanged; only the symmetry is affected.\*

Figure 2 demonstrates how information is stored in a DPWM signal. One cycle is shown with its positive transition referenced to  $t = 0$ . The dashed lines represent arbitrary modulation limits. These limits correspond to 25 and 75 percent symmetries. The modulating signal, designated  $v_m$  causes the switchover time  $t_s$  to change according to the equation  $t_s = \pi(1 + v_m/2)$ . Thus for  $v_m = -1$ , the switchover time will be  $\pi/2$ , and similarly for  $v_m = 1$   $t_s = 3\pi/2$ . If the data conform to the restrictions of modern modulation practices, then no data frequency will be greater than 20 percent of the carrier frequency. Thus the DPWM signal can be separated with a low-pass filter, and the carrier fundamental and its higher harmonics eliminated. This amounts to setting all terms of the Fourier series expansion of the DPWM signal to zero except for the constant  $a_o$ , the d-c term. This term represents the stored data and can be computed from the equation for the Fourier coefficients as follows:

$$a_o = \frac{1}{2\pi} \int_0^{2\pi} f(t) dt \quad f(t) = \begin{cases} 2, & 0 < t < t_s \\ -2, & t_s < t < 2\pi \end{cases} \quad [1]$$

$$t_s = \pi \left(1 + \frac{v_m}{2}\right)$$

$$a_o = \frac{1}{2\pi} \int_0^{t_s} (2) dt + \frac{1}{2\pi} \int_{t_s}^{2\pi} (-2) dt$$

$$= \frac{2}{2\pi} \left[ t \right]_0^{\pi \left(1 + \frac{v_m}{2}\right)} + \frac{2}{2\pi} \left[ -t \right]_{\pi \left(1 + \frac{v_m}{2}\right)}^{2\pi}$$

$$= \frac{1}{\pi} \left[ \pi \left(1 + \frac{v_m}{2}\right) \right] - \frac{1}{\pi} \left[ 2\pi - \pi \left(1 + \frac{v_m}{2}\right) \right]$$

$$a_o = v_m \quad [3]$$

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\* As will be seen later, this is so only for true d-c modulating signals. For varying modulating waveforms (sine waves, triangular waves, etc.), the frequency is also somewhat changed in accordance with the dynamic properties of the modulating signal.

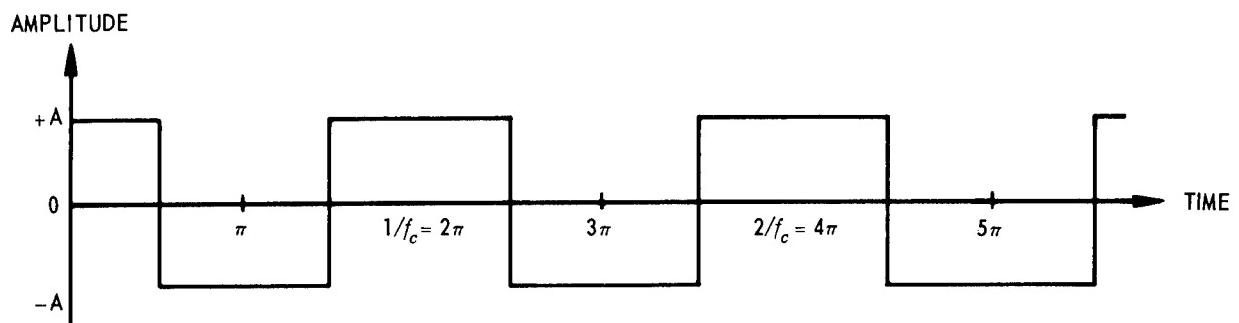


Figure 1a -- Unmodulated

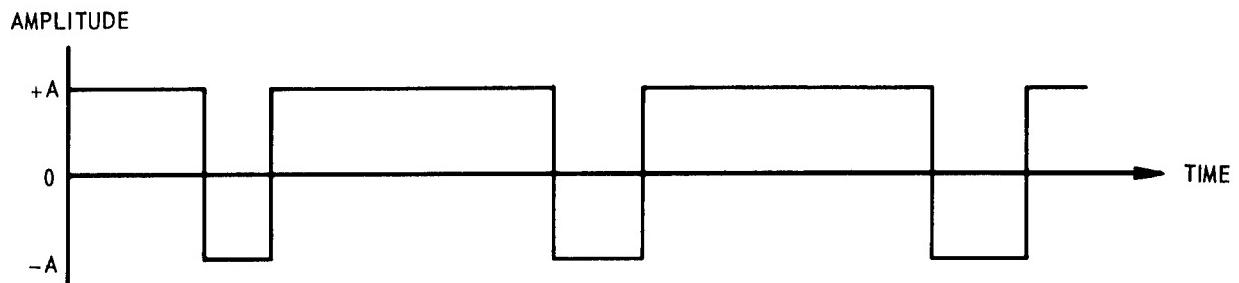


Figure 1b -- Positively Modulated

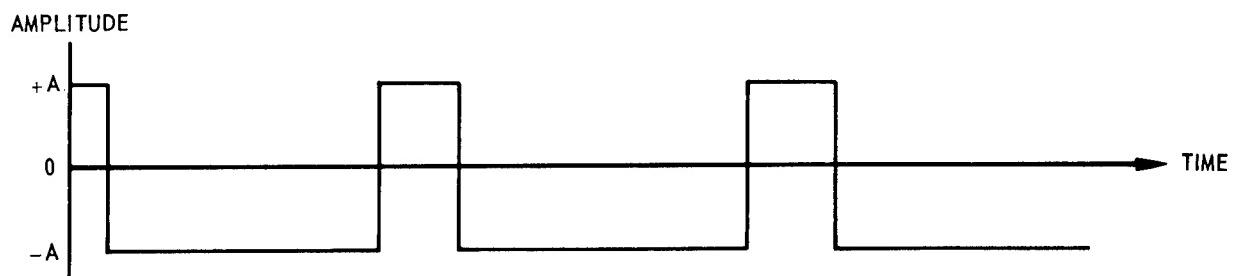


Figure 1c -- Negatively Modulated

Figure 1 -- Differential Pulse Width Modulation

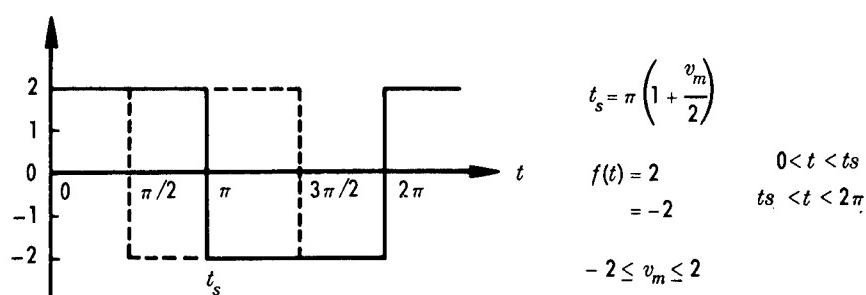


Figure 2 -- Typical Data Storage in Differential Pulse Width Modulation

Thus the data value (the value of  $v_m$ ) is contained in the average value  $a_o$  of the DPWM signal and can be extracted with a low-pass filter. The amplitude of  $\pm 2$  was chosen as a convenience to force the coefficient of  $v_m$  to equal 1. Changing this amplitude is obviously a convenient method of scaling the demodulated signal to any desired value.

## PWM AND DPWM DISSIMILARITY

Conventional PWM and a circuit technique for generating it are displayed graphically in Figure 3. It can be seen from Figure 3d that the modulating voltage  $v_m$  is compared to a reference voltage  $v_c$  in an amplifier which has a gain on the order of 100,000. Whenever  $v_m$  becomes more positive than  $v_c$ , the amplifier output goes positive and vice versa. The zener diodes are used to clamp the output to convenient positive and negative limits. The resultant PWM is illustrated in Figures 3b and 3c. The leading edge of each pulse coincides with the return of the reference voltage to its most negative value. The reference increases in value until coincidence occurs with the data. At this point the pulse ends. The width of the pulse is representative of the data value. Note that only leading edges are periodic, i.e., occur at equal time intervals  $2\pi$ ,  $4\pi$ ,  $6\pi$ , etc. The pulses themselves are not periodic. The centers of energy of the pulses move as a function of the modulating voltage, thus generating harmonic distortion. In addition this causes intermodulation distortion of multifrequency waveforms. Similar distortions occur in FM and PM formats.

Unlike these other formats DPWM is substantially free from these distortions. DPWM replaces the reference voltage sawtooth of Figure 3 with the symmetrical triangle wave of Figure 4. Note that the periodicity of the DPWM remains constant and is fixed by the period of the triangle regardless of data amplitude. Thus no intermodulation products are generated.\* In addition the sampling time base is permanently established so that later recording and reproducing at differing time bases do not affect the established sampling properties. A precision triangular voltage wave is required. The positive and negative slopes of the waveforms must be precisely equal and the zero line must be exactly in the center. In other words, the waveform must exhibit precise symmetry of both time and amplitude so that any symmetry unbalance in the DPWM waveform will be solely the result of the data signal.

## CONSIDERATIONS OF TIME-VARYING DATA

To this point, the modulating voltage  $v_m$  has been considered to have only steady-state characteristics. That is, only d-c voltages have been considered. However, dynamic data must be accommodated in order for the technique to have any usefulness. A good rule of thumb, and one which is observed here, is that the ratio of the sampling frequency to the

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\*Again this is strictly true only for modulating signal frequencies which are much lower than the carrier frequency.

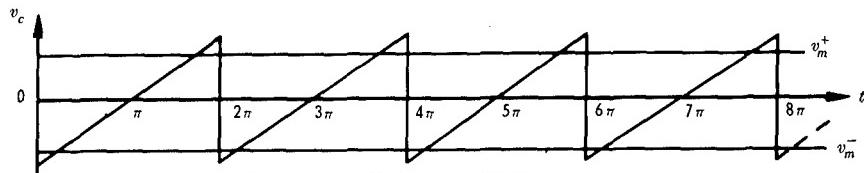


Figure 3a — Reference Voltage  $v_c$

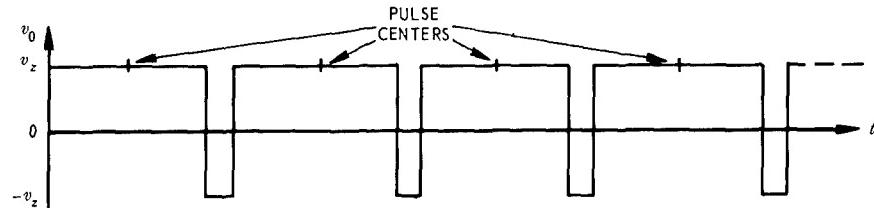


Figure 3b — Positive Modulation

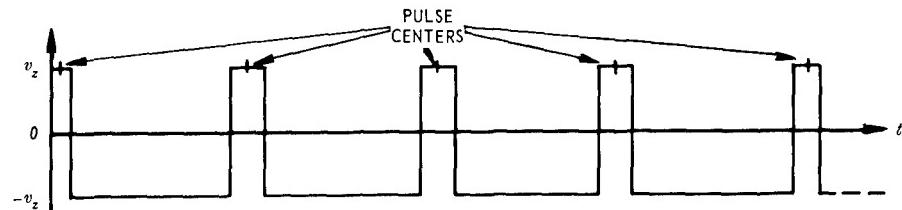


Figure 3c — Negative Modulation

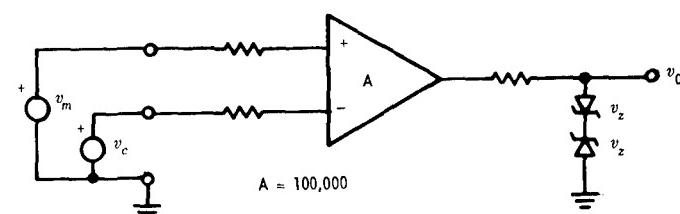


Figure 3d — Typical Circuitry for Generating Pulse Width Modulation

Figure 3 — Conventional Pulse Width Modulation

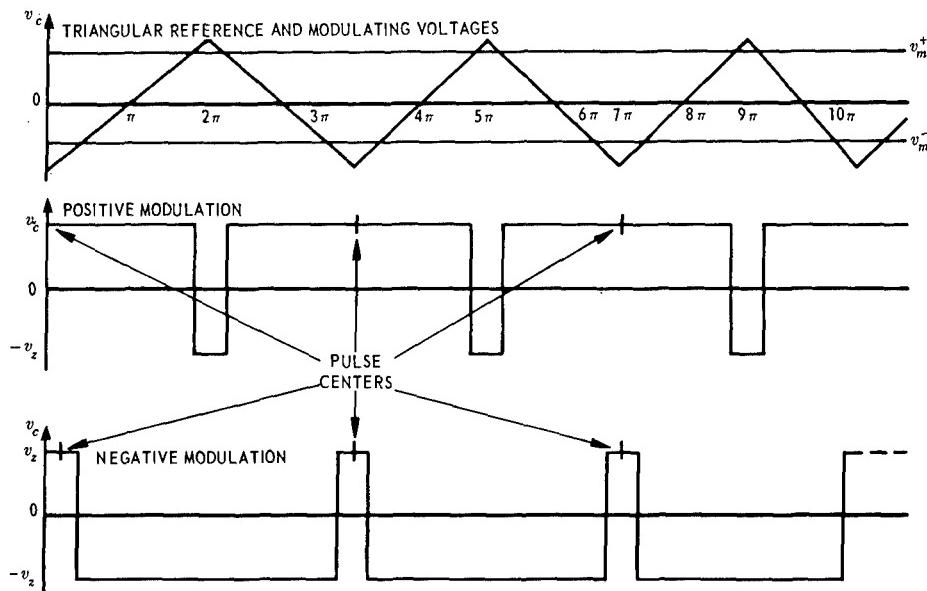


Figure 4 — Differential Pulse Width Modulation Waveforms

data frequency must be no less than 5. Thus, for data of frequencies up to 100 Hz, a triangle frequency of no less than 500 Hz should be used. This permits the use of a physically realizable low-pass filter for demodulation.

Two primary methods of demodulation are discussed later in this report. The more straightforward method is demodulation into the original analog form. If a properly designed low-pass filter is used, no significant errors are introduced as a result of the dynamic characteristics of the data. Only the ordinary amplitude and delay characteristics of the filter are introduced. Again, if the filter is properly designed, these effects are acceptable.

Digital demodulation is a more complex process and involves the use of a digital computer. This demodulation technique offers the preciseness of a time base which is fixed upon recording, a useful asset when spectral analyses are performed. The slight errors introduced during modulation of dynamic signals can be diminished mathematically to obtain the desired results.

The nature of the DPWM signal is mathematically explored in detail in Appendix A which treats both the analog and digital demodulation schemes. The errors involved in digital demodulation are explored for a ramp function input, and estimates are made for the worst case error.

## ADVANTAGES OF THE DPWM FORMAT FOR TAPE RECORDING

The use of DPWM in tape recording overcomes the effects of wow and flutter which so severely hamper formats such as FM. In FM demodulation, the recovered frequency is converted directly and proportionally into a corresponding amplitude or voltage. Tape speed variations (wow and flutter) adversely affect this recovery. The wow and flutter causes frequency changes, and is demodulated just as though it were data. Therefore, an amplitude error is incurred as well as the causative time error.

As already shown in discussing the basic concept (and treated in more detail in Appendix A), the signal recovered from a DPWM modulation scheme is independent (amplitude-wise) of the exact carrier frequency. If the demodulation circuitry is carefully designed, speed variations of  $\pm 50$  percent can be tolerated without significant degrading of signal amplitude. Although the time base information will be degraded during analog demodulation, this time error is seldom important except in the most exacting work. The amplitude accuracy given by DPWM is usually of far greater importance.

The time base change can be corrected by demodulating digitally. The DPWM signal is generated by a precision triangular wave generator. The DPWM time base is set by this triangular wave generator, and is therefore established during the modulation process. Accordingly, the time base can be easily reestablished in the computer during digital demodulation. The amplitude and time base accuracies are faithfully preserved, and there is no need for expensive and bulky tape-handling equipment for the time base control. Thus the application of the DPWM format to tape recording produces at once improved data accuracies and drastic reductions in equipment cost.

## A SYSTEM VIEW OF THE DMTR CONCEPT

A complete digital magnetic tape recording system is comprised of six main subsystems: (1) precision triangular wave generator, (2) recording circuitry, (3) tape deck, (4) reproduce circuitry, (5) analog demodulator, and (6) digital demodulator. Separate record and reproduce circuits must be provided for each channel to be recorded. In addition a reference channel must also be provided for some digital demodulation techniques. This is usually done simply by shorting the input to one data channel. Several variations in technique are possible at the demodulation stage. Subsystems (5) and (6), therefore, are really a multiplicity of different interchangeable circuits, all designed to do the same job in slightly different ways.

The general block diagram of a DMTR system depicted in Figure 5 is a typical scheme; many variations are possible. For instance separate demodulators could be provided for each channel to permit simultaneous playback of all channels.

A separate tape track must be provided for each of the  $n$  channels. In general, then, a multitrack recorder must be used whose number of tracks is at least as great as the number of channels (including the reference channel) to be recorded.

The triangular wave generator provides both amplitude and time references for the tape recorder. It is therefore imperative that this generator be designed to provide a triangular wave of high precision and stability. The data are compared to the triangular wave by the record circuitry. Maximum overall accuracy is established at this point. This fact emphasizes the need for a precision triangular wave, i.e., one which is stable in amplitude, possesses excellent linearity, has two identical slope magnitudes (different only in sign), is free of noise, and has stable frequency. The data-triangular wave comparisons yield the desired DPWM pulse train. This signal is properly conditioned and supplied to the tape deck by the record circuit.

The tape deck enables the storage of data. Record and reproduce heads are provided together with a mechanism for moving the recording tape past these heads at a relatively constant rate. The conditioned DPWM signal is applied by the record head with no bias. The recorded signal is recovered by the reproduce head in differential pulse position modulation (DPPM) form because of the differentiation attending the reproduce process. This small recovered signal is given the necessary amplification in the reproduce circuitry, where it is also reconverted into the DPWM form and sent on, ready for demodulation.

Several interchangeable options are available for demodulation circuitry. The digital demodulation technique described herein is of revolutionary importance because it permits data reduction directly by a digital computer. Other options include low-pass filters of either constant-amplitude or constant-delay types and a differential staggered incremental demodulator or similar device.

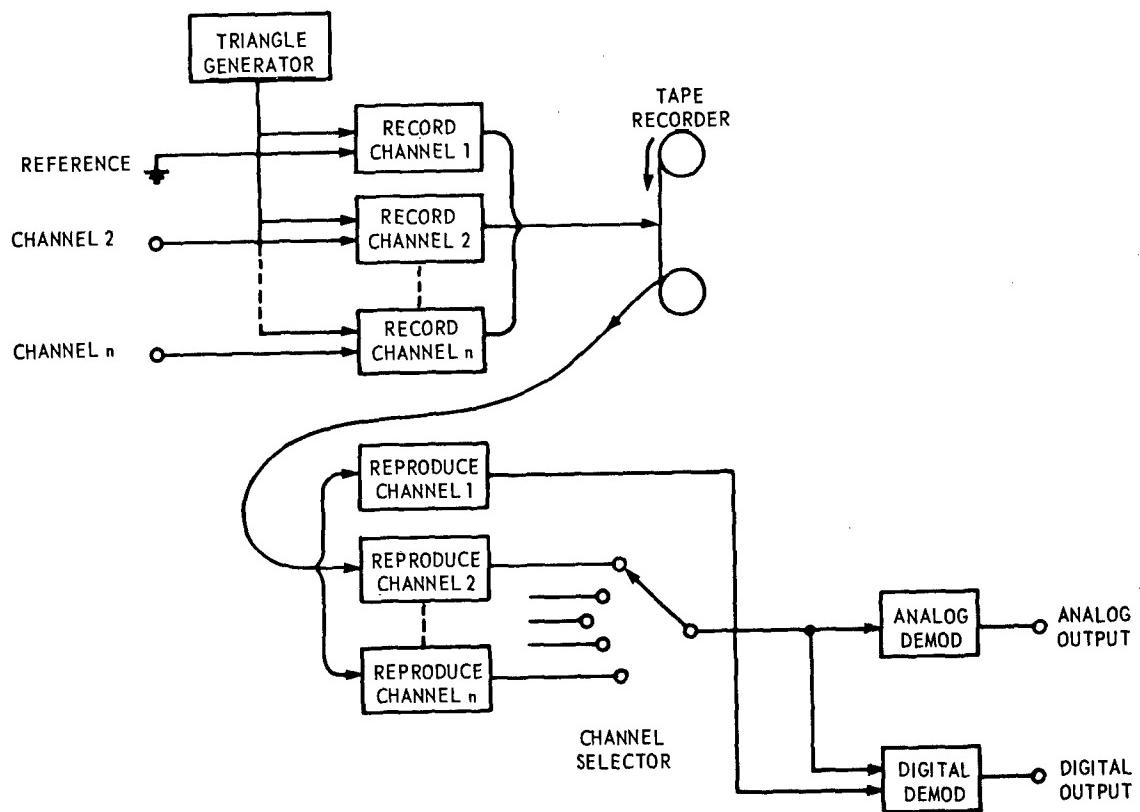


Figure 5 – Typical Digital Magnetic Tape Recorder Scheme

## DATA ACQUISITION AND REPRODUCTION FUNCTION

The typical data acquisition and reproduction function on the DMTR consists of three processes: the recording process, the reproduce process, and the demodulation process.

### Recording Process

The recording process proceeds from the ingestion of the data signal to the application of the data to the tape in DPWM form. The triangular wave generator and the record and tape deck subsystems are employed. A simplified schematic is shown in Figure 6.

The comparator performs the actual process of modulation. Its output assumes one of two states, high or low (best designed to be equal positive and negative voltage), depending on the instantaneous relative amplitudes of the data and triangle. The result is a DPWM signal with fundamental frequency equal to the frequency of the triangular wave. Any ranging (scaling) of the input data is easily handled by changing the relative amplitudes of the data signal and triangular wave before the comparison is made. For instance, a 10-to-1 change in the triangular wave amplitude will cause a 10-to-1 change in the sensitivity of the modulation circuit.

The DPWM signal appearing at the output of the comparator is further amplified and conditioned by the record amplifier before it is applied to the record head. Basically the desired result is to alternately saturate the magnetic tape positively and negatively in accordance with the DPWM signal. Some preemphasis is applied in order to improve the switching time between the positive and negative saturation states. This overcomes the inductance of the record head and permits the required fast switching of the record current.

Note the utter simplicity of the modulation and recording processes. The frequency-sensitive and nonlinear elements usually required for the modulation process are absent here.

The tape and head characteristics play an important part in ensuring a quality recording. High packing densities are typically encountered in recording DPWM (1000-1500 bits per inch), and good characteristics of both head and tape are essential. None of the tapes investigated in this program surpassed the 3M Company 290 audio recording tape, not even the tapes expressly made for digital work. This paradox is attributed to the fact that the nature of DPWM is pseudodigital. The symmetry changes in an analog manner. The ability to measure this symmetry is related to the resolution of the tape. Although digital tape is of good quality (i.e., has few dropouts, etc.) and its saturation levels are quite high, its resolution is not as good as with the analog tape. In digital work signals are always applied at a constant rate, say, 500 or 800 bpi. The tendency for adjacent positive and negative pulses to (slightly) erase one another is not important or even apparent. However, this effect becomes quite significant in DPWM when a positive-going edge and a negative-going edge approach each other. This effect is extremely undesirable. Only tape of first quality with a very thin, homogenous oxide coating (and therefore high resolution) is acceptable.

Another parameter which affects performance is the "slew rate" of the magnetic coating, i.e., the rate at which the particles can change from one saturation state to the other. Experience has shown that this factor is also influenced by the thickness of the oxide coating and by the type of oxide used.

Recording head quality is at least as important as tape quality. Inductance must be kept low in order to achieve high frequency performance. This means as few turns as possible in the head winding, because the record head flux density is proportional to the product of the number of turns and the head current, with few turns the high flux density required must be generated with high currents. The record head must be capable of handling these currents.

Crosstalk in the record head must be kept to a minimum because of the large flux densities which are generated. This problem has been a source of minor irritation on this program because of the initial choice of a high density head (28 tracks per inch). The situation is easily correctable for future recorders by using either a head with fewer tracks (and hence fewer data channels) or a head with better crosstalk characteristics.

### **Reproduce Process**

Reproducing the DPWM signal involves the tape deck and reproduce circuitry subsystems. The recorded signal is recovered in DPPM (differential pulse position modulation) form due to the differentiation of the DPWM signal which occurs in the reproduce head. A positive pulse is recovered for each positive-going DPWM excursion and a negative pulse for each negative-going excursion similar to that presented in Figure 7 which shows the recovered signal after amplification along with the noise which might be expected. The recovered pulse width is a function of the factors mentioned before, i.e., slew rate and resolution of the tape oxide coating, time constant of the record head, amount of pre-emphasis applied, etc. It is also a function of the time constant of the reproduce head circuits and the bandwidth of the reproduce amplifier. It is most desirable that these pulses be as narrow as possible and that they have very small rise times.

**High Gain Reproduce Amplifier.** The reproduced signal must be amplified. The bandwidth of the amplifier should be as wide as is consistent with the high amplification required. The amplified signal is sent to a comparator for reconstruction into DPWM. The comparator has some hysteresis; i.e., instead of comparing the input signal to a fixed level such as 0 V, a portion of the output is returned as positive feedback to the input and used as the threshold of comparison. The output of the comparator can assume one of two stable levels depending on the relative levels of the inputs. A positive-going pulse causes the output to change state to, say, +10 V. Part of this is fed back to the input to establish the lower comparison level as shown in Figure 7. This level must be overcome by the input in order to cause the output to change to its negative state. For instance, if one of the 10 volts at the output is fed back, the input must go below -1 V to overcome the hysteresis and cause a

state change. This is accomplished by a negative-going pulse whose the amplitude must obviously go more negative than the -1 V hysteresis level. The same effect occurs for the opposite polarity case.

To avoid false triggering, the hysteresis levels are carefully set to be less than the peak pulse level yet greater than the attendant noise. If the comparison levels are properly set, the resultant output is a reproduction of the original DPWM signal recorded.

Some noise in the form of "jitter" of the switching times is also produced. The noise arises from several sources. Some "jitter" is present in the original recorded signal because of the noise of the record circuitry, but this is very small if the circuitry has been properly designed. Noise generated by the playback amplifier is slightly more serious because of the large amplification required. However, the selection of quality amplifiers and the proper design can minimize the noise from this source also. Dirty record and playback heads and tape guides cause excess flutter and vibration of the tape which causes noise. The proper maintenance of the tape handling equipment will minimize this problem.

The greatest source of noise, however, is the tape itself. Noise is caused in two ways. Some residual noise is present even on virgin magnetic tape. This is random noise of generally low magnitude caused by the random magnetization of the particles on the unrecorded tape. The second source is the sensitivity inconsistency of the magnetic oxide surface. This coupled with the varying head-to-tape contact causes pulses of varying amplitude to be recovered, even though the record signal remains a constant. Thus a range of pulse amplitudes may be recovered. Figure 8 depicts a typical range of amplitudes which might be encountered for a particular pulse.

Notice in Figure 8 that the comparison level must be set low enough to be intersected by pulse  $p_3$  yet high enough to avoid being affected by the residual noise. The times of intersection are different for the pulses. This uncertainty in the time of intersection is the cause of "jitter." As the comparison level is lowered, these times ( $t_1$ ,  $t_2$ , and  $t_3$ ) come closer together and the magnitude of the "jitter" decreases. Thus the content of the noise in the reproduced signal is likewise decreased. It is easily seen that the comparison level should be made as low as possible but not so low that false comparisons are made from the noise. Also a good tape with a homogeneous oxide should be utilized so that the reproduced pulses are as nearly uniform as possible.

A further deduction to be made from Figure 8 is that the narrower the pulse (and thus the shorter the rise time), the less the differences between  $t_1$ ,  $t_2$ , and  $t_3$ . Ideally, vertical leading edges are desired; physically, they are impossible to attain. This is part of the tradeoff to be made in the reproduce amplifier.

**Missed Pulses.** Suppose that pulse  $p_3$  of Figure 8 is not as large (in amplitude) as shown and that it fails to intersect the upper comparison level. If the amplitude of the negative pulse which preceded  $p_3$  was at least as large as the lower comparison level (LCL)

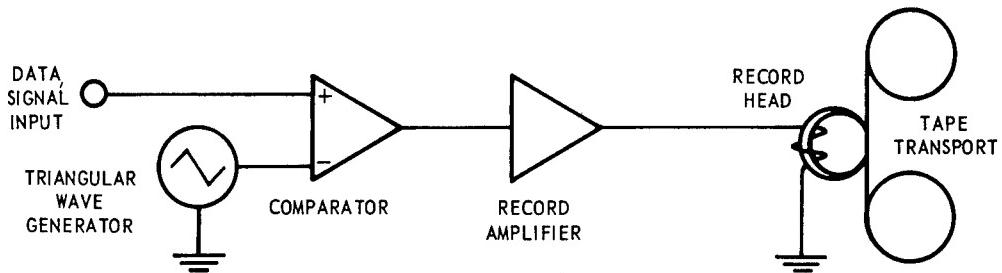


Figure 6 -- Details of Record Process

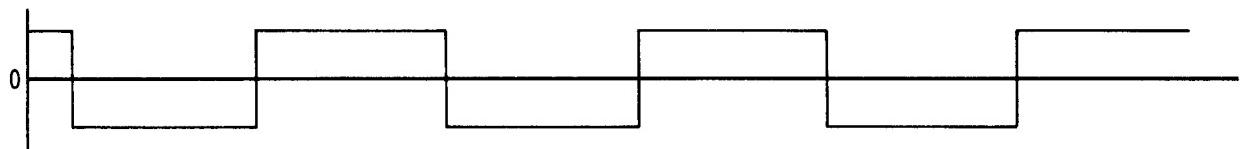


Figure 7a -- Recorded Differential Pulse Width Modulation Signal

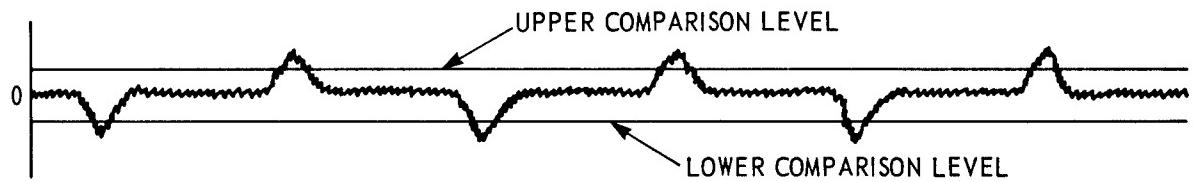


Figure 7b -- Recovered Differential Pulse Width Modulation Signal

Figure 7 -- Recorded and Reproduced Digital Magnetic Tape Recorder Signals

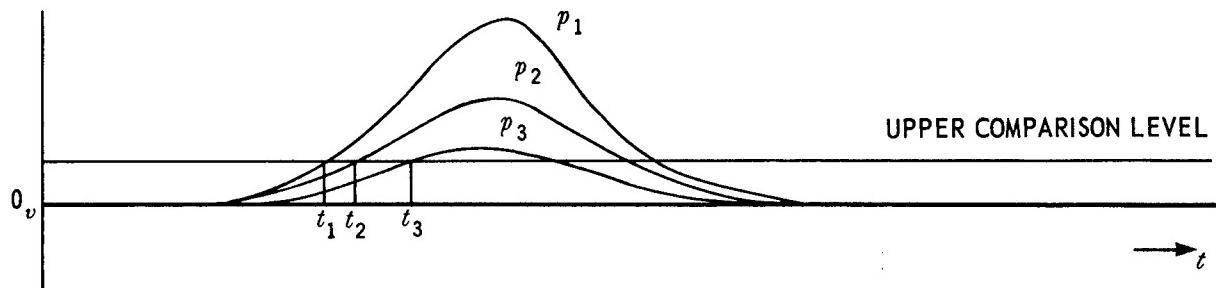


Figure 8 -- Typical Reproduced Pulse of Various Possible Amplitudes

that pulse has left the comparator output in the low state.  $p_3$  fails to change the state of the comparator. The negative pulse which follows  $p_3$  does nothing even though its amplitude is greater than the LCL because a positive pulse is required to switch the output to the high state. In effect a whole DPWM sample is missed, and the result is a DPWM pulse which is twice or more as large as it should be. Also note that pulses are *always* missed in pairs. This inherent feature prevents the phase reversal of the output that would occur if only a single pulse (or an odd number of pulses) were missed.

The superlong pulse causes different anomalies depending on how the DPWM signal is demodulated. As expected, the analog filter demodulates these into positive or negative spikes (depending on whether a negative or positive pulse was missed first) similar to the common "discriminator spikes" of the FM format. The spikes may be difficult to distinguish from data. If a type of sample and hold demodulator is used, missed pulses also create spikes. These are easier to detect as anomalies, however, since the missed pulse creates a sample which is "out of range." This is generally easier to handle than with the filter. Digital demodulation techniques create a different problem. Instead of appearing as spikes, the missed samples are "dropped" from the data. In each case of one or more missed pulses, the only sample retained is the last data value before a pulse is finally received. In this technique, the detection of missing data becomes the problem. However, this can be done by synchronizing the computer clock with the reference channel so that the absence of data can be detected and flagged. These concepts are described in greater detail later in the text (see "Digital Demodulation").

**Effect of Residual Record Head Magnetization.** The record amplifier alternately applies large positive and negative currents to the record head in tempo with the applied DPWM signal. If this current is abruptly removed from the record head, the head will be left with a residual magnetism. The direction of the magnetism will correspond to the polarity of the current at the time the signal is removed. If a recorded tape is then passed by this record head in a reproduce attempt (the tape passes by the record head before reaching the reproduce head because of tape deck geometry), the residual magnetism of the record head adversely biases the recorded tape. Tape sections magnetized in the same polarity as the residual record head magnetism are strengthened; those of opposite polarity are weakened. The net effect is a shift in the position of the polarity reversals on the tape. This shift is unpredictable and depends on the polarity and magnitude of the residual magnetism. Good data tapes can be easily destroyed if played back on a deck with a magnetized head.

Provision for avoiding this condition can be provided. Demagnetization of a head is usually accomplished by subjecting the head to a large alternating magnetic field and slowly reducing the field to zero. In this way, the magnetic materials of the head are cycled in an ever-decreasing hysteresis loop which ends at the origin of the B-H curve; i.e., no residual magnetization remains. The same technique can (and should) be employed by slowly reducing the amplitude of the DPWM signal to the head. This slow removal brings the residual magnetism to zero whereas abrupt removal leaves a large residual magnetism.

## Analog Demodulation

Three techniques have been developed for demodulating DPWM signals. Two of these yield the data in analog form, and the third yields the data in a purely digital form.

**Low-Pass Filter.** Any desired type of low-pass filter can be used to extract the data. The particular type is determined by the characteristics of the output data desired (i.e., constant amplitude, linear phase, etc.). For instance, excellent results can be obtained with a five- or six-pole Butterworth filter with a half-power frequency equal to one-fifth of the DPWM carrier frequency. With a five-pole Butterworth, the carrier is attenuated better than 60 dB, thus permitting a good signal-to-noise ratio. The low-pass analog filter is particularly attractive when good amplitude accuracy is desired. As pointed out in Appendix A, when the positive and negative levels of the DPWM signals are equal in magnitude but opposite in sign, the d-c term of the Fourier series (that term which is extracted with a low-pass filter) is zero for 50-percent symmetry. Furthermore, within the limits of the filter, the term is not a function of carrier frequency, and therefore not affected by tape speed, wow, and flutter.

**Differential Staggered Incremental Demodulator (DSID).** The differential staggered incremental demodulator (DSID) was devised to overcome the phase shift experienced with low-pass filters. The DSID consists of two sample and hold circuits, one circuit for each partial cycle of a DPWM sample (hence the term differential staggered). The width of each partial cycle is converted into a proportional amplitude (incremental), and the amplitude levels are recombined into a staircase replica of the original analog signal (demodulator). The simplified schematic of Figure 9 shows the basic DSID operation.

The DPWM signal is separated by the steering diodes  $D_1$  and  $D_2$  into its positive and negative parts so that the positive half is fed only to the positive integrator  $I^+$  and hold amplifier  $H^+$ . Similarly, the negative half DPWM pulse widths are sent only to  $I^-$  and  $H^-$ . Because of the differential nature of DPWM, each of these positive and negative pulses can be handled separately to yield independent samples. Each of these samples begins as a width, is transformed into an amplitude proportional to that width in the integrator, and sent to a hold amplifier. Positive and negative samples are then recombined arithmetically to form a representation of the original data. This recombination automatically cancels the offsets generated by each integrator. The waveforms of the circuit of Figure 9 are shown in Figure 10. The necessary control pulses which are generated by one-shots A and B are not depicted.

Briefly the circuit operation is as follows.  $D_1$  clips the DPWM signal and forms a train of positive pulses with varying widths. These pulses are integrated in  $I^+$  at a constant rate. The result is a voltage proportional to the pulse width as shown in Figure 10e. During the period just following the pulse integration when the output of the  $I^+$  integrator is constant,

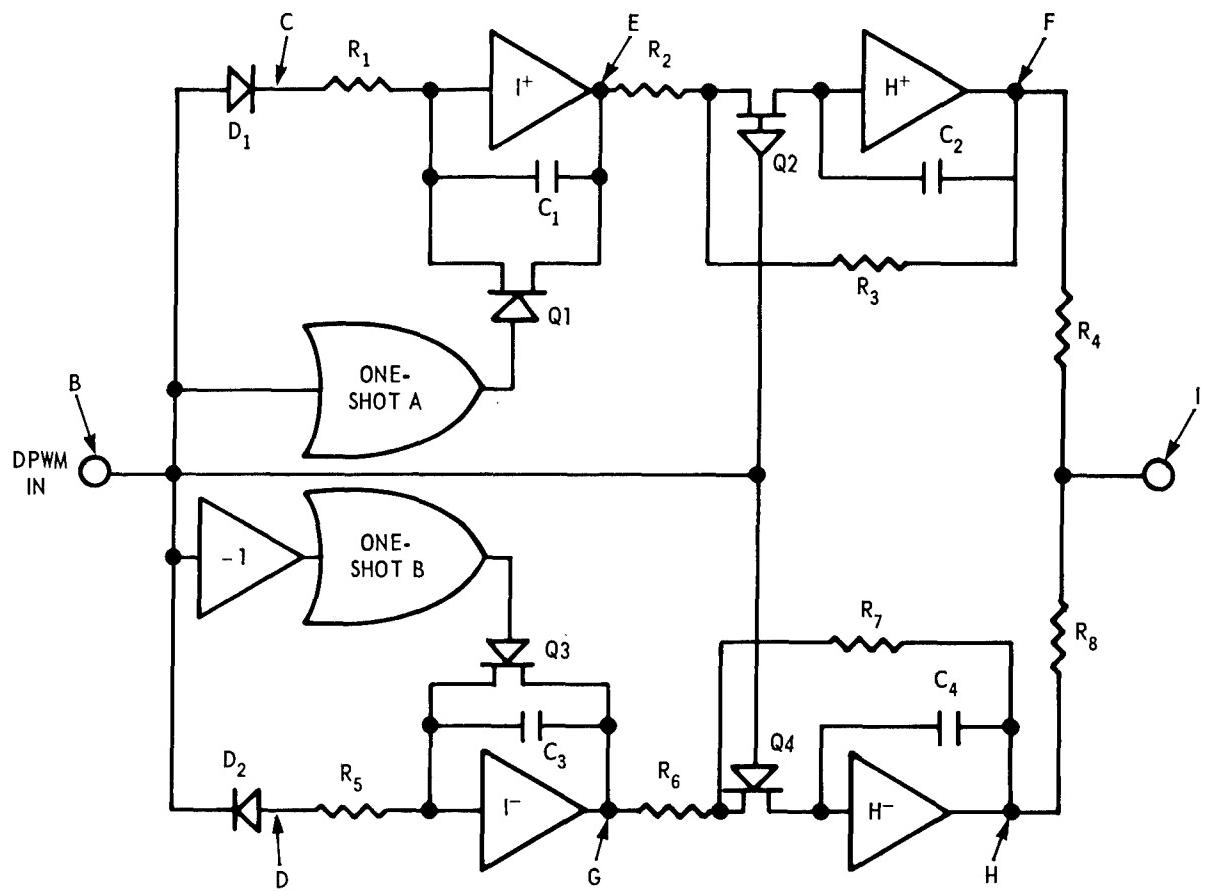


Figure 9 – Basic Operation of the Differential Staggered Incremental Demodulator

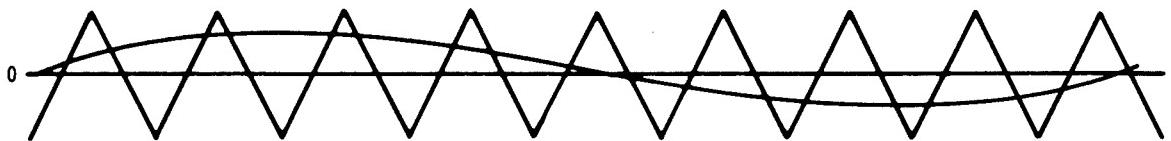


Figure 10a — Data and Triangular Modulator

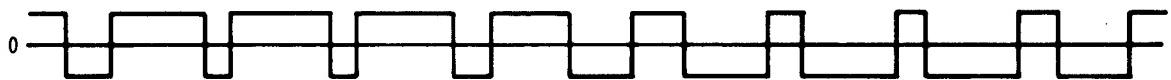


Figure 10b — DPWM Representation



Figure 10c — Positive Half-Samples



Figure 10d — Negative Half-Samples



Figure 10e — Integration of Positive Pulses (Output of Integrator  $I^+$ )



Figure 10f — Output of Positive Hold Amplifier  $H^+$

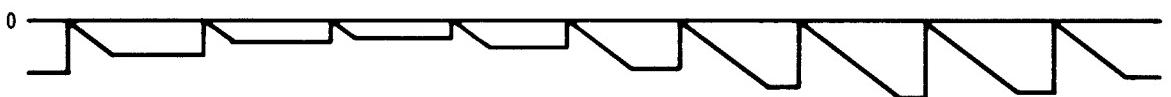


Figure 10g — Integration at Negative Pulses (Output of Integrator  $I^-$ )

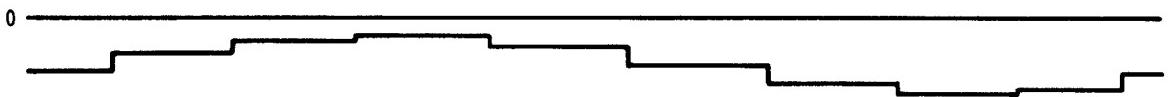


Figure 10h — Output of Negative Hold Amplifier  $H^-$



Figure 10i — Resultant Demodulated Output

Figure 10 — Waveforms of the Differential Staggered Incremental Demodulator

the hold amplifier operates as a unity gain amplifier because FET Q2 is turned on. At the end of this period Q2 is turned off, converting  $H^+$  to the hold mode. At the same time, FET Q1 is briefly turned on by a short pulse from one-shot A. This resets the integrating capacitor  $C_1$  to zero, and a new integration cycle begins on the next pulse. A similar process occurs in the other half of the DSID. Here, however, the direction of integration is reversed. This is necessary since for positively increasing data the positive pulses increase and the negative pulses decrease. The positive integrator yields increasingly positive levels, and the negative integrator yields decreasingly negative levels. The result is the same. After the summation through  $R_4$  and  $R_8$  the offsets are cancelled and the result shown in Figure 10 is obtained.

The steps can easily be removed with a low-pass filter. However, this detracts from the major advantage of the DSID. The demodulator delay is limited to a half-cycle of the carrier frequency, and a low-pass filter only adds further delay. Paradoxically, although the steps appear to detract from the waveform, they do in fact contain as much information as does the DPWM signal itself.

### Digital Demodulation

The overwhelming advantage of the Digital Magnetic Tape Recorder and the DPWM format is the capability for direct demodulation in a digital form with high precision. The two related techniques developed to produce this result will be discussed in connection with their use with the prototype recorder (Model 300). Basic to both techniques is the generation of a reference (or "sprocket") frequency which is a large multiple of the DPWM sampling frequency. This reference frequency is then used to measure the widths of the DPWM pulses by gating and counting.

**Phase-Locked Loop (PLL).** Although PLL has been known and understood for many years, the complicated and expensive circuitry involved precluded practical application until recently when integrated circuits became available for fabrication purposes. The PLL is an electronic servo system whose properties can be utilized to multiply a varying frequency by a constant. The circuitry assumes the normal servo system format, beginning with a phase comparator which compares the phase of the system input with a feedback signal which is a facsimile of the output. Any phase error is converted to a voltage and used to control a voltage-controlled oscillator (VCO) which generates the output frequency. If it is desired that this frequency be a multiple of the system input frequency, the output is divided by the multiplication factor and returned to the phase comparator as mentioned above. This closes the loop. The output frequency in such a system is a multiple of the input frequency and "tracks" it.

This principle is used to great advantage in the DMTR. One track of the tape is used to record an unmodulated DPWM carrier. When played back, the frequency of this signal changes in proportion to the combined wow and flutter effects encountered during the

recording and playback processes. The signal is then applied to the input of a PLL with, for example, a multiplication factor of 200. Provided the frequency of the wow and flutter components are significantly lower than the frequency of the DPWM carrier, the multiplied frequency at the output of the PLL also exhibits frequency modulation as a result of wow and flutter. The effect is to generate a fixed number of pulses (in this case, 200) for each DPWM cycle regardless of the record and playback speed or the induced wow and flutter.

By virtue of the fixed tape relationship between the reference track and the data tracks, the signals of reproduced data (in DPWM form) share the same wow and flutter modulation as the reference. Therefore in order to recover the stored data, it is only necessary to gate the multiplied reference ("sprocket") by the datum DPWM sample. Because the total length of a DPWM sample is equivalent to 200 "sprocket" pulses, if the gating signal is made to be the positive half of the datum signal, a pulse with 50-percent symmetry (unmodulated DPWM signal) will yield a pulse burst of 100 pulses. Positive modulation (> 50-percent symmetry) will yield more pulses, and negative modulation (< 50-percent symmetry) will yield fewer. The pulses need simply be counted, say in a computer, to determine the data values. In effect, the system is an electronic variable scale because a ratio of 200 pulses per DPWM cycle is established, wow and flutter and changes in tape speed notwithstanding! This fact permits very inexpensive tape-handling equipment to be used and thus greatly reduces recorder costs. A block diagram of the type of system discussed above is presented in Figure 11.

Notice that one element of the PLL is a filter. Because the other elements of the loop are integrated circuits their transfer functions are usually fixed. The filter is of custom design in order to produce the desired loop response. The filter must reject all of the DPWM carrier frequency yet pass the frequencies of tape flutter. As tape moves past the magnetic heads the vibration generated can often be of high enough frequency to be significantly close to the DPWM frequency. When this is the case, the necessary filter design becomes critical or even impossible. Then a compromise must be made or the PLL abandoned in favor of another technique.

$\frac{A}{A+B}$  **Ratio Correction for Flutter.** The data stored in the DPWM signal are present

as much in the negative portion of the waveform as in the positive portion. It should be obvious then, that the data can be expressed as the ratio of the positive portion to the whole. If the positive portion pulse width is called  $A$  and the negative portion pulse width  $B$ , than the desired ratio is  $\frac{A}{A+B}$ . The ratio holds perfectly if the flutter frequencies are low enough so that pulse widths  $A$  and  $B$  are equally affected. It is much easier to achieve this condition than to design the necessary filter for the PLL. To use the  $\frac{A}{A+B}$  ratio correction both pulse widths  $A$  and  $B$  must be measured. This can be achieved by simply inverting the DPWM signal and repeating the gating process to obtain the value of  $B$ . The necessary computations are then done in the computer as before. For the 200 pulse/cycle example and a DPWM symmetry of 50 percent, the computations might look as follows:

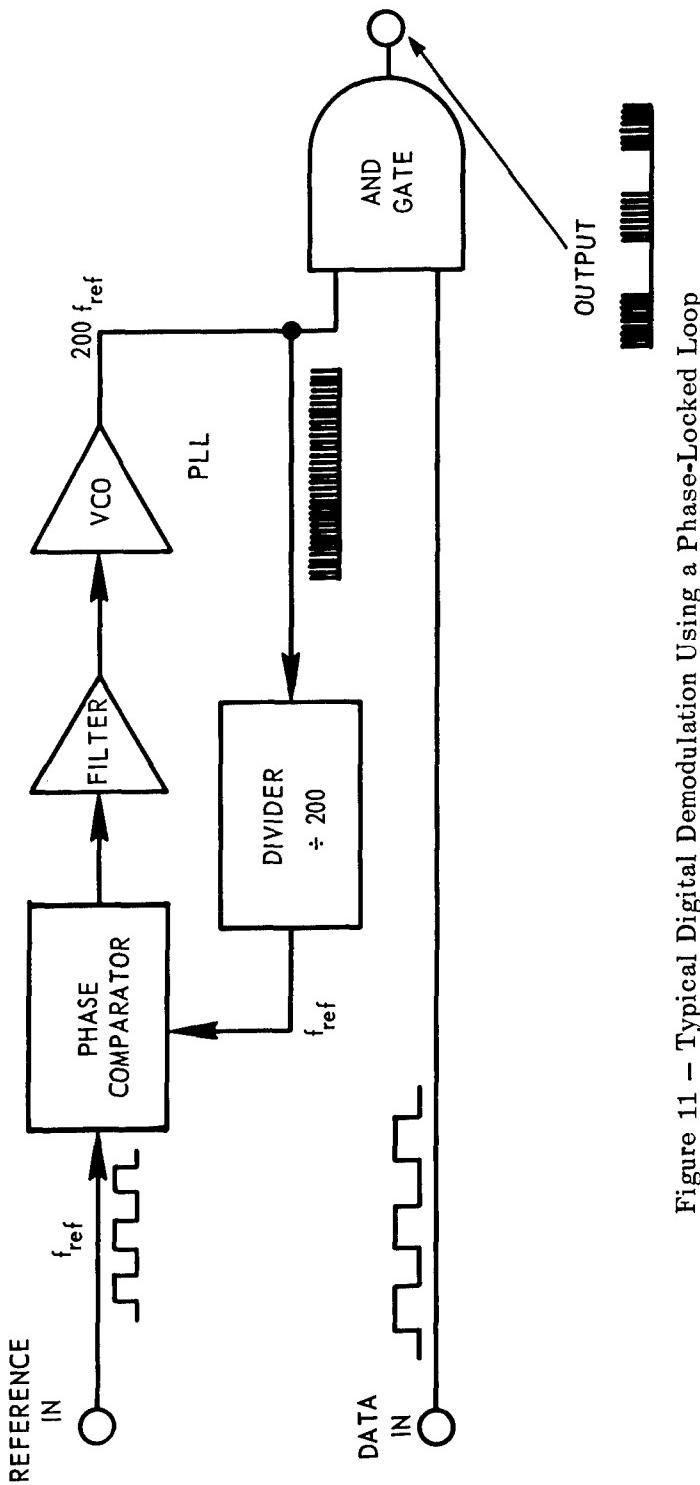


Figure 11 – Typical Digital Demodulation Using a Phase-Locked Loop

$$A = 100$$

$$B = 100$$

$$\frac{A}{A+B} = \frac{100}{100+100} = 0.5$$

Suppose now that there is a 10-percent decrease in tape speed that is not corrected by the PLL. Then

$$A = 100 + 0.10(100) = 110$$

$$B = 100 + 0.10(100) = 110$$

$$\frac{A}{A+B} = \frac{110}{110+110} = 0.5, \text{ the same result.}$$

Scaling can now be applied to return the result to the desired base of 200:

$$200 \frac{A}{A+B} = \frac{(200)(110)}{110+110} = 100$$

which is equivalent to a datum value of zero. Zero suppression can also be achieved by subtracting the residual value equivalent to zero, 100. The entire computation is now

$$x = \frac{200 A}{A+B} - 100$$

As a further example, assume a DPWM symmetry of 75 percent equivalent to a datum value of 50. Further assume an uncorrected speed increase of 15 percent between recording and playback (this is an extreme value for uncorrected tape-speed-induced error):

$$A = 150 + 0.15(150) = 127.5$$

$$B = 50 + 0.15(50) = 42.5$$

Because half a pulse cannot be counted, these values are rounded off by the counters to  $A = 127$  and  $B = 42$ . The computer computations are now:

$$x = 200 \frac{A}{A+B} - 100$$

$$= \frac{(200)(127)}{127+42} - 100 = \frac{25400}{169} - 100$$

$$= 150.3 - 100$$

$$x = 50.3$$

The residual error is much less than would be achieved directly with a 15-percent change in tape speed.

**Missing Pulses.** The easiest method of generating the numbers  $A$  and  $B$  is to electronically count the pulses in the gated pulse burst (the OUTPUT of Figure 11). But what happens when on playback a pulse is missed as discussed previously? As noted then, a single pulse can never be missed—only pulse pairs. A tremendous advantage is gained if the multiplication factor of the PLL is set to be a power to two, say 512, and the counter used to count the pulse bursts also has this capacity but no more. If a pulse pair is missed under these conditions the long DPWM cycle which results has minimal effect because after 512 counts, the counter automatically resets to zero (the counter overflows out of capacity). For instance, suppose a negative pulse is missed; as a result, the next positive pulse is ignored. Thus the value for  $A$  is very large, so large in fact that it is the summation of  $A + B$  of the missed cycle plus the  $A$  value of the next cycle, or approximately  $2A + B$ . But  $A + B = 512$  and  $A + B + 1 = 0$ . Therefore, the counter resets after the appropriate elapsed time for  $A + B$  of the missed cycle. The counter continues counting the next  $A$  value which it counts as  $2A + B - (A + B + 1) = A - 1$ . The result is off by only one count in 512 and the sample missed is completely ignored! Of course the result can be extended to more than one missed pulse pair.

**Free-Running Oscillator.** If the user is willing to double his effort and measure the values of both pulse widths  $A$  and  $B$  in order to calculate the ratio  $\frac{A}{A+B}$ , it is not always necessary to use the PLL at all. Provided the changes in tape speed are not too severe the mathematical correction by the computer is quite satisfactory. Accordingly, the sum of the number of pulses counted for  $A$  and  $B$  will be relatively constant and permit sufficient precision and resolution for the measurements to remain consistent. In this situation, the PLL can be replaced by a stable free-running oscillator. Both techniques have been investigated, and the results are documented in the section on Model 300 evaluation.

### Computer Interface Techniques

The ideal method of digitally reducing DMTR data is to have a DMTR reproduce unit as an on-line computer peripheral. For the investigative program which was conducted such elaboration was well beyond the limited scope permitted. However, such a capability should be an ultimate goal in further development programs.

There were two obvious alternative methods. First, playback of the data in analog form with subsequent analog-to-digital conversion on the SDS 910 at NSRDC could have produced the required digital format. However this technique does not utilize nor evaluate the advantages obtained from the aforementioned digital reproduction scheme, and it was rejected on that basis.

The second method was the one adopted, namely, use of an inexpensive, limited-capacity minicomputer to generate a CDC 6700-compatible input tape. This method has the advantage of machine accessibility, low operating and programming costs, and ease of program debugging. The limitations were machine capacities—both in memory size and

operating speed. The speed limitation restricted data translation to one channel at a time; the memory restricted the lengths of the data runs translated. Neither limitation adversely affected the evaluation of the DMTR because it required only short data runs on one channel. The actual transcription was made on an Interdata Model IV minicomputer with 16k of memory. This permitted approximately 4000 successive data points to be transcribed from one of the DMTR channels, and proved satisfactory for the intended evaluation.

## ADVANTAGES AND LIMITATIONS OF THE DMTR CONCEPT

The DMTR provides foremost a low-cost method of collecting data. Savings are realized in the cost of handling and reducing data as well as in the initial capital investment for equipment. This reduction can be as much as 50 percent or more compared to equivalent equipment that use FM formats. The savings are primarily related to the allowable reduction in equipment complexity because heavy tape speed control equipment is not required. This means fewer parts, fewer contacting surfaces, and less critical machining. Moreover, costly speed feedback control systems are not required. In addition the DMTR circuits are much simpler than those used in FM recorders and therefore less costly to manufacture.

A second source of savings relates to the equipment and time required to effect computer reduction of the data. The reproduced signal is already in a digital format and analog-to-digital conversion is not required. Hence savings are realized through reduced equipment usage costs, reduced personnel costs, reduced software costs, and quicker data turnaround times.

Quality performance is not sacrificed to cost. The DMTR performs to high degrees of accuracy and precision. In fact the DMTR will outperform most FM instrumentation tape recorders; the prototype has shown the capability to reproduce data with an accuracy of  $\pm 1$  percent of peak-to-peak full scale with comparable precision!

Another advantage is light weight which results from the lack of heavy precision tape speed control equipment. This, together with the achievable circuit simplicity, yields a machine which is easy to understand, easy to use, and easy to service. Further, because the recorded signals operate in the saturation regions of the oxide coating, rather inexpensive tape can be used compared to that required in FM work. Lastly, data for playback are simultaneously available in digital and analog forms.

The major limitation of the DMTR is the quantity of data which can be stored. Unlike FM formats in which sine waves can be multiplexed, and unlike pure digital formats where compaction techniques can be used, the DMTR requires the full-track bandwidth to record a channel of data. Thus the DMTR is limited to one channel per track. Only the use of high density recording heads (30 to 100 channels per inch) permits simultaneous recording of many channels. Further the DMTR must reproduce the symmetry of the recorded signal as well as the frequency. This requires about five to ten times the channel bandwidth needed to record and reproduce data in the FM format. The reproduced pulses must have as short a rise time as possible;

this requires additional bandwidth, approximately another factor of two. Thus for a given tape speed, the DMTR data bandwidth will be about 1/20 of that obtainable with FM. For example, the prototype built will reproduce 2.5 kHz data at a tape speed of 60 ips compared to the IRIG double-extended bandwidth of 40 kHz. However in view of the DMTR advantages and the low frequency content of most Navy R&D data, a 2.5 kHz bandwidth seems more than enough for most applications.

## DESCRIPTION AND EVALUATION OF MODEL 300

The NSRDC Model 300 Digital Magnetic Tape Recorder pictured in Figure 12 was developed to demonstrate and evaluate the capabilities of a tape recorder that uses DPWM principles. A Teac Model A-7030 tape deck (priced around \$750) was purchased and modified to satisfy the performance requirements for Model 300. These modifications included raising the tape speed to 60 ips, removing the audio electronics and installing the required DPWM electronics and related power supplies. In addition new heads were installed for erase, record, and reproduce. These are high-performance, fast-response digital heads selected specifically to record and reproduce the DPWM pulses. The record and reproduce heads contain seven independent tracks each, and permit the recording of seven independent channels of data. For digital playback, one of the channels was made a reference by grounding the input to that channel. A closeup of a section of DMTR tape (recordings made visible with Magne-See, a magnetic particle emulsion) is reproduced in Figure 13. Note the changing symmetry of the data signals recorded on Tracks 1, 3, 5, and 6.

## SALIENT PARAMETERS AND SPECIFICATIONS

The salient specifications, characteristics, and leading particulars are described in Table 1. These specifications are given for a tape speed of 60 rather than 30 ips although the recorder will operate at either.

Note that the input sensitivity is consistent with that available on most commercial instrumentation recorders. It permits a full-scale input of  $1 V_{rms}$  ( $\pm 1.414 V$ ), which is equivalent to a digital output count of  $\pm 141$  counts out of the possible  $\pm 256$  counts. Thus 55 percent of the range is utilized, and the resultant symmetry extends from 22.5 to 77.5 percent. This range avoids pulse cancellation on the tape when flux reversals are close together.

The accuracy specification defines the ability of the DMTR to reproduce the true input. The mean DMTR reproduced output will be within 1 percent (of peak-to-peak full scale) of the recorded input. Full scale is  $\pm 1.414 V$  or  $2.828 V$ ; 1 percent of full scale, then is approximately  $0.028 V$ . Thus for an input of  $+1V$ , for example, the mean reproduced output will be between  $+0.972$  and  $+1.028 V$ .

Precision is the specification which describes how well a measurement can be repeated. It is usually described in statistical terms. Precision is a description of the tightness of the



Figure 12 – Model 300 Digital Magnetic Tape Recorder Prototype



Figure 13 – Enlarged Section of Digital Magnetic Tape Recorder Tape and Recorded Differential Pulse Width Modulation Signals  
Notice that the reference signal is recorded on track 4. Tracks 1,2,3,5,6, and 7 contain data. The vertical lines are the polarity changes of the DPWM signal.

TABLE 1 - MODEL 300 DMTR SPECIFICATIONS

(Specifications are for a tape speed of 60 ips)

Electronics	
Input Sensitivity (full scale)	1 V <sub>rms</sub> ( $\pm 1.414$ V)
Input Impedance	10 M $\Omega$
Accuracy (amplitude-analog and digital)	$\pm 1$ percent of full scale
Precision	$\pm 1$ percent of full scale ( $3\sigma$ level of significance)
Linearity (analog and digital)	$\pm 0.5$ percent full scale
S/N Ratio	48 dB (analog)
Digital Resolution (each datum point)	0.35 percent of full scale
Bandwidth	d-c to 2.5 kHz (-3 dB at 2.5 kHz)
Number of channels (including reference)	7
Tape Deck	
Type	Modified Teac A-7030
Reel Size (maximum)	10 1/2 in. (7200 ft of 1/2-mil tape)
Size	20 7/8 x 17 1/2 x 8 1/4 in.
Weight	Approximately 40 lb
Recording Time (at 60 ips)	24 min for 7200 ft of tape
Power Requirements	115 VAC, 60 Hz
Fast Winding/Rewinding Times	400 sec for 7200 ft of tape
Tape	
Type	3M Type 290
Cost (7200 ft)	Approximately \$9.00
Size	1/4 in. wide x 7200 ft long
Number of tracks (including reference)	7

distribution of measurements about the average value; 99.7 percent of the measurements will vary from the mean by a value no greater than three times the standard deviation ( $\sigma$ ) or  $3\sigma$ . The value of  $3\sigma$ , then, defines precision; for the DMTR, this value is 1 percent of full scale or  $0.028V$ . Thus 99.7 percent of the reproduced samples will be within  $0.028V$  of the average of all of the samples, and this average will be within  $0.028V$  of the true value.

## CIRCUITS OF THE SUBSYSTEMS

The Model 300 circuits operate basically on the principles discussed previously. The schematics for Model 300 were recorded in seven NSRDC drawings that are included as Figures 14 through 20. The contents are tabulated below for quick reference.

Contents	Figure No.	NSRDC Dwg. No.
Block Diagram	14	B-851-1 Rev A
Tape Transport	15	B-851-2
Card Interconnections	16	B-851-3
Triangle Generator	17	B-851-4
Record Card	18	B-851-5 Rev B
Reproduce Card	19	B-851-6 Rev A
Five-Pole Butterworth Filter (analog demodulation)	20	B-851-7

These drawings cover only the circuitry contained physically within the recorder itself. In addition Figure 21 depicts the circuitry for the Differential Staggered Incremental Demodulator (NSRDC Drawing C-403 Rev D) which can be installed in place of the five-pole Butterworth filter if DSID-type analog demodulation is desired. The computer interface circuitry is shown separately in additional figures and will be discussed later.

### Block Diagram (Figure 14)

Figure 14 shows the capabilities of Model 300. Note that all seven channels (time referenced to one triangle generator) are simultaneously recorded and simultaneously reproduced. Recording and reproduction can be performed simultaneously (in other words, read after write); however, the reproduced data are slightly delayed because of the physical spacing of the record and reproduce heads. This spacing is approximately 1.92 in. and causes a 32-msec delay at 60 ips. Any one of the seven channels can be selected by a front panel switch and fed to the internal analog demodulator. Either the record signal or the reproduced signal can be so demodulated, and this choice is also made by a front panel switch. This capability is useful for field checking the recorded data and for setting up the tape recorder.

References given on Figure 14 direct the user to the other figures which pertain to each of the functional blocks of Figure 14.

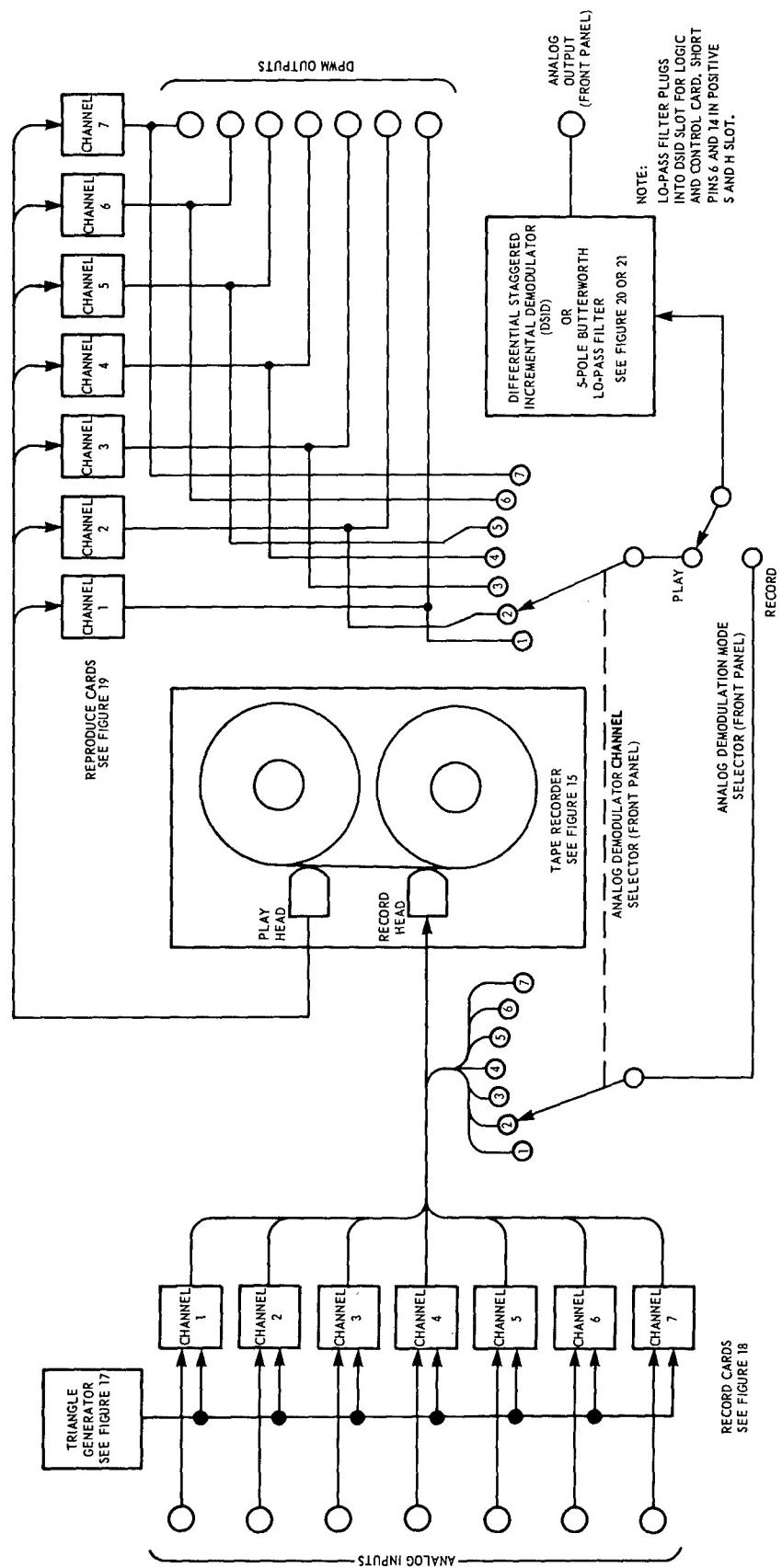


Figure 14 - Model 300 Block Diagram

### Tape Transport Schematic (Figure 15)

Figure 15 shows the internal wiring of the tape transport itself. All of the functions necessary to control the motion of the tape are included here together with a schematic of the erase oscillator. The oscillator operates at a frequency of 125 kHz and is used in the record mode to erase all tracks simultaneously before the tape passes the record head. Selective single track erasure is not provided. The erase circuit is functional in the record mode. Because of the high magnetization levels used in the DPWM recording process, it is highly desirable to preprocess tape previously recorded with DPWM signals through a bulk tape eraser. This device can generate an erase flux field whose strength far exceeds the strength that can be generated by the erase head. The bulk tape eraser will ensure the least residual magnetism in DMTR recordings and thus the greatest signal-to-noise ratio.

### Card Rack Interconnecting Diagram (Figure 16)

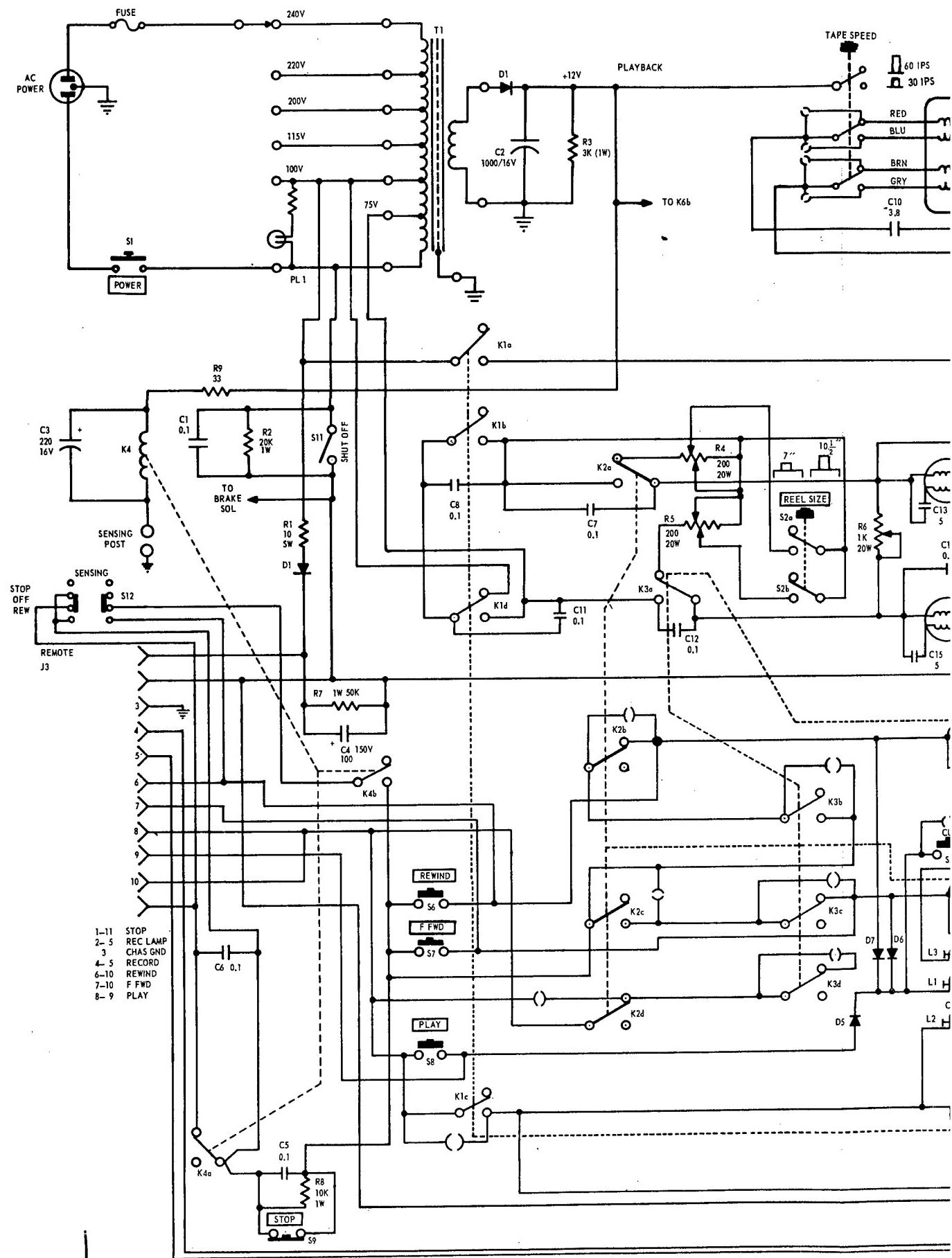
Figure 16 indicates the interconnections for the card rack, the power supply connections, and the external switches and resistors for the analog demodulator. This schematic is self-explanatory.

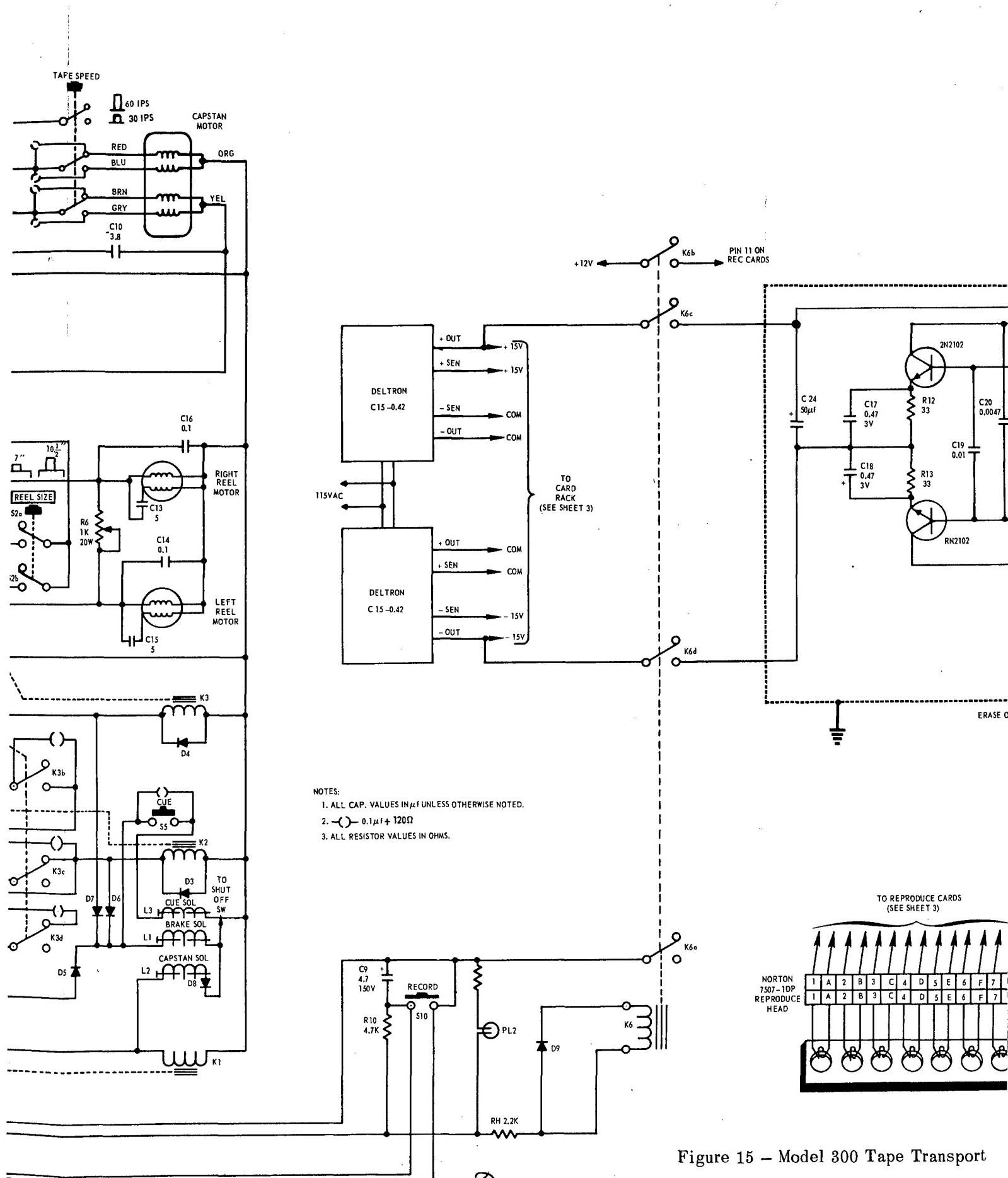
### Triangle Generator (Figure 17)

A self-regenerating triangle generator is used because of its simplicity and stability. The frequency of the triangle generator, and thus the sampling rate of Model 300, is 12.5 kHz. Note from Figure 17 that the circuit consists of only two operational amplifiers and their associated components. A1 operates as a comparator to generate a square wave whose amplitude is controlled by the closely matched zener diodes D1 and D2. This matching is required in order for the rates of positive and negative integration to be the same at integrator A2. And these rates must be the same in order to produce a triangular wave with positive and negative slopes of equal magnitude. The integrating constant of A2, and thus the triangular wave frequency, is controlled by R9.

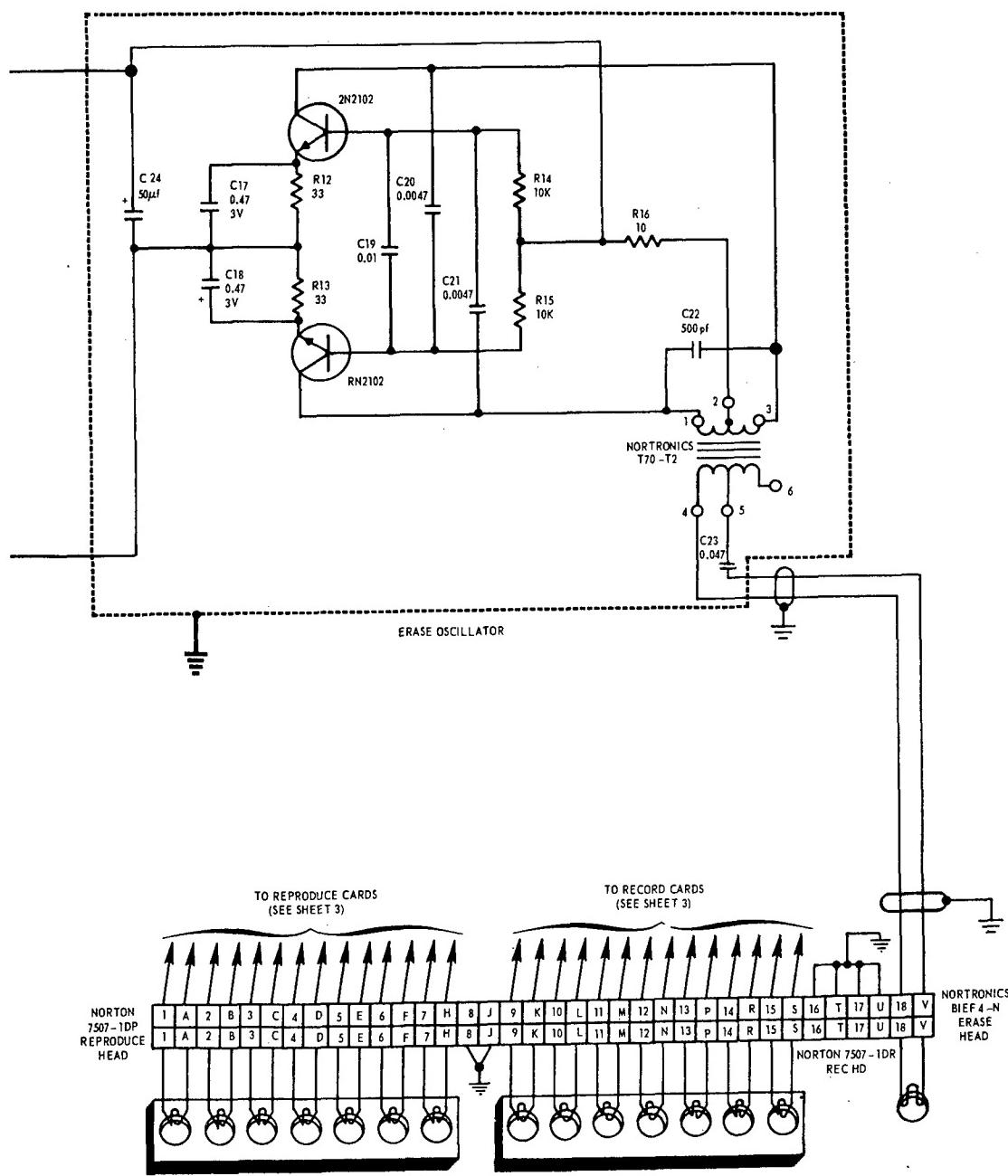
The triangle output is sampled by R4, and fed back to the comparator to complete the loop. The ratio of square wave (fed back through R6 and R7) to triangular wave feedbacks determines the amplitude of the triangle. That is, the triangle amplitude must overcome the square wave fed back at the positive input to the comparator in order to generate a comparator output change.

To illustrate, assume that the A1 output is in its positive state, say, +10 V. The output of A2 decreases in a ramp-wise fashion because it operates as an inverting integrator. If R7 is at its midpoint such that  $R6 + R7 = 13.5 \text{ k}\Omega$  by superposition the voltage at the positive input to A1 (pin 3) is 4.024 V (due to the +10 V at the output of A1 plus  $0.598 \times$  triangle output voltage). The comparator will not change state until its inputs are at the same potential. Nominally the negative input (pin 2) is at 0 V so the following equation prevails:





PIN 11 ON  
REC CARDS



ure 15 – Model 300 Tape Transport

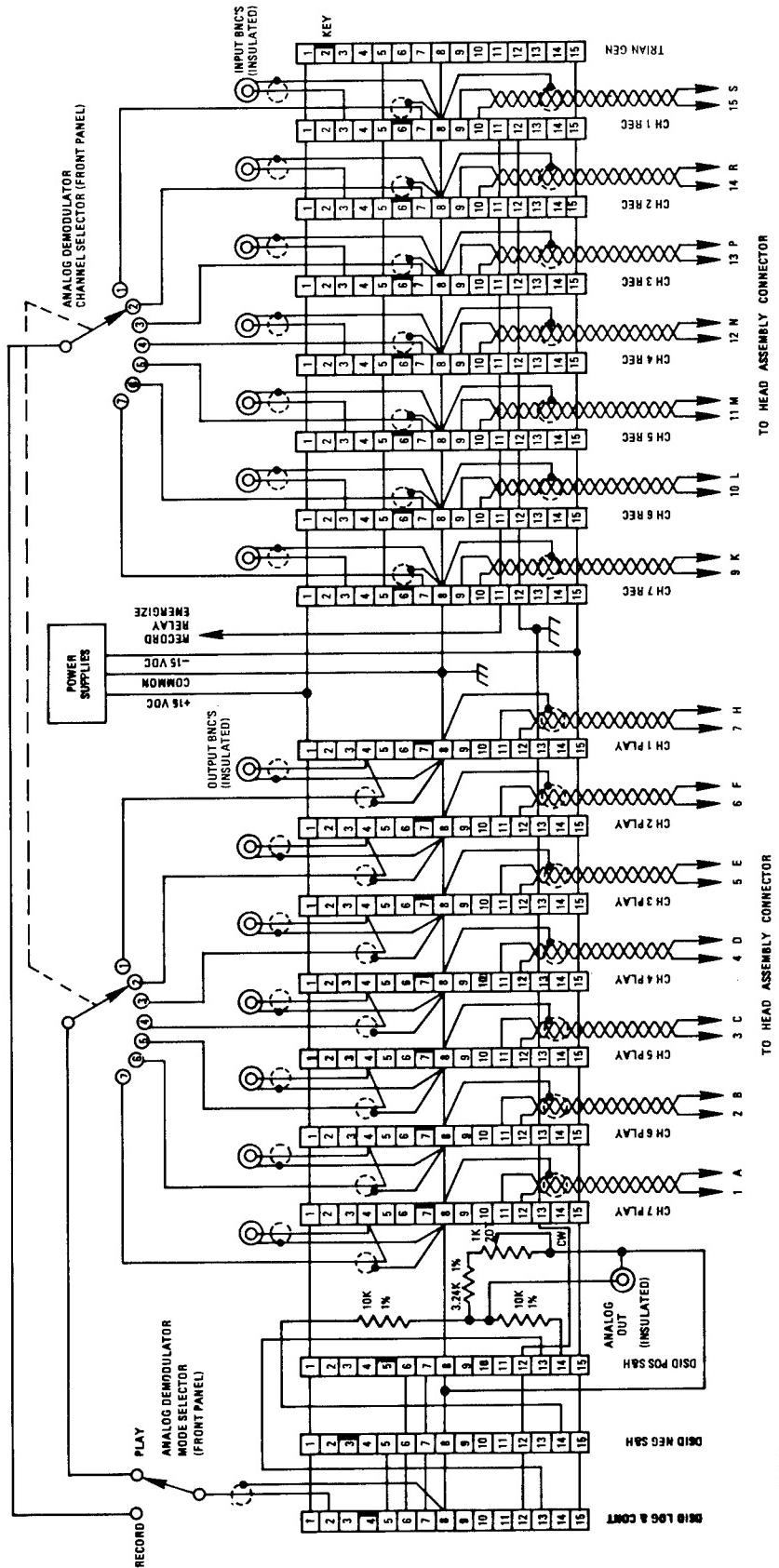


Figure 16 – Model 300 Card Rack Interconnections

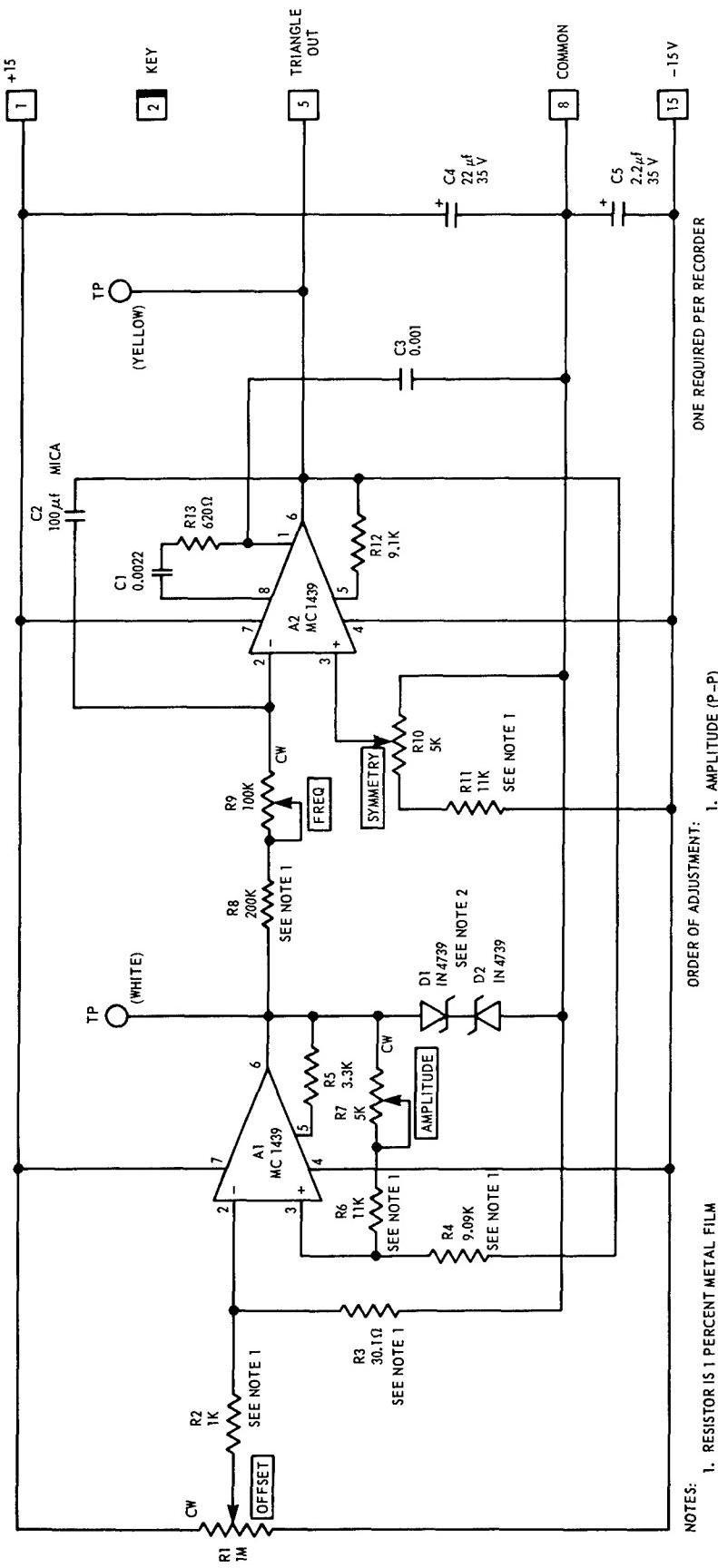


Figure 17 – Model 300 Triangle Generator

$$0.598 \times (\text{triangle output voltage}) + 4.024 V = 0$$

Solving:

$$\text{Triangle output} = -\frac{4.024}{0.598} = -6.729 V$$

Thus, when the triangular wave amplitude reaches  $-6.729 V$ , a comparison is made by A1 which reverses its output to  $-10 V$ . A2 now begins integration in the positive direction, and the process is repeated continuously.

The offset and symmetry of the triangular wave are controlled by R1 and R10, respectively. R1 adds in a small voltage to adjust the comparison levels, thus adjusting the negative and positive extremities of the triangle up and down together. Similarly R10 supplies whatever small voltage is required to equalize the positive and negative integration rates.

### Record Card (Figure 18)

The record card accepts the datum signal and the triangular wave, produces a corresponding DPWM signal, conditions the signal, and presents it to the record head. FET Q1 provides high signal input impedance. FET Q2 likewise provides large triangular wave input impedance so that the triangle generator can supply many record cards. Sensitivity and offset adjustments are also conveniently provided at this stage.

The DPWM comparisons are made by A1. The resultant signal amplitudes are clipped to the desired level by D1 and D2. This signal is sent to the record current amplifier through resistors R9 and R10. Preemphasis in the form of the DPWM derivative to generate the sharp rise and fall times in the record head is provided by C1 (padded by C5). This sum of the DPWM signal and its derivative is accepted by A2 and sent on to the record head as a current. This same current returning from the head passes through R13, generating the necessary feedback signal. When not recording, R12 ( $1 M\Omega$ ) is inserted in the feedback loop, thus reducing the gain of the amplifier. The desired record current and compensation (preemphasis) to produce the best results can be adjusted by R11 and R10, respectively.

### Reproduce Card (Figure 19)

The reproduce function in the DMTR consists of retrieving the low-amplitude differential pulse position modulation (DPPM) pulses from the tape, amplifying the pulses, and converting them back again to DPWM. C1 in parallel with the reproduce head tunes the input circuit to the best resonance for the reproduced signal. The signal is amplified by A1 which operates with a gain of 1000. The amplified pulses are conveyed to comparator A2 through C4 which blocks any d-c offset which might exist at the output of the amplifier. The comparator with adjustable hysteresis is set to trigger on the pulses and reject the residual noise level. The reconstructed DPWM appears directly at the comparator output. Because of the large signal

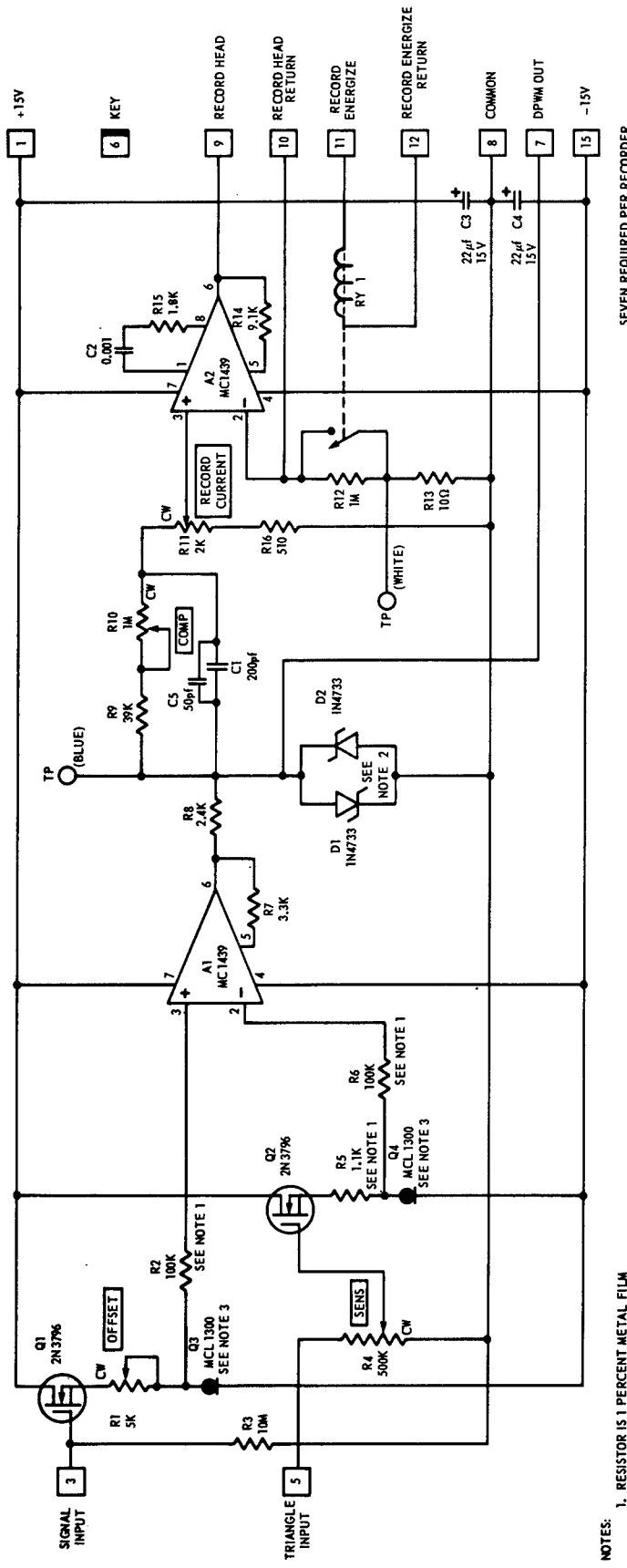
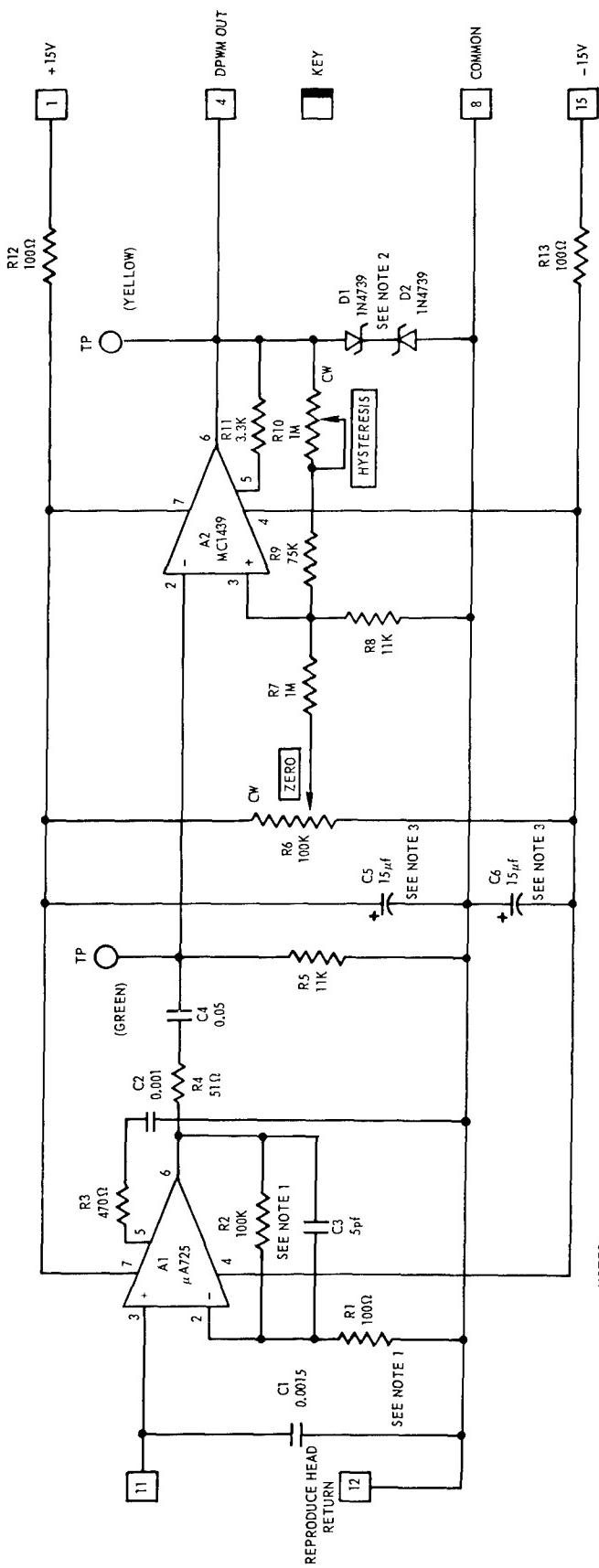


Figure 18 – Model 300 Record Card

**NOTES:**

1. RESISTOR IS 1 PERCENT METAL FILM
2. D1 AND D2 MATCHED IN FORWARD DIRECTION
3. Q3 AND Q4 MATCHED  $\pm$  20 PERCENT



#### NOTES:

1. RESISTOR IS 1 PERCENT METAL FILM
  2. D1 AND D2 MATCHED IN REVERSE DIRECTION (9.1V)
  3. LOCATE NEAR P C PINS

Figure 19 – Model 300 Reproduce Card

at A2 and the small signal at A1, isolation resistors R12 and R13 and decoupling capacitors C5 and C6 are used to minimize effects on and by the power supplies.

#### **Five-Pole Butterworth Filter Analog Demodulator (Figure 20)**

A1 of the low-pass filter is simply an operational amplifier operating open loop (gain = 100,000) with 10-V clipping diodes on the output. This permits the circuit to accept and identically demodulate DPWM signals of any amplitude. The resulting normalized DPWM signal is then sent to the two-stage, five-pole active filter consisting of A2, A3, and their associated components. Signal offset control is provided at the input of A2, and overall circuit gain control is provided at the output of A2.

#### **Differential Staggered Incremental Demodulator—DSID (Figure 21)**

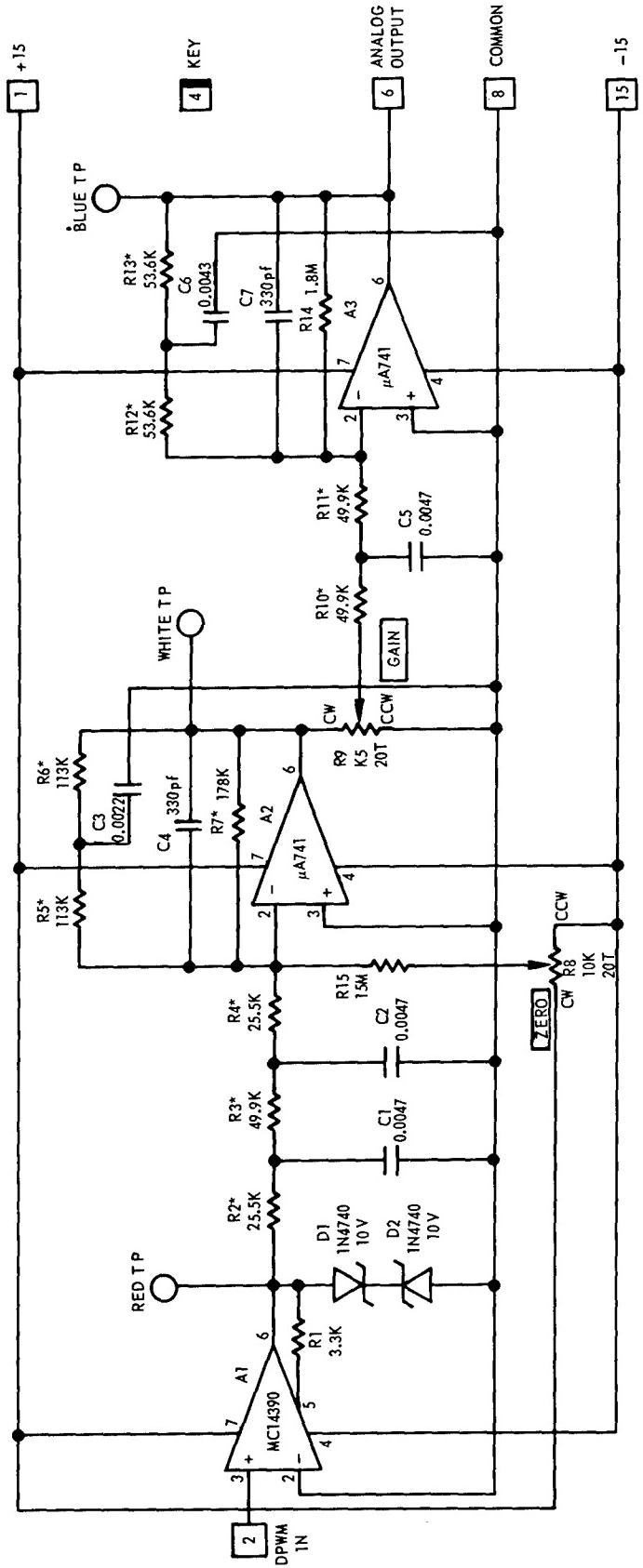
If desired, the DSID can be installed in place of the low-pass filter. As shown in Figure 21, the DSID operates similarly to the description given previously. As in the filter, a normalized DPWM signal is produced by A2, permitting signals of all amplitudes to be accepted. The complete DSID is contained on three circuit cards. The LOGIC and CONTROL card contains the signal-normalizing amplifier mentioned above, an amplifier which produces an inverted DPWM signal, and the logic modules which generate the reset pulses. The inverted DPWM is called the COMPENSATION signal because it compensates for the charge fed through to the "hold" capacitors, C9 and C18, when the FET switches PQ4 and NO4, change the hold amplifiers from the read mode to the hold mode.

The POSITIVE SAMPLE AND HOLD card performs the functions relating to the positive or "A" half of the DPWM sample. Likewise the NEGATIVE SAMPLE AND HOLD card performs those functions relating to the negative or "B" half. The resulting waveforms are depicted in Figure 10f and 10h, respectively. These signals are summed together through resistors R44 and R45 (also shown in Figure 16) and made available on the tape recorder front panel. The resultant demodulated signal is similar to that of Figure 10i.

#### **Digital Interface Electronics (Figure 22)**

The digital evaluation of Model 300 was done on a CDC 6700 digital computer. This computer requires an input tape of certain format which was generated on an Interdata Model IV minicomputer. The digital interface electronics were designed to mate the DMTR with the Model IV. The output format available from the interface can be either binary or BCD. Binary was used with Model IV. A block diagram for the entire interface is presented in Figure 22. The schematics are given on two drawings, Figures 23 and 24.

As indicated on Figure 22, the gating card is used to convert the DPWM signal to micrologic-compatible levels (TTL and DTL types) of 0-5 V. The present prototype allows for the digital conversion of only one channel at a time. The selection of one of the outputs



NOTES:

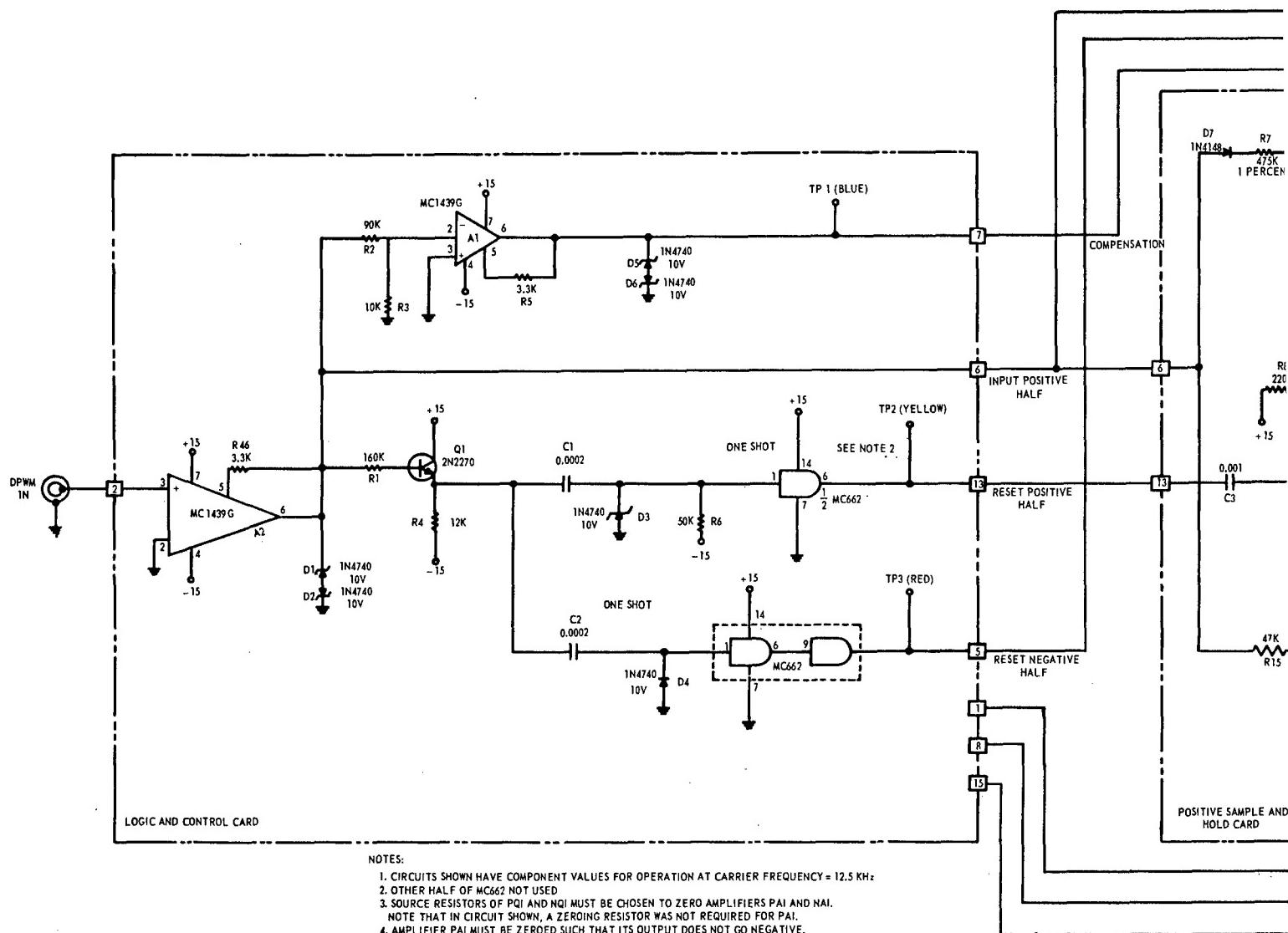
1. "\*" INDICATES 1 PERCENT METAL FILM RESISTOR.

2. THIS ANALOG DEMODULATOR IS INTENDED FOR USE IN PLACE OF THE DIFFERENTIAL STAGGERED INCREMENTAL DEMODULATOR (NSRDC DWG C-403.) THEY CANNOT BE USED SIMULTANEOUSLY. THE CARD SHOWN REPLACES THE DSD LOGIC AND CONTROL CARD; THE POSITIVE S AND H CARD IS REPLACED BY A CARD WITH PINS 6 AND 14 SHORTED TOGETHER; AND THE NEGATIVE S AND H CARD IS NOT USED.

3. DATA INTELLIGENCE BANDWIDTH IS 2500 Hz (-3dB), AND AT 12.5 kHz (THE CARRIER FREQUENCY) THE OUTPUT IS -76dB.

4. ONE REQUIRED PER RECORDER.

Figure 20 — Model 300 Low-Pass Filter Analog Demodulator



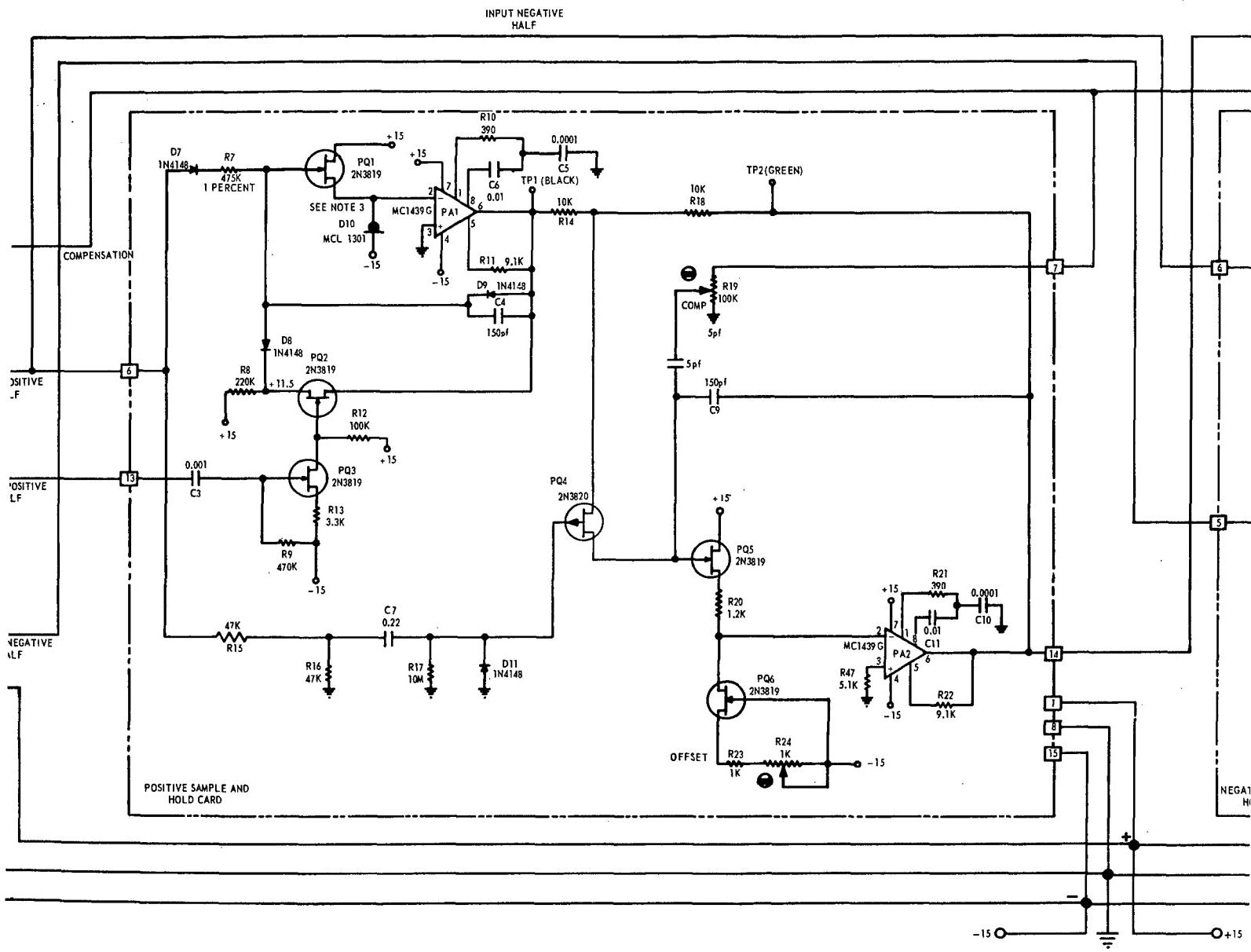
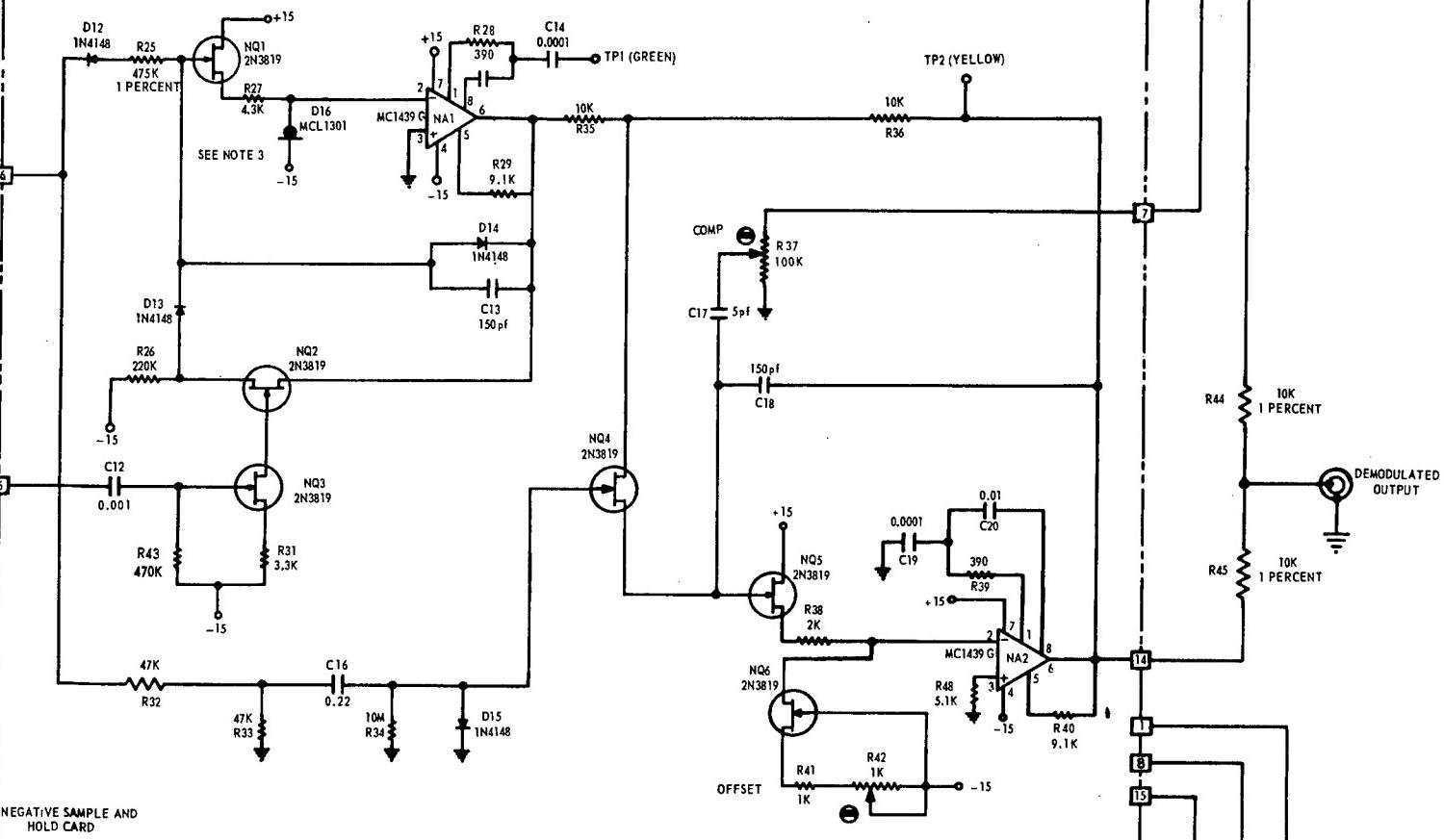


Figure 21 – Model 300 Differential Staggered Incremental Demodulator



3

+15

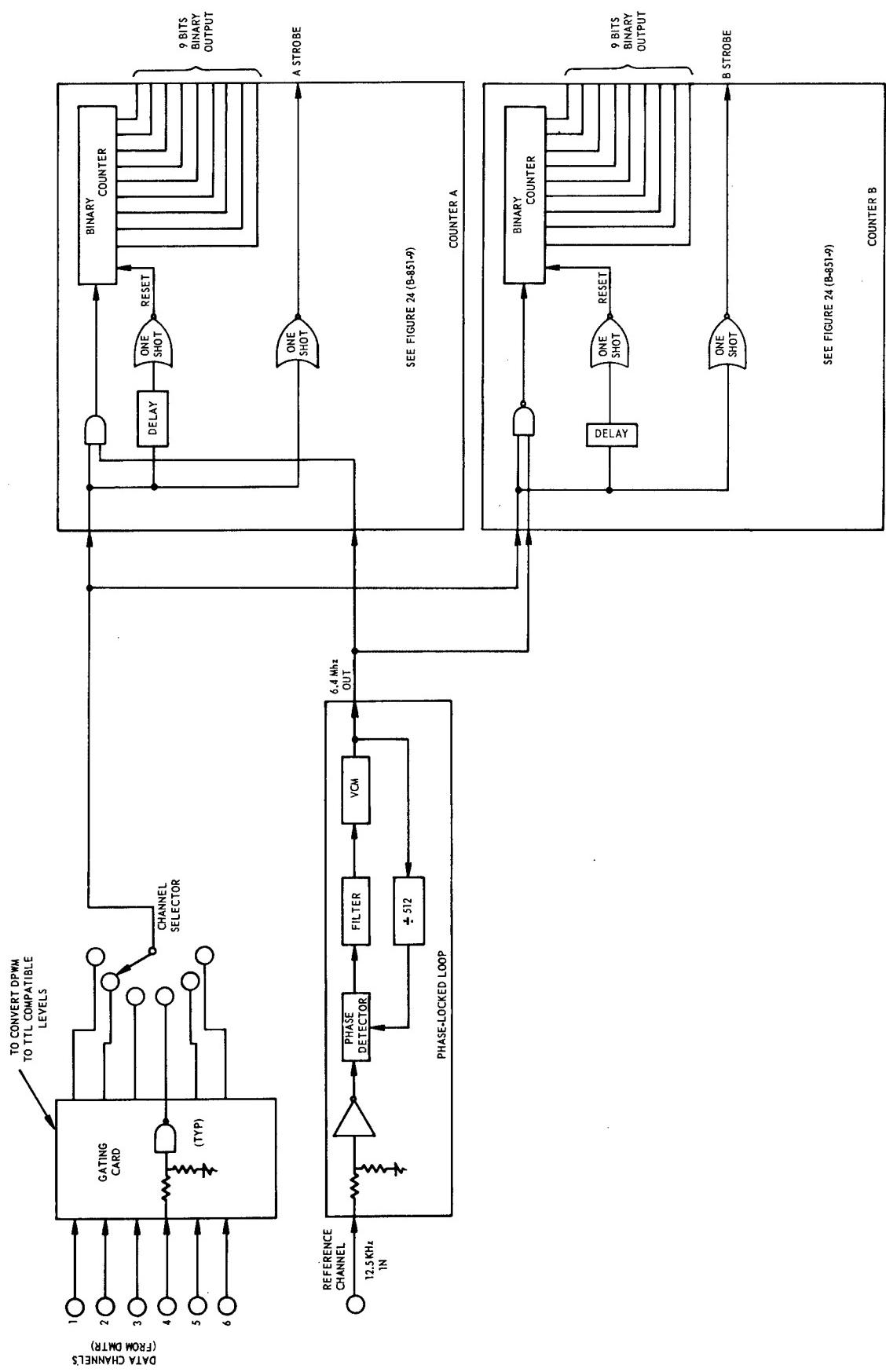


Figure 22 – Digital Interface Electronics General Arrangement

of the gating card can be made with the channel selector. Additional channel demodulation capabilities can be provided by adding counters for each desired channel. Additional PLL's are not required.

The tape track selected as the reference is plugged into the PLL input. This is also buffered by an inverter to provide TTL/DTL levels. The PLL itself is composed of microcircuits with the proper external components to provide a multiplication of 512 (the multiplication factor used with Model 300) and the required filtering. The selected data signal and the output of the PLL are both fed to each of two counter cards. The *A*, or positive, portion of a DPWM sample is used to gate the 6.4 MHz reference into counter *A*. Likewise the *B* portion is used to gate the reference into counter *B*. At the end of each gated burst, the values of *A* and *B* are available at the outputs of the respective counters in 9-bit binary form (BCD is also easily provided by merely changing from hexadecimal to decade counters). The strobe is present between the times the counter is finished counting and the reset pulse appears. The strobe can be used to tell the computer that the data are present and available for reading. *A*'s and *B*'s occur in pairs at a rate of 12,500/sec/channel.

**Gating Modules and PLL (Figure 23).** This circuitry provides the necessary buffering of the DPWM signals from the tape recorder to convert to micrologic-compatible levels. The buffering is provided by the MC 1806 quad AND gate chips.

The circuitry of Figure 23 also contains the PLL. Motorola microcircuits were used. Five chips and an external filter are required. The MC 4044 phase frequency detector receives the reference frequency (12.5 kHz) rectangular wave from the DMTR and the divided-down 6.4 MHz "sprocket" output. The phase difference between these signals is measured and converted by the filter into a d-c voltage (proportional to the phase difference) used to correct the 6.4 MHz VCO. The VCO is changed in such a manner as to eliminate the phase error. The 6.4 MHz is divided by the three MC4016 programmable counters, thus closing the loop.

**Counter for Digital Interface Electronics (Figure 24).** This counter module accepts the DPWM data rectangular wave train and the 6.4 MHz reference signal from either the PLL or a free-running oscillator. The reference is gated by the data signal in the first AND gate of the MC 1806, producing a pulse burst equivalent to the datum value. This pulse burst is counted by the three hexadecimal counters (MC 839's) and gated through the MC 3001 output AND gates. The necessary timing and control signals are generated by the one-shot circuitry made from the MC 858 NAND gates. Typical circuitry waveforms are shown at the designated circuit locations.

## MODEL 300 DMTR EVALUATION

On completion of construction of the Model 300 prototype, evaluation tests were structured to demonstrate its capabilities. Data for both analog and digital playback modes were desired. A single test tape was manufactured to evaluate the recorder. This tape

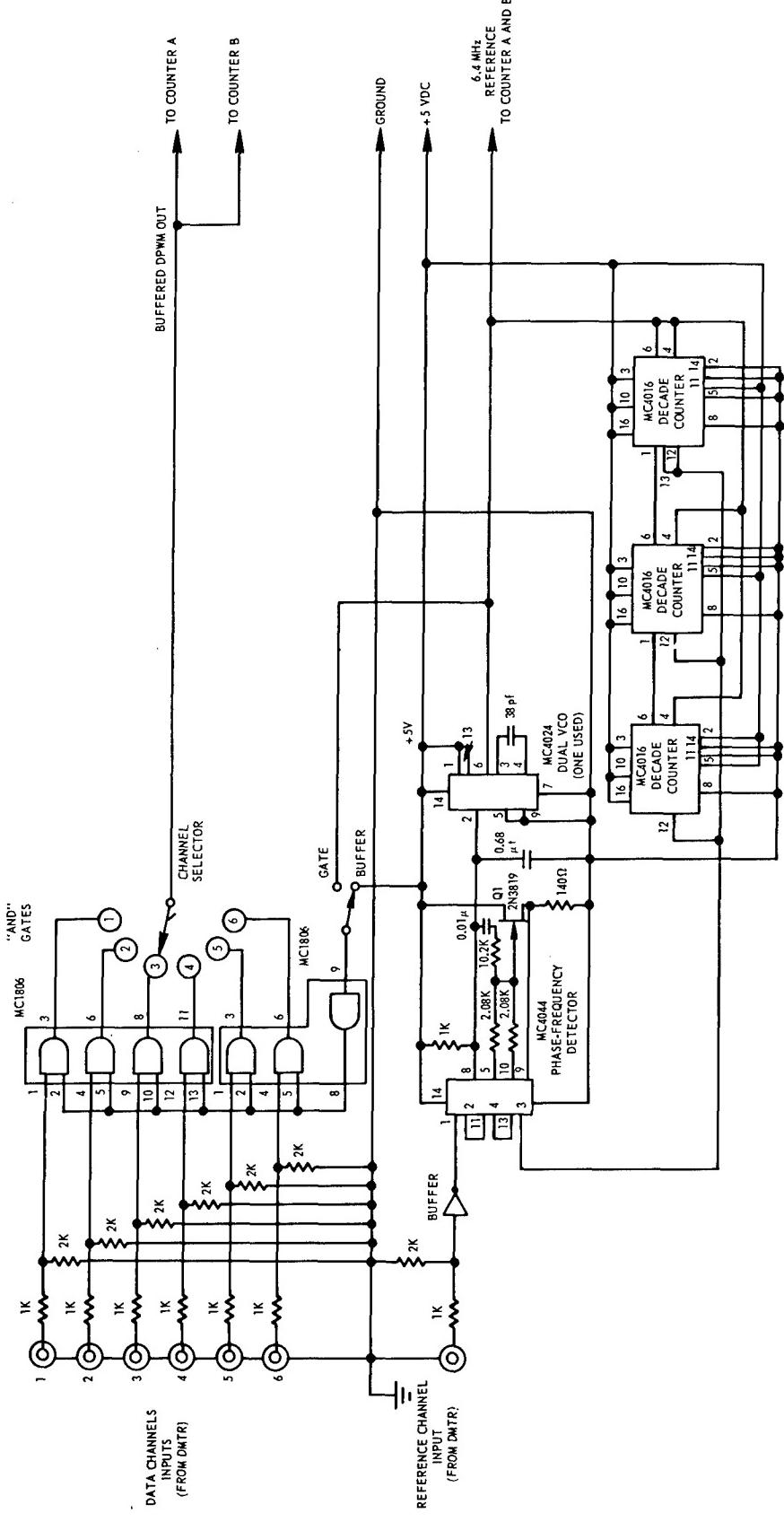


Figure 23 – Digital Interface Electronics – Gating Modules and Phase-Locked Loop

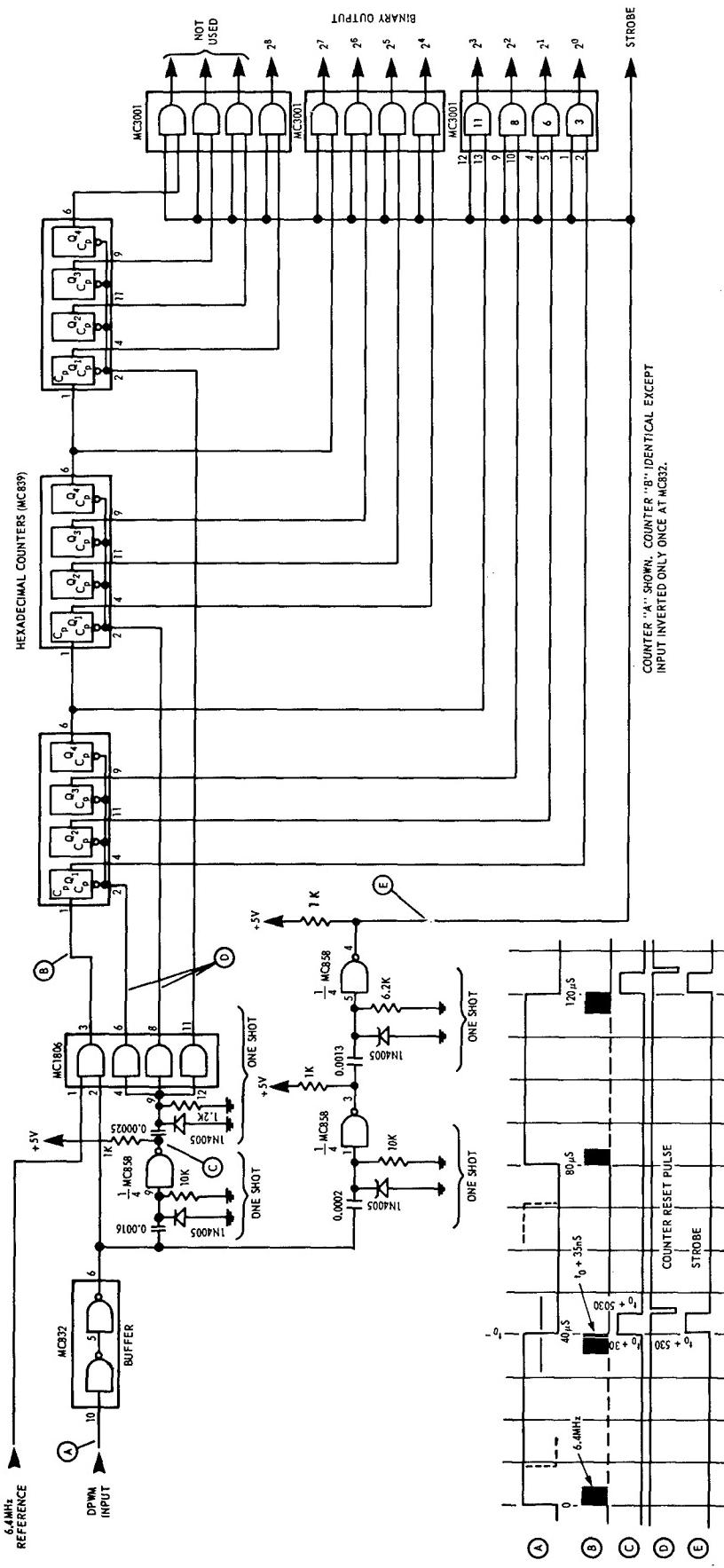


Figure 24 – Digital Interface Electronics Counter

contained 17 static data levels covering the full-scale range ( $\pm 1.414$  V) of Model 300. On the same tape were made seven dynamic calibration records which cover the frequency range of the recorder. The tape was labeled TT-1, and its contents are listed in Table 2. In making this tape, channels 2 and 6 were impressed with the data listed in Table 2, and channel 4 was shorted at the input to produce the reference. The static levels were imposed with a General Precision DIAL-A-SOURCE, Model DAS-46. The dynamic signals were applied with a Wavetek Model 142 Oscillator.

Because Model 300 had read after write capability, more extensive analog tests were performed than permitted by test tape TT-1. These were performed by simultaneously recording and playing back while making measurements at the analog output. These tests were performed on channels 2 and 6. The results were discussed in the following section.

### Analog Tests

All tests were conducted with a five-pole Butterworth low-pass filter installed for analog demodulation.

The analog output from test tape TT-1 is given for comparison in the section on digital tests. The data presented here were taken with the DMTR in the record mode with simultaneous playback. These data are more exhaustive than those recorded on TT-1, and are used for that reason. The test setup is shown in Figure 25. From this setup the information of Table 3 and Table 4 was obtained. As indicated in Table 3, the worst deviation from the ideal straight line, and hence the accuracy (only in-range data considered), was 0.39 percent of the  $p - p$  full scale (at an input of +1.000 V) for channel 2, and 0.32 percent of full scale (at an input of -1.400 V) for channel 6. The worst nonlinearities from the calculated best straight lines were 0.32 and 0.14 percent full scale for channels 2 and 6 respectively. The error curves for the data of Table 3 are shown in Figure 26. The calculated best straight lines are for channel 2:

$$V_{\text{out}} = 0.9955 V_{\text{in}} - 0.00124$$

and for channel 6:

$$V_{\text{out}} = 0.9958 V_{\text{in}} + 0.00076$$

The frequency response characteristics were investigated only for channel 2. First the response characteristics of the filter alone, then the filter plus the system were measured. The results are tabulated in Table 4 and presented graphically in Figure 27.

The output signal-to-noise (S/N) ratio was measured at zero and at positive and negative full scale. In no case was the rms noise level worse than 48 dB below the full-scale rms signal level of  $1V_{\text{rms}}$ .

The playback stability (that is, the stability of the analog demodulator) was checked by repeatedly replaying the same section of tape over a 3-hr period. The drift for the six repetitions made at 30-min intervals (constant ambient temperature  $\pm 2^\circ$  F) was 0.07 percent

TABLE 2 – CONTENTS OF MODEL 300 EVALUATION  
TAPE TT-1

File	Condition	Start Time (min)	Tape Counter (Start)
1	0.000 V	0:00	0000
2	-1.414 V	0:30	0070
3	-1.200 V	1:00	0140
4	-1.000 V	1:30	0200
5	-0.800 V	2:00	0290
6	-0.600 V	2:30	0375
7	-0.400 V	3:00	0445
8	-0.200 V	3:30	0520
9	0.000 V	4:00	0600
10	0.200 V	4:30	0700
11	0.400 V	5:00	0770
12	0.600 V	5:30	0845
13	0.800 V	6:00	0930
14	1.000 V	6:30	1010
15	1.200 V	7:00	1100
16	1.414 V	7:30	1185
17	0.000 V	8:00	1270
18	10 Hz, 1 V <sub>rms</sub>	8:30	1360
19	100 Hz, 1 V <sub>rms</sub>	9:00	1450
20	500 Hz, 1 V <sub>rms</sub>	9:30	1550
21	1 kHz, 1 V <sub>rms</sub>	10:00	1650
22	2 kHz, 1 V <sub>rms</sub>	10:30	1760
23	2.5 kHz, 1 V <sub>rms</sub>	11:00	1860
24	3 kHz, 1 V <sub>rms</sub>	11:30	1970
			12:00 (off)

TABLE 3 – DATA FROM ANALOG EVALUATION  
USING STATIC LEVELS

(The sign of the numbers in the error columns  
indicate direction of error)

Input (V)	Output (V)			Error (Percent FS)	
	Channel 2	Channel 6	Channel 2	Channel 6	
Over Range	{ -1.600 -1.500	-1.598 -1.505	-1.582 -1.488	0.07 -0.18	0.64 0.42
	-1.400	-1.404	-1.391	-0.14	0.32
	-1.300	-1.302	-1.292	-0.07	0.28
	-1.200	-1.196	-1.196	0.14	0.14
	-1.000	-0.992	-0.995	0.28	0.18
	-0.800	-0.792	-0.796	0.28	0.14
	-0.600	-0.593	-0.596	0.25	0.14
	-0.400	-0.395	-0.400	0.18	0
	-0.200	-0.197	-0.199	0.11	0.03
	0.000	0.001	-0.001	0.03	-0.03
	0.200	0.198	0.200	-0.07	0
	0.400	0.396	0.402	-0.14	0.07
	0.600	0.593	0.602	-0.25	0.07
	0.800	0.792	0.800	-0.28	0
	1.000	0.989	0.996	-0.39	-0.14
	1.200	1.191	1.195	-0.32	-0.18
	1.300	1.294	1.292	-0.22	-0.28
	1.400	1.396	1.392	-0.14	-0.28
Over Range	{ 1.500 1.600	1.501 1.605	1.487 1.582	0.03 0.18	-0.46 -0.64

TABLE 4 – FREQUENCY RESPONSE DATA FOR DIGITAL  
MAGNETIC TAPE RECORDER ANALOG EVALUATION  
WITH LOW-PASS FILTER

Frequency (Hz) (Constant Amplitude)	Filter Output (V)	Filter Output (dB)	System Output (V)	System Output (dB)
10	*		0.998	0
20	*		1.000	0
50	1.000	0	1.004	+ 0.34
100	1.000	0	1.006	+ 0.50
200	*		1.004	+ 0.34
300	1.000	0	*	
500	0.998	0	0.994	- 0.06
1000	0.994	- 0.06	0.982	- 0.16
1500	0.996	- 0.04	0.980	- 0.18
2000	0.967	- 0.28	0.952	- 0.43
2300	0.872	- 1.20	*	
2500	0.760	- 2.38	0.751	- 2.48
2700	0.625	- 4.08	*	
3000	0.435	- 7.22	0.435	- 7.22
5000	0.042	- 27.54	0.140	- 17.08
7000	7.8 mv	- 42.16	beginning of aliasing	
10000	1.1 mv	- 59.18		
12500	0.3 mv	- 70.46		
15000	0.1 mv	- 80.00		
20000	0			

\*Data point not taken.

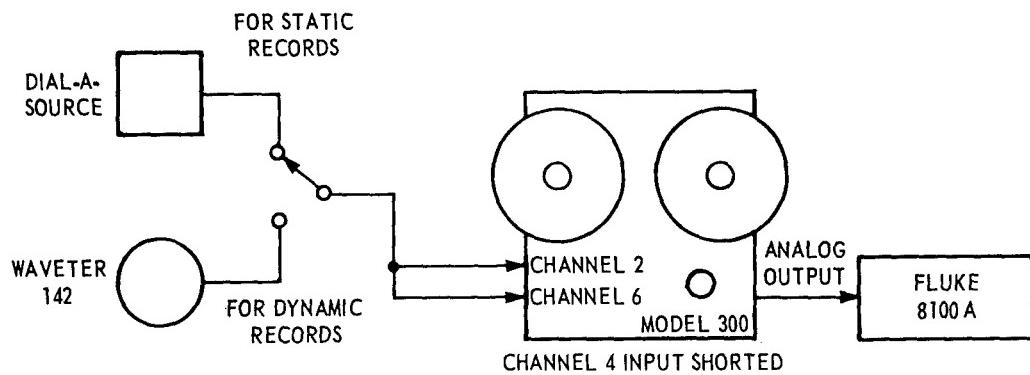


Figure 25 – Analog Evaluation Test Setup

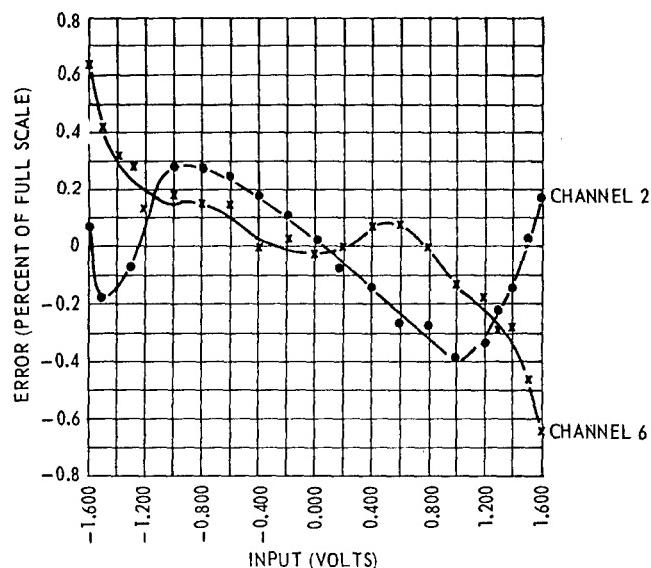


Figure 26 – Error Curves for Channels 2 and 6 (Analog Playback)

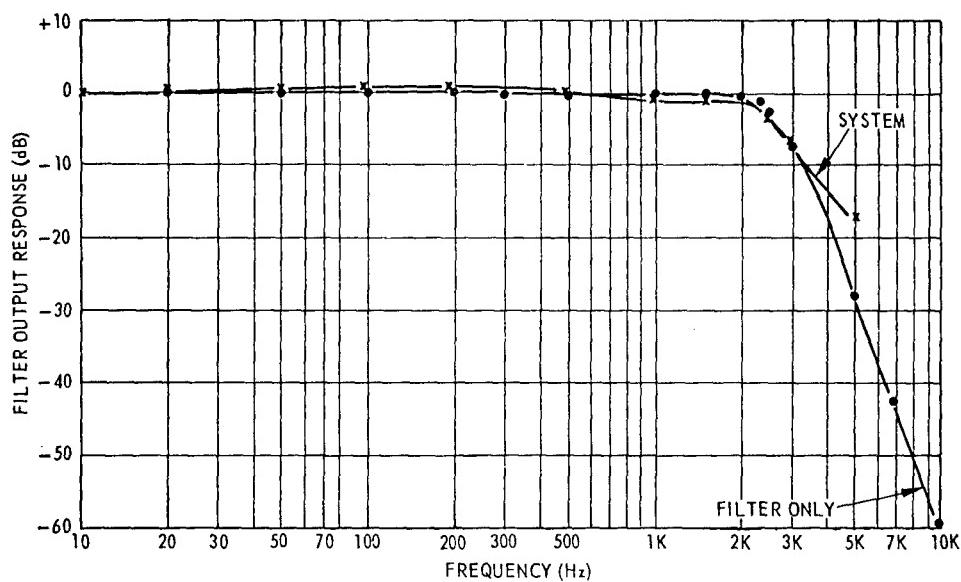


Figure 27 – Analog Frequency Response

of full scale. A further check was made over this same period to determine how faithfully the DMTR could repeat record and reproduce data. This check revealed a drift of 0.3 percent of full scale.

In summary, the following parameters of the DMTR in the analog playback mode were determined from measurement:

Accuracy	= 0.4 percent of full scale
Linearity	= 0.3 percent of full scale
S/N Ratio	$\geq 48$ dB
Bandwidth	d-c to 2.58 kHz (-3 dB at 2.58 kHz)
Drift (3-hr period)	< 0.1 percent reproduce 0.3 percent record and reproduce

### Digital Tests

All digital evaluation was performed on the data from channel 2 (see also Appendix B). More than one channel would have been used except for the tremendous volume of data that would have been generated. Checks with the analog demodulator indicated that channel 2 was a fair, representative channel.

The final computations were made in a CDC 6700 high-speed digital computer. The DMTR concept lends itself to direct interface of the DMTR with such a computer. If this were done, the recorder would become an on-line peripheral. However, this possibility was ruled out because of the investigative nature of the DMTR effort, and some other input device to the CDC 6700 had to be used. The device chosen was the nine-track tape recorder. The necessary nine-track tape was generated by hard-wire interfacing the DMTR to an Interdata Model IV minicomputer and outputting the Model IV in nine-track format. For the intended purpose, this solution was as good as if not better than interfacing directly to the 6700. Not only was the interfacing technique proven, but the evaluation was less costly and time consuming because of the almost infinitesimal cost of Model IV time compared to 6700 time and the much greater availability of the Model IV.

**Test Configuration and Techniques.** Test tape TT-1 was used exclusively for the digital evaluation (refer to Table 2 for the contents of the 24 files on TT-1). The outputs of channel 2 and channel 4 (the reference channel) were connected to the interface electronics described in Figures 22-24. Each of the two nine-bit outputs (counter A and counter B) were then fed to a nine-bit latch which lengthened the time during which the data were available. Each strobe pulse was fed to an adjustable one-shot. In these ways, the timing of the signals was optimized for Interdata Model IV use.

The Model IV used was equipped with 16,000 eight-bit memory locations; 640 of these locations were required for program storage. Each DMTR counter output consists of nine bits so that two Model IV memory locations were required for each counter output (the remaining seven bits were used for identification). Since both counter outputs were required

for the evaluation, a total of four memory locations was used to store each datum sample. Therefore after removal of the 640 memory locations for the program (leaving 15,360), a total of 3840 datum samples were storable. This represents 307.2 msec of data at the Model 300 rate of 12,500/sec. The lowest frequency on TT-1 (file 18) is 10 Hz, so that even this short sampling yields three complete cycles of data.

On completion of the memory loading, the data were block transferred to a nine-track tape recorder (Kennedy Model 3110) which produced the CDC 6700 compatible tape. Each of the 24 files were sampled, first with a PLL in the digital interface electronics and then with the PLL replaced by a free-running oscillator which operated near the nominal 6.4 MHz frequency required. Thus the resultant nine-track tape, ready for loading onto the CDC 6700, contained a total of 48 files. For surety two such tapes were made (not copies) and are designated DTT-1 and DTT-2.

Digital test tape DTT-2 was then played into the CDC 6700 and the data it contained were analyzed. The following information was provided as output data from the computer for each file:

1. Listing of all  $A$ 's and  $B$ 's in pairs for record purposes
2. Histogram (distribution) of  $A + B$
3. Range of  $A + B$
4. Mean of  $A + B$
5. Standard deviation ( $\sigma$ ) of  $A + B$
6. Listing of all  $V_1$ 's
7. Listing of all  $V_2$ 's
8. Means of  $V_1$ 's and  $V_2$ 's
9. Standard deviations of  $V_1$ 's and  $V_2$ 's

$$\left( V_1 = \frac{\frac{12}{A+B} - 256}{100} \right)$$

$$\left( V_2 = \frac{A - 256}{100} \right)$$

In addition the following was obtained for only the dynamic data (files 18-24 and 42-48):

10. Graphical outputs of a few cycles of the data from each file automatically computer plotted. Both  $V_1$  and  $V_2$  were plotted for each dynamic file.
11. Data distributions for each dynamic file.

**Reduced Computer Results.** The data for the static files are summarized in Table 5. Note that the value  $3\sigma$  (three standard deviations) is given for each voltage; 99.7 percent of all the samples for each voltage varied from the mean by no more than  $3\sigma$  value for that voltage. Figure 28 shows the error curves for  $V_1$  with and without a PLL as well as  $3\sigma$  precision bands. The error curves for  $V_2$  using a PLL are shown in Figure 29. Notice the loss of precision at those levels for which a dropout occurred on the reference track. The following conclusions can be drawn from these data.

TABLE 5 – SUMMARY OF DIGITAL EVALUATION RESULTS FOR THE STATIC FILES

	File Number	Input (volts)	A + B		V <sub>1</sub>		V <sub>2</sub>		Analog Output
			Mean	$\sigma$	Mean	$3\sigma_1$	Mean	$3\sigma_2$	
Phase-Locked Loop Reference	1	0.000	512.0	2.213	-0.006	0.025	-0.006	0.041	0.000
	2	-1.414	512.0	2.386	-1.412	0.021	-1.412	0.027	-1.428
	3	-1.200	512.0	2.300	-1.197	0.021	-1.197	0.029	-1.201
	4	-1.000	512.0	2.559	-0.998	0.022	-0.998	0.033	-0.995
	5	-0.800	511.9	3.216	-0.783	0.036	-0.785	0.111	-0.785
	6	-0.600	512.0	2.257	-0.595	0.021	-0.595	0.034	-0.590
	7	-0.400	512.0	3.401	-0.399	0.033	-0.400	0.131	-0.394
	8	-0.200	512.0	2.270	-0.202	0.021	-0.202	0.038	-0.198
	9	0.000	512.0	2.443	-0.005	0.023	-0.005	0.042	-0.001
	10	0.200	512.0	2.406	0.195	0.022	0.195	0.044	0.196
	11	0.400	512.0	2.210	0.394	0.022	0.394	0.043	0.396
	12	0.600	512.0	2.210	0.593	0.022	0.593	0.045	0.595
	13	0.800	512.0	2.293	0.792	0.021	0.793	0.048	0.795
	14	1.000	512.0	2.183	0.992	0.021	0.992	0.049	0.994
	15	1.200	512.0	2.173	1.194	0.021	1.194	0.051	1.196
	16	1.414	512.0	3.271	1.407	0.026	1.405	0.229	1.418
	17	0.000	512.0	2.458	-0.003	0.022	-0.003	0.042	0.001
Free-Running Oscillator Reference	25	0.000	510.3	1.558	-0.001	0.020	-0.010	0.031	-
	26	-1.414	510.4	1.678	-1.411	0.021	-1.414	0.025	-
	27	-1.200	510.5	1.661	-1.194	0.020	-1.198	0.024	-
	28	-1.000	510.5	1.587	-0.993	0.020	-0.998	0.025	-
	29	-0.800	510.4	1.677	-0.780	0.021	-0.785	0.028	-
	30	-0.600	510.5	1.665	-0.591	0.020	-0.596	0.028	-
	31	-0.400	510.6	1.707	-0.395	0.021	-0.401	0.030	-
	32	-0.200	510.6	1.662	-0.198	0.021	-0.205	0.031	-
	33	0.000	510.6	1.708	-0.002	0.021	-0.009	0.033	-
	34	0.200	510.6	1.728	0.198	0.021	0.189	0.035	-
	35	0.400	510.5	1.674	0.397	0.021	0.389	0.035	-
	36	0.600	510.4	1.623	0.596	0.021	0.586	0.035	-
	37	0.800	510.5	1.670	0.796	0.021	0.786	0.038	-
	38	1.000	510.5	1.662	0.994	0.021	0.984	0.039	-
	39	1.200	510.6	1.681	1.196	0.020	1.186	0.041	-
	40	1.414	510.6	1.671	1.409	0.020	1.398	0.042	-
	41	0.000	510.8	1.667	0.000	0.021	-0.006	0.033	-

1. When a PLL was used there was no significant difference between the means of corresponding  $V_1$ 's and  $V_2$ 's. There was an improvement in  $\sigma$  by a factor of 1.5 to 2.5 of  $V_1$  over  $V_2$  ( $\sigma$  improvement seemed to be greater at higher than at lower data values). This reduced precision of  $V_2$  was offset by the reduced computational complexity required to calculate  $V_2$ .

2. The difference in the means of the  $V_1$ 's with and without a PLL was only 0.2 percent of full scale.

3. The distribution of  $A + B$  was tighter *without* the PLL by a factor of about 1.4. Of course the sample was taken over only 307.2 msec so that the effect on  $\sigma$  caused by low frequency or long-term changes in tape speed or by changes of the free-running oscillator frequency are not evaluated. The tighter distribution without the PLL indicates that the PLL design was insufficient to maintain a tight lock on the multiplication factor 512. This supposition was strengthened when anomalies were discovered in files 5, 7, and 16 (indicated by the large standard deviations associated with these files). It was found on analysis that in each case the bad section of data was due to a dropout in the reference channel. After each dropout, the PLL reacted like a servo system (which it is) having a natural frequency of about 1 kHz but with a very low damping constant, requiring nearly 5 msec to settle. It is known that the Model 300 DMTR has a flutter component somewhat higher than 1 kHz. This explains the inability of the PLL to maintain tight lock and thus the higher standard deviations for  $A + B$  when the PLL was used. This same condition explains the slightly increased precision of  $V_1$  when the free-running oscillator was used, an improvement by a factor of about 1.95 except for those files which contained dropouts. There the improvement was less (about 1.5) because of the inability of the free-running oscillator to "drop out". The improvement was also very pronounced in the comparison of  $V_2$ 's where an improvement by a factor of 1.25 in precision using the oscillator over the PLL was calculated except for files with reference dropouts, in which cases the improvement ranged from 4.0 to 7.0.

4. When a free-running oscillator was used the improvement in precision of  $V_1$  over  $V_2$  ranged from a factor of 1.2 to 2.1, again depending on the value of the data (higher levels demonstrated greater improvement).

5. When a free-running oscillator was used, the means of  $V_2$ 's suffered the greatest inaccuracies. This is because the equation for  $V_2$  ( $V_2 = \frac{A - 256}{100}$ ) was based on a multiplication of 512 which, as can be seen from the means of  $A + B$ , was not the case. If the term "256" in the equation for  $V_2$  were replaced by one-half the mean of  $A + B$ , the equation would work perfectly. Unfortunately this requires a calculation of the "instantaneous" mean of  $A + B$  to allow for widely varying tape speeds. Such a calculation is very impractical. The approach of using the  $V_2$  equation, then, is unsatisfactory.

6. The best results were obtained by calculating for  $V_1$  and using a free-running oscillator. This also permitted the resolution to be varied by arbitrarily changing the ratio of oscillator frequency to DPWM sampling rate.

7. The effect of dropouts on the reference track has already been discussed. However, dropouts can also occur on a data track. The effect is not noticeable for static levels because the data which should occur in the dropout region are not missed—3840 samples were taken regardless of gaps in the data. Since all samples are the same (static data), the absence of data points cannot be detected. This does not hold for dynamic data where missed data points result in discontinuities (recall that the read strobes which cause the computer to read counters  $A$  and  $B$  are generated by the data signal; thus no data signal, no strobes and no samples). Although the values of each datum point in a dropout is lost, it is possible to discover the number of such lost points provided the dropout is not long. Either counter  $A$  or counter  $B$  is counting at any given time. When a dropout occurs, pulses are missed in pairs for the duration of the dropout. Therefore the counter that was in operation when the dropout began continues in operation throughout the duration of the dropout. This counter will fill up once and reset for each sample missed. It is a simple matter to add more stages to the counter so that for every sample missed, the counter simply overflows into these stages. The additional stages, then, actually count the number of overflows and hence the number of samples missed. These additional stages can then be read out with the next sample, and the number of missed samples and their locations are automatically recorded.

8. The DMTR techniques were most effective with the equation for  $V_1$  and a free-running oscillator. The curve in Figure 28b shows an inaccuracy (with the exception of the data for  $-0.800\text{ V}$ ) no greater than  $\pm 0.33$  percent of full scale. In addition the combined effects of accuracy and precision gave an inaccuracy no greater than  $\pm 1$  percent of full scale, again with the exception of the data of file 29. However, the inaccuracy of file 29 recurred in every test and did not fall in line with any of the other data points. It is therefore thought to be the result of an inaccurate data input level. Even with that file included, the accuracy was  $\pm 0.71$  percent and the combined effects of accuracy and precision were only  $\pm 1.45$  percent.

9. The linearity for  $V_1$ 's as calculated from files 25–41 (free-running oscillator) was  $\pm 0.49$  percent of full-scale deviation from the best straight line. The equation for the best straight line is:

$$V_{\text{out}} = 0.994 V_{\text{in}} + 0.0015$$

Ignoring file 29, the linearity deviated no more than 0.24 percent of full scale from the best straight line.

10. The full-scale signal-to-noise ratio calculated for the  $V_1$ 's of files 25–41 was 46 dB; for  $V_2$ 's of files 25–41, 40 dB; for  $V_1$ 's of files 1–17, 44 dB; and for  $V_2$ 's of files 1–17, it was 38 dB when dropouts were ignored and 25 dB where they were taken into account.

To summarize the digital evaluation of the static levels (files 1–17 and 25–41), it is concluded that excellent results can be achieved when the voltages are calculated from the samples by the equation for  $V_1$  and when a stable, free-running oscillator is used with the digital electronics. The oscillator should be closely set to the desired multiplication factor ,

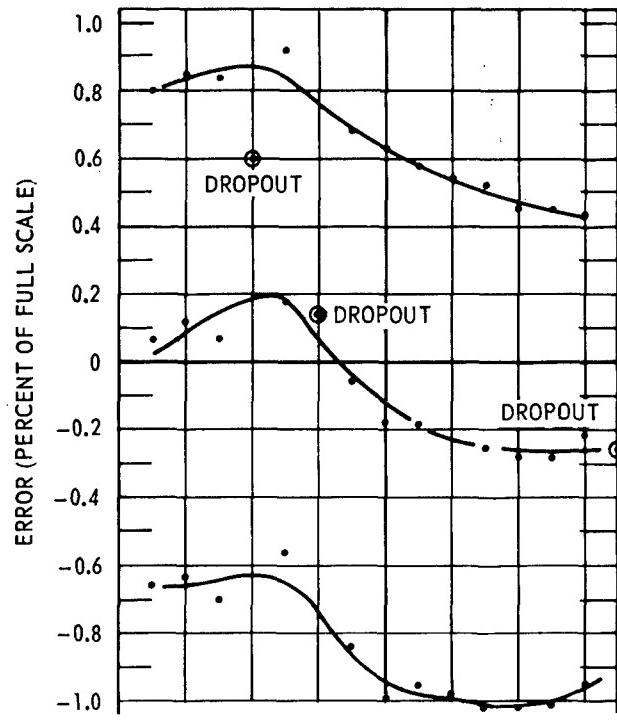


Figure 28a — Files 1–17 (with Phase-Locked Loop)  
(Points with Dropouts are not Included in Error Band)

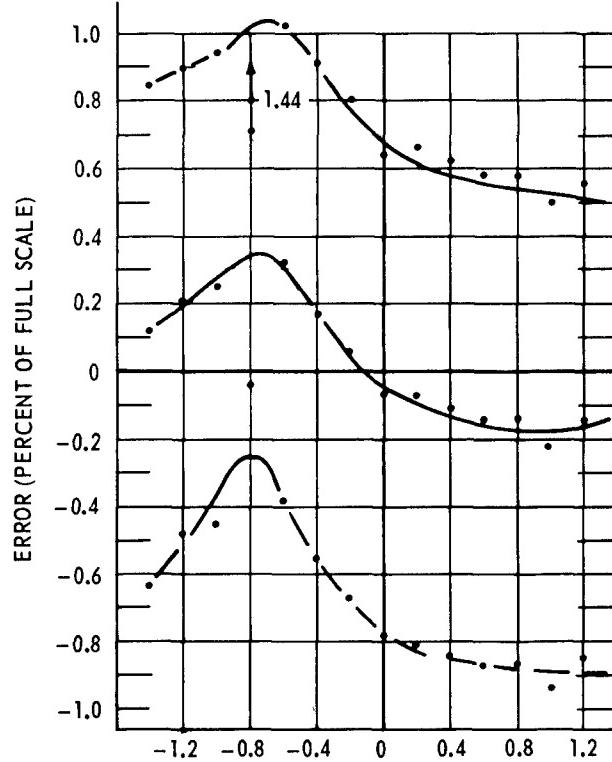


Figure 28b — Files 25–41 (with Free-Running Oscillator)

Figure 28 — Error Curves for  $v_1$  with and without a Phase-Locked Loop

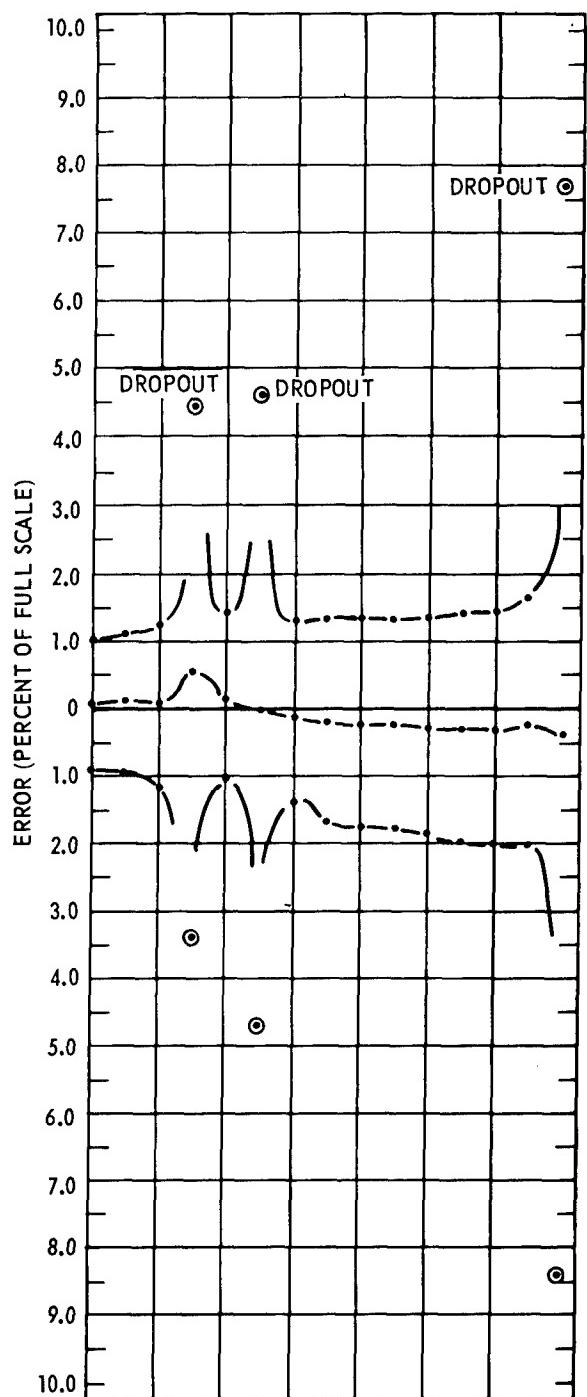


Figure 29 – Error Curve for  $v_2$  Using a Phase-Locked Loop

thus establishing the resolution. The following performance was measured by using data taken under these conditions and a multiplication factor of 512:

Accuracy	$\pm 0.71$ percent of full scale
Linearity	$\pm 0.49$ percent of full scale
Full Scale S/N Ratio	46 dB
Precision	$\pm 0.74$ percent of full scale ( $3\sigma$ significance)

The dynamic data (files 18-24 and 42-48) are a series of single-frequency sine waves. Each file of a group contains a different frequency from each of the others of a group. These frequencies range from 10 Hz at the low frequency end to 3 kHz at the high end. The frequency in each file is:

File Numbers	Content (Hz)
18,42	10
19,43	100
20,44	500
21,45	1000
22,46	2000
23,47	2500
24,48	3000

Little more could be economically learned from the dynamic data over what was learned from the static. However portions of the files were plotted by the CDC 6700. The plots for  $V_1$  and  $V_2$  of files 42 and 43 are presented in Figures 30-33. Note the improvement in  $S/N$  of  $V_2$  over  $V_1$  in each case. In addition, Appendix C tabulates the distributions of the data points of each file for both  $V_1$  and  $V_2$  and gives a few graphically as well. The distributions show the characteristic arcsin shape expected from sampled sinusoids. No unusual anomalies were discovered.

It should be noted that the 50-mV offset present in the sinusoid displays was present in the original signal and was not introduced by the DMTR signal or the evaluation techniques.

## OTHER USES OF THE CONCEPTS

Mention should be made of other uses for the concepts described herein. By far the most utilitarian application thus far devised for DPWM is the DMTR described in this report. This use can be extended to other types of tape recording devices. Small portable devices such as cassette and cartridge recorders are candidates for prime consideration. The large variations in tape speed that accompany battery operation will cause no problem if DPWM techniques are used. The possibility even exists for a "shirt pocket-sized" instrumentation recorder.

Another technique has been developed to allow the multiplexing of two or more PWM signals into a single DPWM signal, thus increasing the channel capacity.

Portions of the circuitry can be used in other applications. For instance, an accurate analog-to-digital converter can easily be developed by using the circuits of the triangle

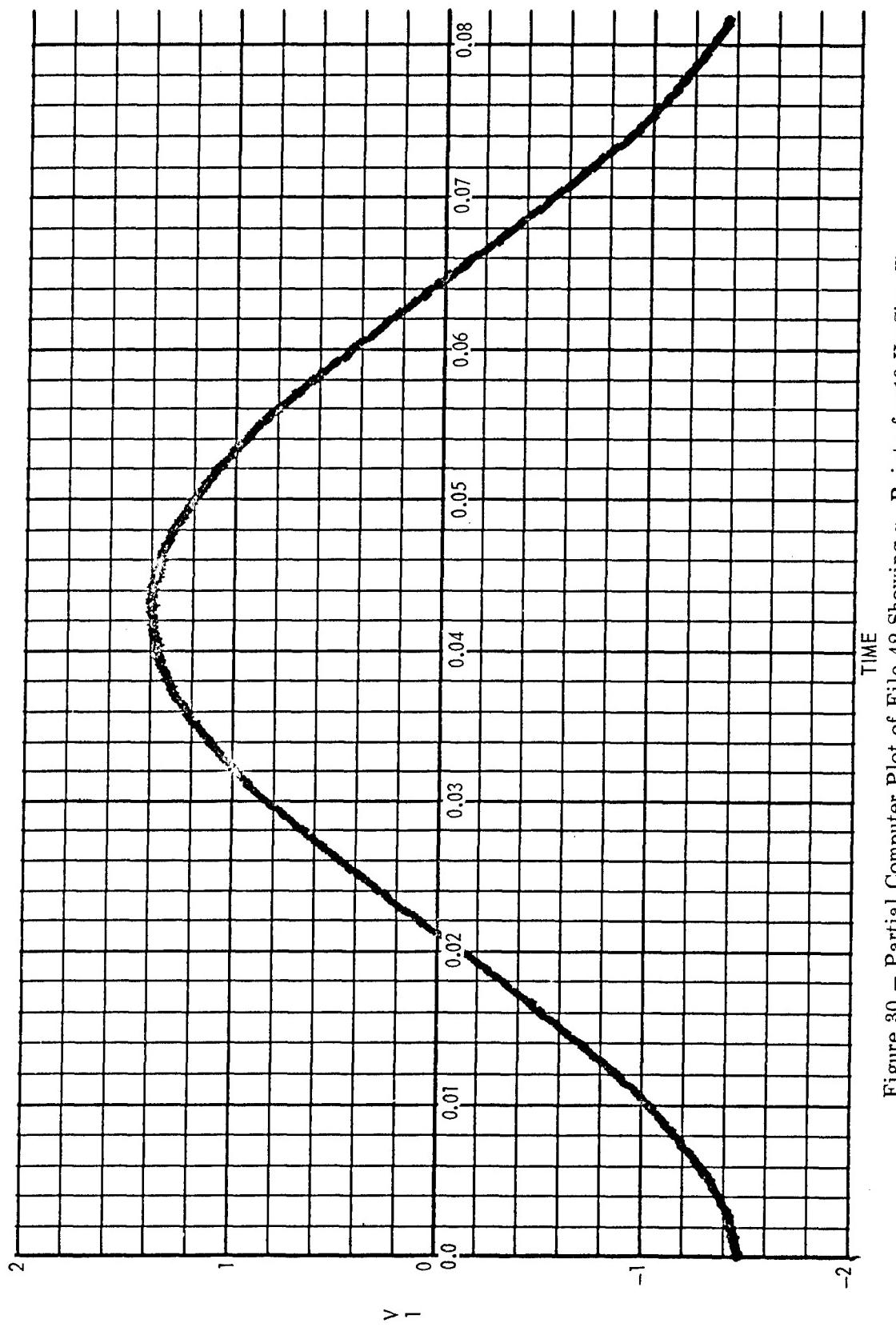


Figure 30 – Partial Computer Plot of File 42 Showing  $v_1$  Points for 10-Hz Sine Wave

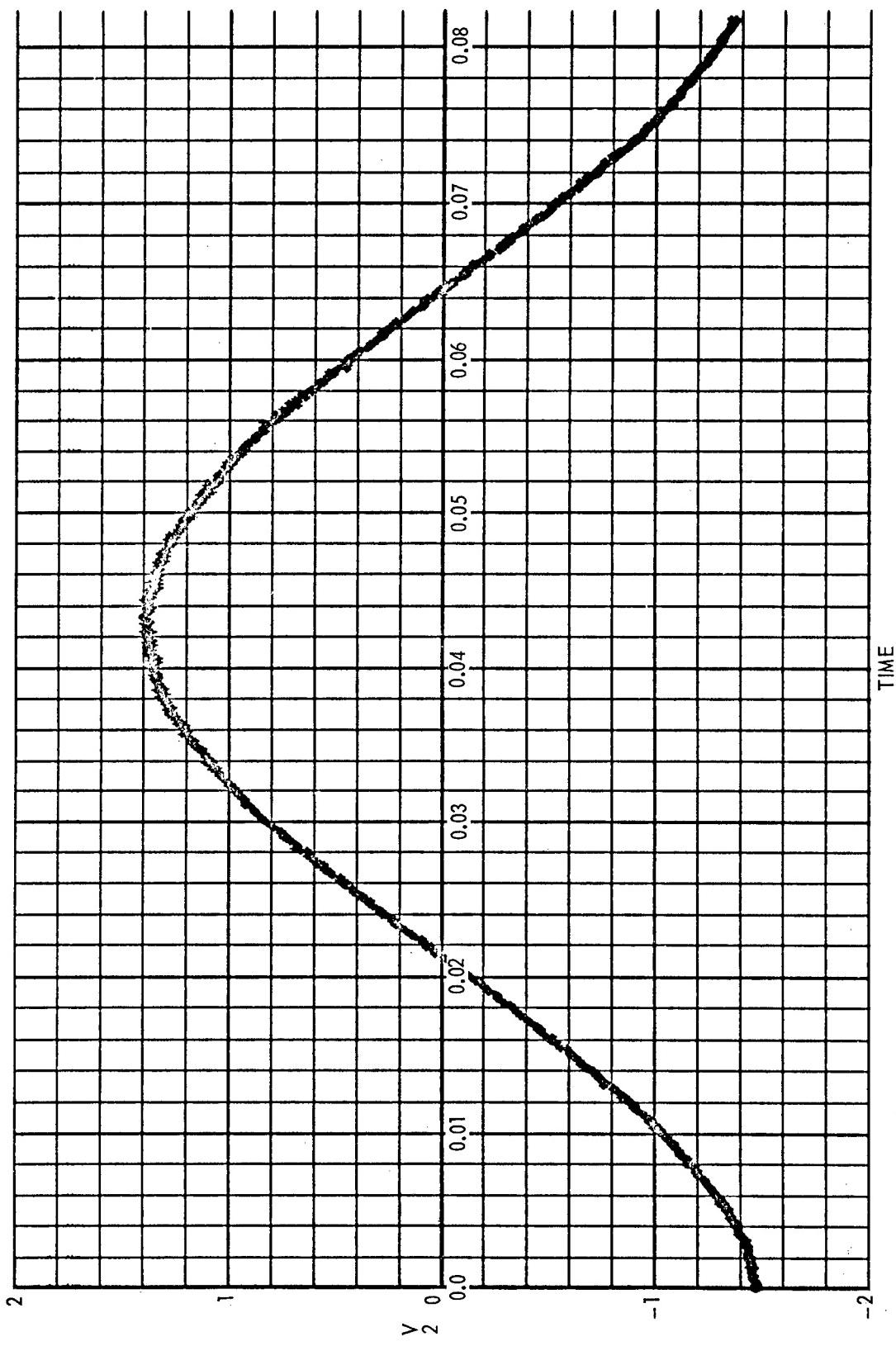


Figure 31 – Partial Computer Plot of File 42 Showing  $v_2$  Points for 10-Hz Sine Wave

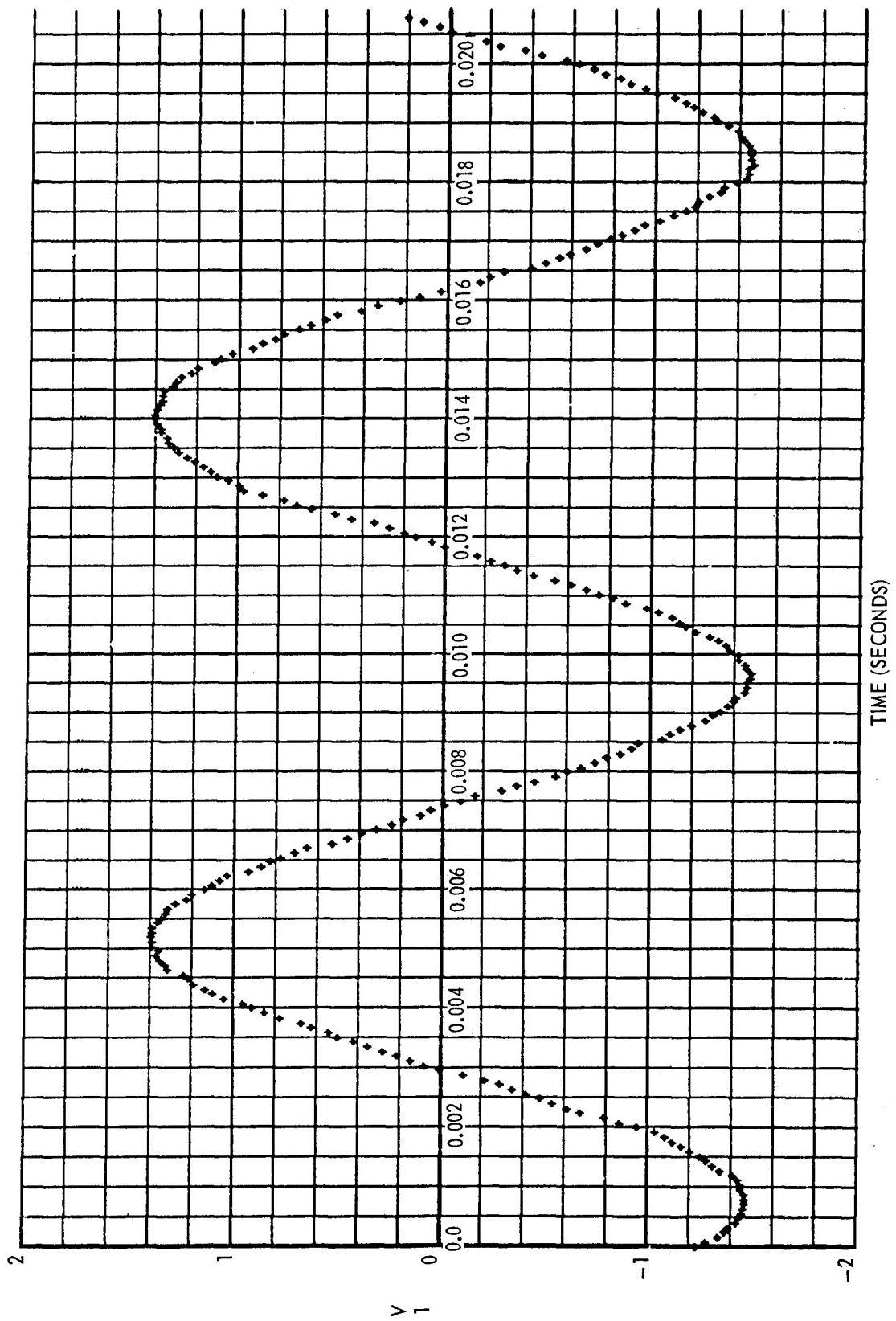


Figure 32 — Partial Computer Plot of File 43 Showing  $v_1$  Points for 100-Hz Sine Wave

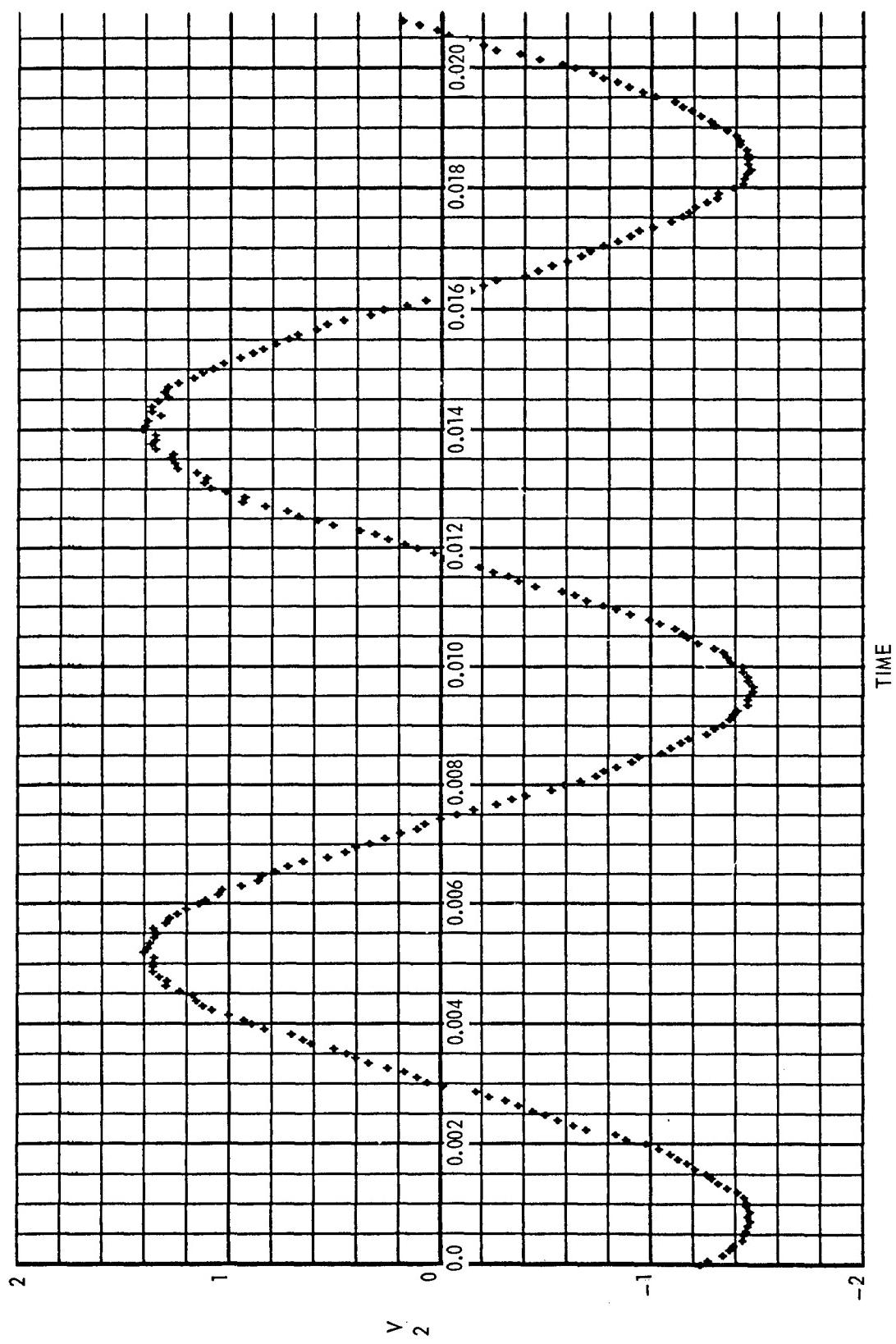


Figure 33 -- Partial Computer Plot of File 43 Showing  $v_2$  Points for 100-Hz Sine Wave

generator, record card (comparator and input stages), and the phase-locked loop and counters. As in the tape recorder, sampling rate and precision could easily be changed to meet different needs. The raw DPWM signals rather than their digital equivalents can also be telemetered from remote locations, thus reducing the required bandwidths.

## RECOMMENDATIONS

1. The additional digital bit rate required presently hampers increasing the "digital bandwidth" of the recorder. Therefore techniques should be developed to extend this bandwidth for both analog and digital outputs.
2. Methods for more direct access to high-speed digital computers than that used in this development should be investigated in order to further reduce data reduction time, the amount of equipment involved, and hence the cost.
3. A production development program should be initiated to refine the tape recorder properties (possibly conducted by commercial concerns).

## ACKNOWLEDGMENTS

A mere acknowledgment is insufficient to express the invaluable effort expended on this program by Mr. George Cook. It is he who was initially responsible for the DMTR concept. Until Mr. Cook's retirement in 1969, the author worked in this program under the steady-ing influence of his most capable guidance. Were it not for his innovative nature, his discontent with the "status quo," and his extensive foresight, the continuing development of the tape recorder might never have occurred.

An expression of gratitude is due Mr. Dave Milne of the Central Instrumentation Department for his help in the solution of tricky circuit problems on numerous occasions. Mr. Milne is also primarily responsible for the development of the DSID circuitry.

Finally, the author acknowledges the efforts of Mr. Jack Gordon of the Ship Performance Department who provided and developed the software for the Interdata Model IV minicomputer and those of Messrs. Ralph Johnson and Richard Sigman of the Computation and Mathematics Department who developed the techniques and software for the digital analysis on the CDC 6700 computer.

## APPENDIX A

### CONSIDERATIONS OF DPWM STIMULATED BY STATIC AND DYNAMIC DATA

Differential pulse width modulation (DPWM) is a sampling technique for data conversion. The data to be sampled  $v_m$  are compared (in amplitude) to a triangular voltage  $v_r$ . From this comparison, a rectangular wave  $v_c$  is formed. The data are stored in this rectangular wave as a function of its symmetry. Because the rectangular wave is generated from a comparison of the data with the reference triangular wave, and if the data amplitude is always kept smaller in magnitude and frequency than the triangular wave,

$$|v_m| < |v_r| \quad [A-1]$$

$$f_m < f_r \quad [A-2]$$

the resultant DPWM signal will have an average frequency  $f_c$  equal to  $f_r$ . If  $v_m$  is allowed the same amplitude range as  $v_r$ , then the symmetry  $P$  will have the range 0 percent  $< P <$  100 percent. Furthermore if  $v_r$  is symmetrical about the abscissa or time axis (i.e., its positive and negative amplitudes are equal in magnitude), then  $P = 50$  percent for  $v_m = 0$ .

The DPWM signal is shown in Figure A-1. as  $v_m$  changes, the pulse width  $t_y - t_x$  changes proportionally. Also  $t_z$  changes as a result of the pulse width change of the next sample located at  $t = 2\pi$ . If  $v_m$  and  $v_r$  are normalized so that  $-1 \leq v_r \leq 1$  and  $-1 < v_m < 1$ , then a table of values for  $v_m$ ,  $t_x$ ,  $t_y$ ,  $t_z$  can be made:

$v_m$	$t_x$	$t_y$	$t_z$
-1	0	0	$2\pi$
0	$-\pi/2$	$\pi/2$	$3\pi/2$
1	$-\pi$	$\pi$	$\pi$

The following relationships result

$$t_x = -\frac{v_m + 1}{2} \pi \quad [A-3]$$

$$t_y = \frac{v_m + 1}{2} \pi \quad [A-4]$$

$$t_z = \frac{3 - v_m}{2} \pi \quad [A-5]$$

The Fourier series for a DPWM signal resulting from static stimuli can be found from this information. Because even function symmetry exists,  $f(t) = f(-t)$ , the sine coefficients are zero,  $b_n = 0$ . Therefore  $a_0$ , the steady state term, and  $a_n$ , the cosine coefficients are all that are required.<sup>1</sup>

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<sup>1</sup>Goldman, S. A., "Frequency Analysis, Modulation, and Noise," McGraw-Hill Book Company Inc., New York (1948) Chapters 1,2, and 5.

$$\begin{aligned}
a_o &= \frac{1}{T} \int_0^T f(t) dt \\
&= \frac{1}{2\pi} \int_{t_x}^{t_y} (1) dt + \frac{1}{2\pi} \int_{t_y}^{t_z} (-1) dt \\
&= \frac{1}{2\pi} \int_{-\frac{v_m+1}{2}\pi}^{\frac{v_m+1}{2}\pi} dt - \frac{1}{2\pi} \int_{\frac{v_m+1}{2}\pi}^{\frac{3-v_m}{2}\pi} dt
\end{aligned}$$

$$a_o = v_m$$

[A-6]

$$\begin{aligned}
a_n &= \frac{2}{T} \int_0^T f(t) \cos n\omega_c t dt \quad \left[ \omega_c = 2\pi/T = 1 \right] \\
&= \frac{1}{\pi} \int_{t_x}^{t_z} f(t) \cos nt dt \\
&= \frac{1}{\pi} \int_{-\frac{v_m+1}{2}\pi}^{\frac{v_m+1}{2}\pi} \cos nt dt - \frac{1}{\pi} \int_{\frac{v_m+1}{2}\pi}^{\frac{3-v_m}{2}\pi} \cos nt dt \\
&= \frac{1}{\pi} \left[ \frac{\sin n\left(\frac{v_m+1}{2}\pi\right)}{n} - \frac{\sin n\left(-\frac{v_m+1}{2}\pi\right)}{n} - \frac{\sin n\left(\frac{3-v_m}{2}\pi\right)}{n} + \frac{\sin n\left(\frac{v_m+1}{2}\pi\right)}{n} \right] \\
&= \frac{4}{n\pi} \left[ \sin n\left(\frac{v_m+1}{2}\pi\right) \right] \\
&= \frac{4}{n\pi} \left[ \sin \frac{n v_m}{2} \pi \cos \frac{n\pi}{2} + \cos \frac{n v_m}{2} \pi \sin \frac{n\pi}{2} \right] \\
a_n &= \begin{cases} \frac{4}{n\pi} (-1)^{\frac{n}{2}} \sin \left( \frac{n v_m}{2} \pi \right) & \text{for } n \text{ even} \\ \frac{4}{n\pi} (-1)^{\frac{n-1}{2}} \cos \left( \frac{n v_m}{2} \pi \right) & \text{for } n \text{ odd} \end{cases} \quad [A-7]
\end{aligned}$$

By translating angles and combining the two terms of the coefficient the complete series is found to be:

$$f(t) = v_m + \frac{4}{n\pi} \sin n \left( \frac{v_m+1}{2} \right) \pi \cos nt \quad [A-8]$$

which can be expanded to

$$f(t) = v_m + \frac{4}{\pi} \cos \frac{v_m}{2} \pi \cos t - \frac{2}{\pi} \sin v_m \pi \cos 2t - \frac{4}{3\pi} \cos \frac{3v_m}{2} \pi \cos 3t + \dots$$

Clearly the data  $v_m$  can be simply retrieved with a low-pass filter whose cutoff frequency lies below the normalized fundamental frequency  $\omega_c = 1$ .

The situation is modified somewhat if  $v_m$  is dynamic in nature. Again assume that  $-1 < v_m < 1$ . For any data source  $v_m(t)$ , the pulse width is assumed to have the form

$$\Delta t = \frac{T}{2} + \frac{T}{2} v_m(t) = \frac{T}{2} [1 + v_m(t)] \quad [\text{A-9}]$$

where  $T$  (the sampling period)  $\leq \frac{1}{5f_m}$ . A mathematical expression for the sampling function  $f_s(t)$  can be derived as follows from Figure A-2:

$$c_n = \int_{t_1}^{t_2} e^{-jn\omega_o t} dt = \frac{1}{n\omega_o} \left( \frac{e^{-jn\omega_o t_2} - e^{-jn\omega_o t_1}}{-j} \right)$$

Substituting  $t_2 = t_1 + \Delta t$

$$c_n = \frac{1}{n\omega_o} e^{-jn\omega_o t_1} \left[ \frac{e^{-jn\omega_o \Delta t} - 1}{-j} \right] \quad [\text{A-10}]$$

By factoring out of the bracket the term

$$\begin{aligned} c_n &= \frac{1}{n\omega_o} e^{-jn\omega_o t_1} e^{-jn\omega_o \frac{\Delta t}{2}} \left[ \frac{e^{-jn\omega_o \frac{\Delta t}{2}} - e^{jn\omega_o \frac{\Delta t}{2}}}{-j} \right] \\ &= \frac{2}{n\omega_o} e^{-jn\omega_o \left( t_1 + \frac{\Delta t}{2} \right)} \left[ \frac{e^{jn\omega_o \frac{\Delta t}{2}} - e^{-jn\omega_o \frac{\Delta t}{2}}}{2j} \right] \end{aligned}$$

Recognizing that  $t_1 + \frac{\Delta t}{2} = 0$  and that

$$\frac{e^{jn\omega_o \frac{\Delta t}{2}} - e^{-jn\omega_o \frac{\Delta t}{2}}}{2j} = \sin n\omega_o \frac{\Delta t}{2}$$

$$c_n = \frac{2}{n\omega_o} \sin n\omega_o \frac{\Delta t}{2} = \Delta t \frac{\sin n\omega_o \frac{\Delta t}{2}}{n\omega_o \frac{\Delta t}{2}} \quad [\text{A-11}]$$

From the exponential form of the Fourier series

$$f_s(t) = \frac{1}{T} \sum_{n=-\infty}^{\infty} c_n e^{jn\omega_o t}$$

$$f_s(t) = \frac{\Delta t}{T} \sum_{n=-\infty}^{\infty} \frac{\sin n\omega_o}{n\omega_o} \frac{\Delta t}{2} e^{jn\omega_o t} \quad [A-12]$$

By extracting the term for  $n = 0$  and combining terms for positive and negative  $n$ , the following is obtained:

$$f_s(t) = \frac{\Delta t}{T} + \frac{\Delta t}{T} \sum_{n=1}^{\infty} \left[ \frac{\sin n\omega_o}{n\omega_o} \frac{\Delta t}{2} e^{jn\omega_o t} - \frac{\sin -n\omega_o}{n\omega_o} \frac{\Delta t}{2} e^{-jn\omega_o t} \right]$$

$$= \frac{\Delta t}{T} + \frac{\Delta t}{T} \sum_{n=1}^{\infty} \frac{2 \sin n\omega_o}{n\omega_o} \frac{\Delta t}{2} \left( \frac{e^{jn\omega_o t} + e^{-jn\omega_o t}}{2} \right)$$

$$= \frac{\Delta t}{T} + 2 \sum_{n=1}^{\infty} \frac{\Delta t}{T} \frac{\sin n\omega_o}{n\omega_o} \frac{\Delta t}{2} \cos n\omega_o t \quad [A-13]$$

Substituting from Equation [A-9] for  $\Delta t$  the expression for the DPWM signal  $v_c(t)$ , results:

$$v_c(t) = \frac{\frac{T}{2} [1 + v_m(t)]}{T} + 2 \sum_{n=1}^{\infty} \frac{\Delta t}{T} \frac{\sin n\omega_o}{n\omega_o} \frac{\Delta t}{2} \cos n\omega_o t$$

$$v_c(t) = \frac{1}{2} + \frac{v_m(t)}{2} + 2 \sum_{n=1}^{\infty} \frac{\Delta t}{T} \frac{\sin n\omega_o}{n\omega_o} \frac{\Delta t}{2} \cos n\omega_o t \quad [A-14]$$

The first term of Equation [A-14], a d-c level, is a consequence of the amplitude nonsymmetry of  $f_s(t)$ . The second term contains the data and has a bandwidth equal to the data. The third term represents frequency components about  $n\omega_o$  and will thus be rejected by a low-pass filter having a bandwidth equal to that of  $v_m(t)$ .<sup>2</sup> Therefore, the data can be recovered with a low-pass filter.

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<sup>2</sup>Hancock, F. C., "Introduction to The Principles of Communication Theory," McGraw-Hill Book Company, Inc., New York (1961) Chapters 1 and 2.

The foregoing described a method of reducing the DPWM to the original analog form of the modulating signal. Because of the nature of the DPWM, the contained information can also be extracted digitally by one of several suitable methods. Basically, each process measures the pulse width or symmetry of each DPWM sample in some manner.

The digitizing process, however, implies that a single value accurately represents each sample. This is not the case for sampled dynamic data. The sample lacks derivation (or rate) information and is therefore of less value than an analog sample taken at the same time. The analog techniques yield the necessary derivative information for the data during the time  $\Delta t$ . Furthermore a value obtained digitally implies that a sample was taken in zero time which is clearly not the case. Thus the digital process spreads the zero-time sample out over the sample period  $\Delta t$ . This is the same as converting an impulse into a pulse of width  $\Delta t$ . A network which performs that conversion has a  $\frac{\sin x}{x}$  transfer function. The digital process has the same effect as passing the original data signal through a low-pass filter with a  $\frac{\sin x}{x}$  characteristic. The first zero would occur at  $1/\Delta t$ . Thus in the DPWM for small  $\Delta t$  (low data voltage) the bandwidth is large whereas for large  $\Delta t$ , the first zero occurs at lower frequency.

A simple example should further clarify the effect. Let the data take the form of a ramp

$$v_m = m_m t + v_o \quad [A-15]$$

where  $v_o$  is the ordinate intercept. Figure A-3 shows this function as it intersects the carrier signal  $v_c$ .

The carrier function is defined as

$$v_c^- = -m_c t - A, \quad -\pi \leq t \leq 0 \quad [A-16]$$

$$v_c^+ = m_c t - A, \quad 0 < t \leq \pi$$

The intersection of  $v_m$  with  $v_c^-$  is

$$t^- = -\frac{(v_o + A)}{m_m + m_c} \quad [A-17]$$

and the intersection of  $v_m$  with  $v_c^+$  is

$$t^+ = -\frac{(v_o + A)}{m_m - m_c} \quad [A-18]$$

The center of the resultant DPWM pulse  $t_{p.c.}$  is

$$t_{p.c.} = \frac{t^+ + t^-}{2} = \frac{m_m (v_o + A)}{m_c^2 - m_m^2}$$

This represents the time error; i.e., the pulse is actually centered at  $t = t_{p.c.}$ , not  $t = 0$ . This is equivalent to

$$v_m|_{t_{p.c.}} = \frac{m_m^2 (v_o + A)}{m_c^2 - m_m^2} + v_o$$

or an error  $\epsilon$  equal to  $v_m|_{t_{p.c.}} - v_o$ :

$$\epsilon = \frac{m_m^2 (v_o + A)}{m_c^2 - m_m^2} \quad [A-19]$$

Thus the error is zero when either  $m_m = 0$  or  $v_o = -A$ . Also, the greater the number of samples taken per cycle of data, the greater will be the denominator  $m_c^2 - m_m^2$  and the smaller the error. Under the restriction  $m_m \leq \frac{m_c}{5}$ , the maximum possible error is ( $v_o = A$ ):

$$\begin{aligned} \epsilon_{\max} &= \frac{m_c^2/25 (2A)}{m_c^2 - m_c^2/25} = \frac{m_c^2 (2A)}{24 m_c^2} \\ \epsilon_{\max} &= \frac{A}{12} \end{aligned} \quad [A-20]$$

The above error is extreme, however, because  $m_m = m_c/5$  and  $v_o = A$  cannot occur simultaneously. When this is taken into consideration, a recalculation shows that the actual maximum error is  $\frac{A}{15}$ . This represents approximately a 3.3-percent error at the upper frequencies. Further restrictions are usually made to limit the amplitude of  $v_m$  to  $A/2$ . This further reduces the maximum uncorrected error to about 2.3 percent.

Error can be further reduced mathematically in the digital computer to about 1 percent. Refer to the computer program of Appendix B. Thus the DPWM can be digitally reduced with a fixed time base to an amplitude accuracy on the order of 1 percent. This makes the DPWM digital demodulation scheme ideally suited for real-time data spectrum analysis.

It should be kept in mind that as with any sampling system, the input data for DPWM must be band limited to prevent aliases of higher frequencies from appearing. This is also important in order to prevent higher order spectra signals from being folded into the original spectrum.

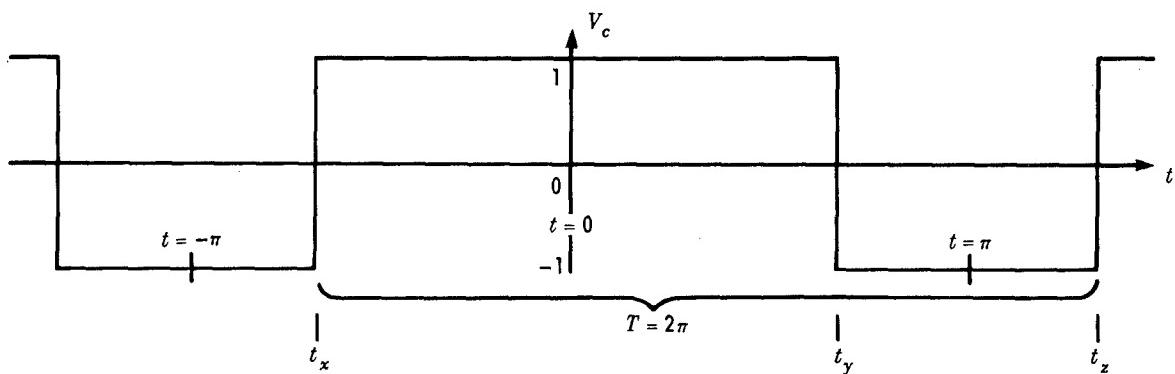


Figure A1 – Normalized Differential Pulse Width Modulation Sample

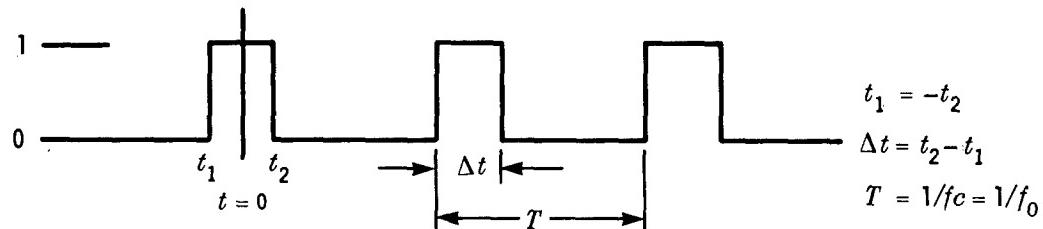


Figure A2 – The Sampling Function

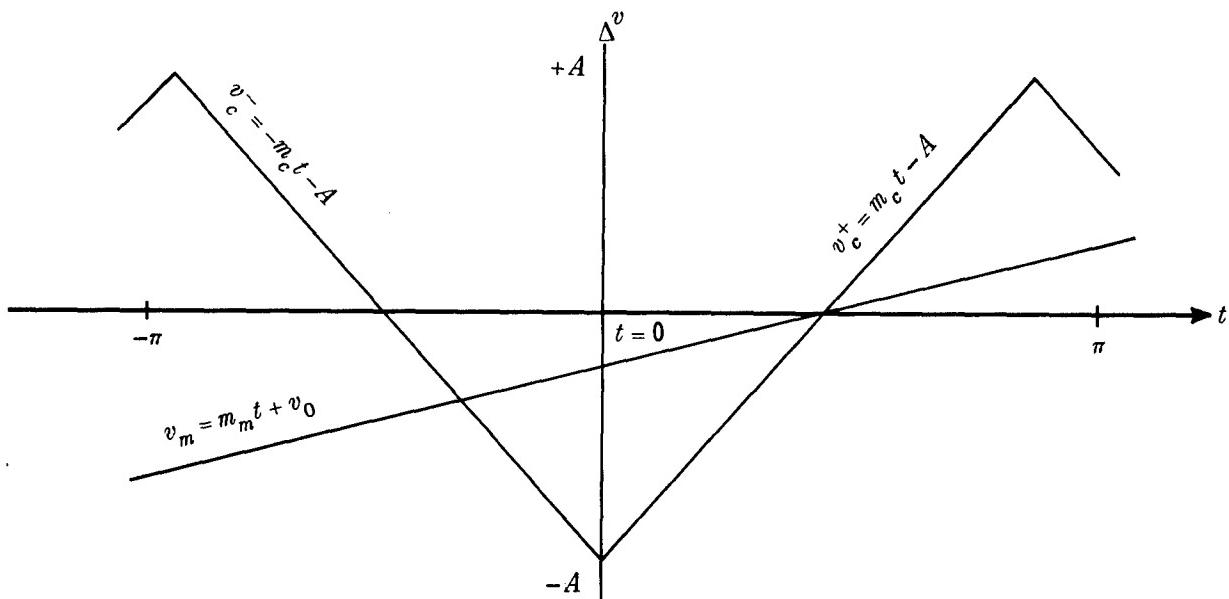


Figure A3 – Intersection of a Ramp with Differential Pulse Width Modulation Triangular Carrier

## APPENDIX B

### DIGITAL INTERFACING TECHNIQUES (HARDWARE AND SOFTWARE) USED WITH THE DMTR MODEL 300

Two digital computers were used in the digital evaluation of Model 300. The initial manipulation was performed on an Interdata Model IV minicomputer. The Model 300 DMTR output was wired to the Interdata input, thus enabling direct data transfer. The Interdata Model IV coded and reformatte the data into a configuration suitable for use in the CDC 6700 general purpose computer. The reformed data were then put on nine-track magnetic tape. This tape was used to provide input to the CDC 6700, which actually performed the data analysis.

#### INTERDATA MODEL IV

The configuration consisted of an Interdata Model IV computer with 16K of memory, one half-word *I/O* module, one Kennedy *I/O* module, and one Kennedy Model 3110 nine-track tape deck. Because the data samples from the Model 300 consisted of two nine-bit words (*A* and *B*), a modification was required to the half-word *I/O* module which normally accepts 16 bits of data in two eight-bit groups. The READ BLOCK instruction was used which provides the high transfer rate required (150 kHz). Eighteen bits make up a DMTR SAMPLE (two nine-bit words), and a 24-bit input capability was established by the *I/O* module modification. The surplus bits were filled with a code which enabled the CDC 6700 to recombine the eight-bit words received from the nine-track tape in the proper order. A complete sample has the following format:

BIT POSITION

1	2	3	4	5	6	7	8	
$2^8$	$2^7$	$2^6$	$2^5$	$2^4$	$2^3$	$2^2$	$2^1$	- $A_1$
0	0	0	0	0	0	$2^0$	1	- $A_2$
$2^8$	$2^7$	$2^6$	$2^5$	$2^4$	$2^3$	$2^2$	$2^1$	- $B_1$
0	0	0	0	0	0	$2^0$	0	- $B_2$

Each *A* and *B* was broken into two halves:  $A_1$  and  $A_2$ ,  $B_1$  and  $B_2$ . Data bits were loaded into all locations of  $A_1$  as shown and the least significant *A* bit into location 7 of  $A_2$ . The zeros of locations 1 through 6 of  $A_2$  and  $B_2$  are used to indicate the subscript 2 since data of such a low value cannot occur. Location 8 of the same lines which contain the zeros reveals whether the datum is an *A* or *B* value. Thus each pair of eight-bit words is identifiable and decodable.

Each sample requires four memory locations. Thus (after some memory is set aside for program storage) 3840 samples of data were stored at a time. These 3840 samples, constituting a file, were then read onto the nine-track tape in four equal records of 960 samples. All files were generated identically.

## CDC 6700 COMPUTER

The nine-track tape containing 48 files was loaded on the CDC 6700 general purpose computer. Each file was unpacked into 60-bit CDC 6700 words. Thus each 60-bit word consisted of 7 1/2 eight-bit bytes (one track of the nine-track tape was reserved for parity, leaving eight data bits). After unpacking, the coding included with the data was searched to establish the proper starting point. That is, data must be read in order starting with  $A_1$ , then  $A_2$ ,  $B_1$ ,  $B_2$ , and so forth. After the entire file had been unpacked, a listing of all  $A$ 's and all  $B$ 's in a file, and a histogram (distribution) of  $A + B$  were printed for record purposes. For each  $A, B$  pair voltages  $v_1$  and  $v_2$  were calculated as follows:

$$v_1 = \frac{\frac{512 A}{A + B} - 256}{100}$$

$$v_2 = \frac{A - 256}{100}$$

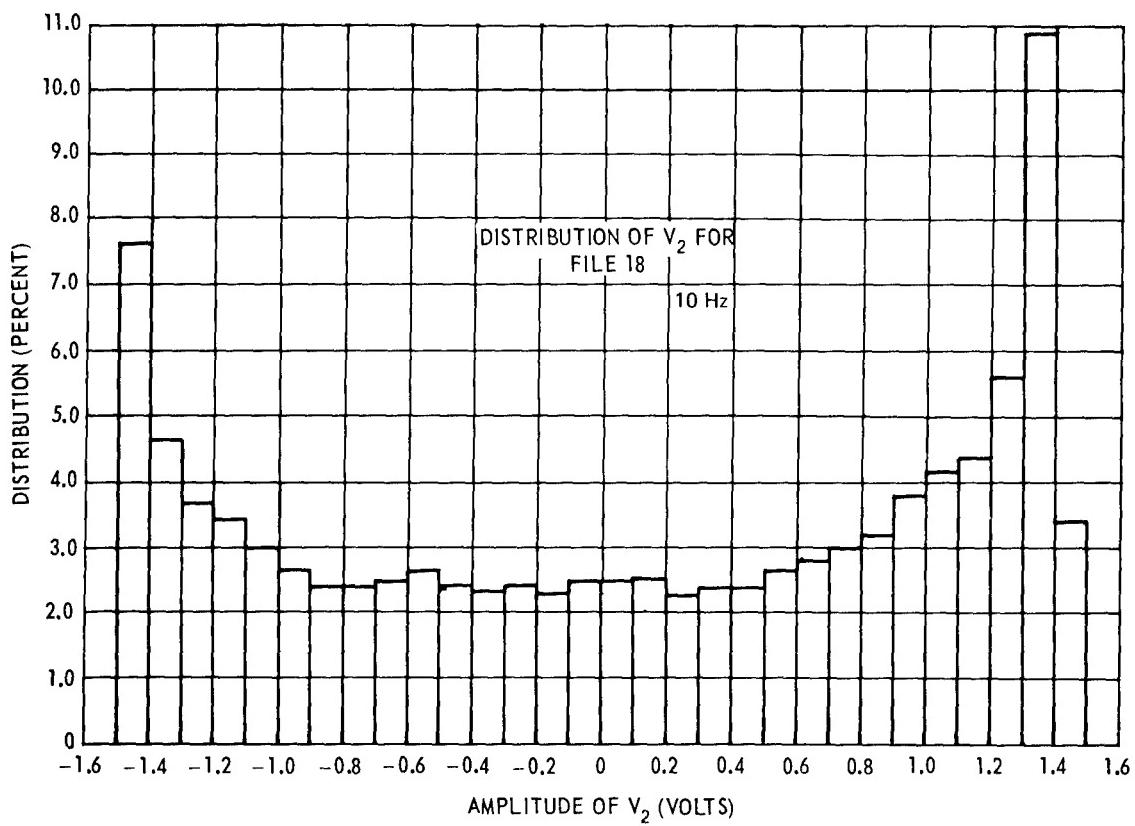
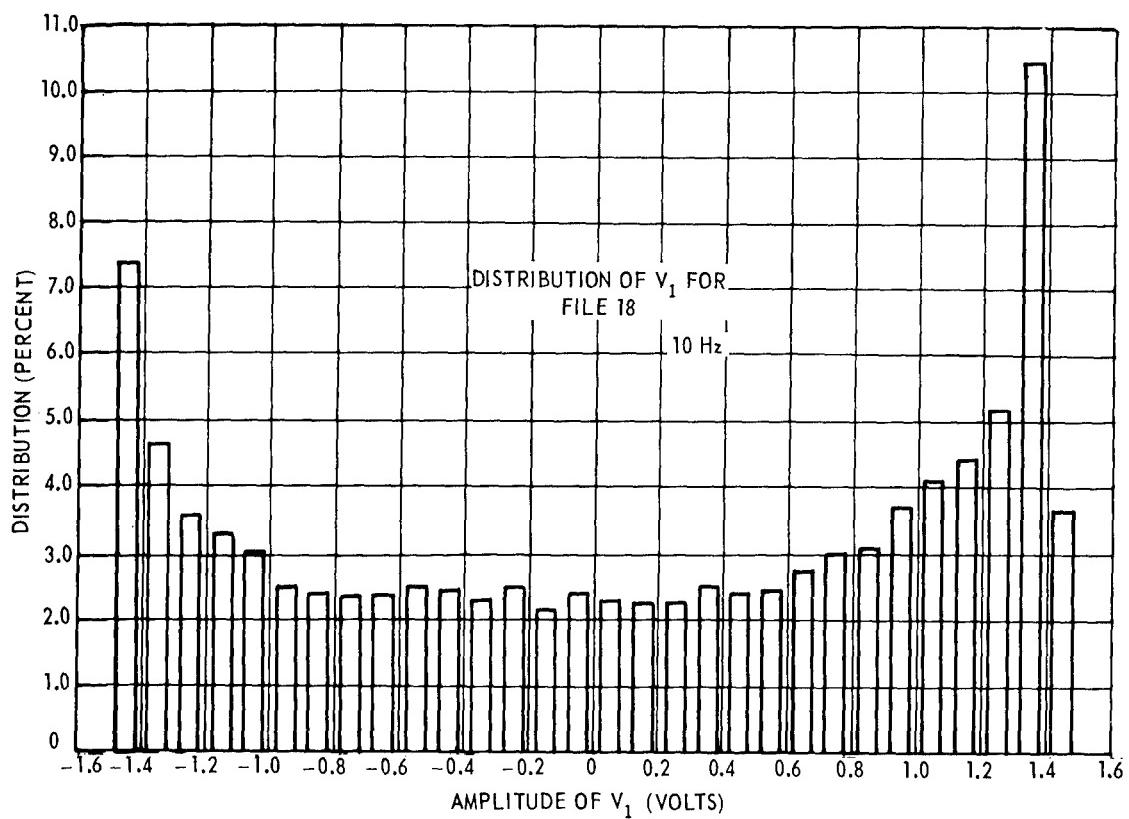
These voltages (3840 each) were then printed out. Each file was similarly unpacked and printed. During execution of the program, certain statistics are calculated for each file: the number of  $A, B$  pairs  $n_{A+B}$ ,  $\overline{A+B}$ ,  $\sigma_{A+B}$ ,  $\overline{v_1}$ ,  $\sigma_1$ ,  $3\sigma_1$ ,  $\overline{v_2}$ ,  $\sigma_2$ , and  $3\sigma_2$ . These statistics are also printed with the file voltage listings.

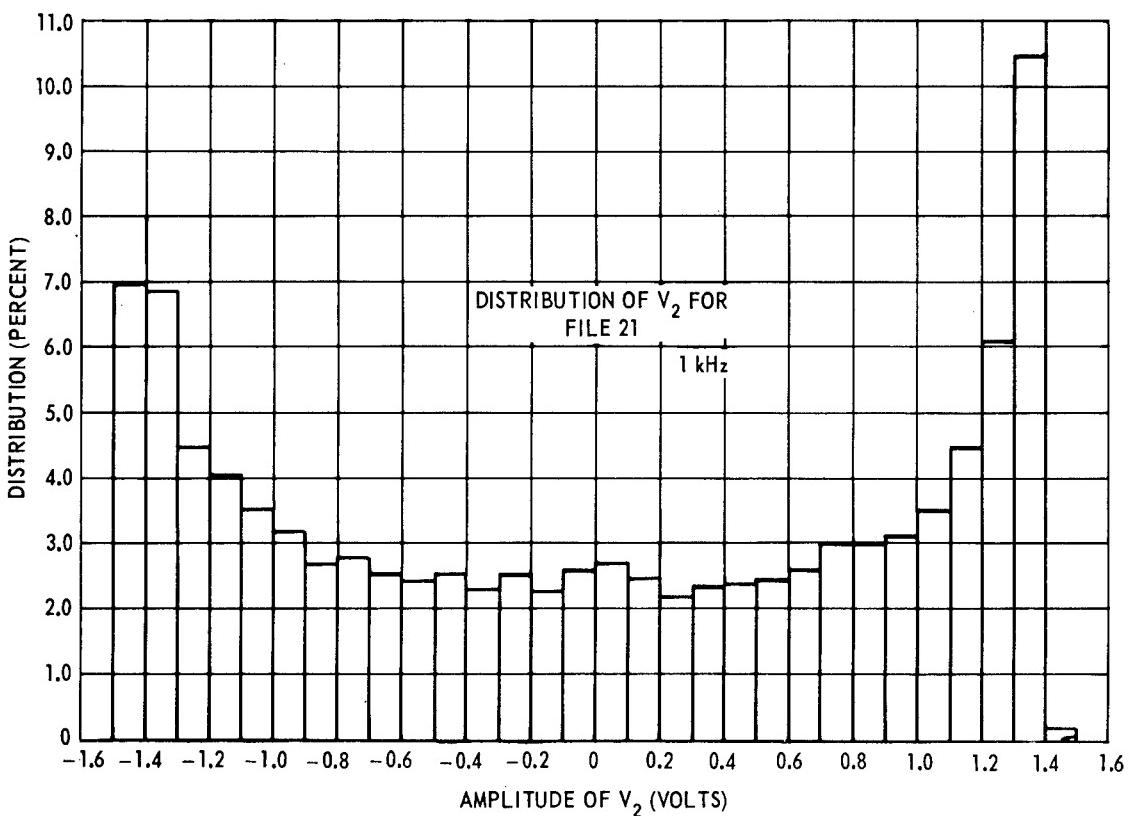
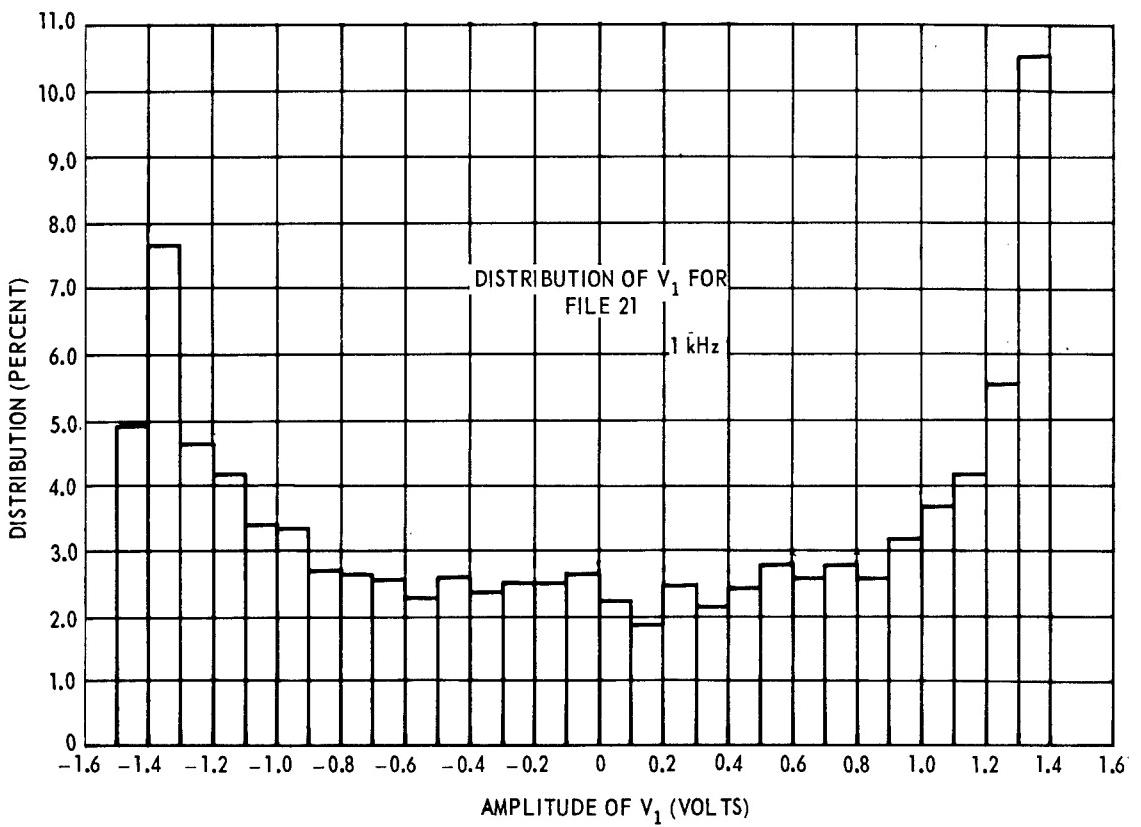
The following are printed for each record of data processed: (1) a message disclosing whether all, part, or none of the record was used in the voltage calculations and (2) the first eight 60-bit words of unpacked data. For each file processed, the printed output includes: (1) tabular listing of the  $A$ 's and  $B$ 's, (2) histogram of  $A + B$ , (3) mean and standard deviation of  $A + B$ , (4) tabular listing of  $v_1$ 's and  $v_2$ 's, and (5) means, standard deviations, and three standard deviations of  $v_1$  and  $v_2$ . These data are summarized on the last printed page.

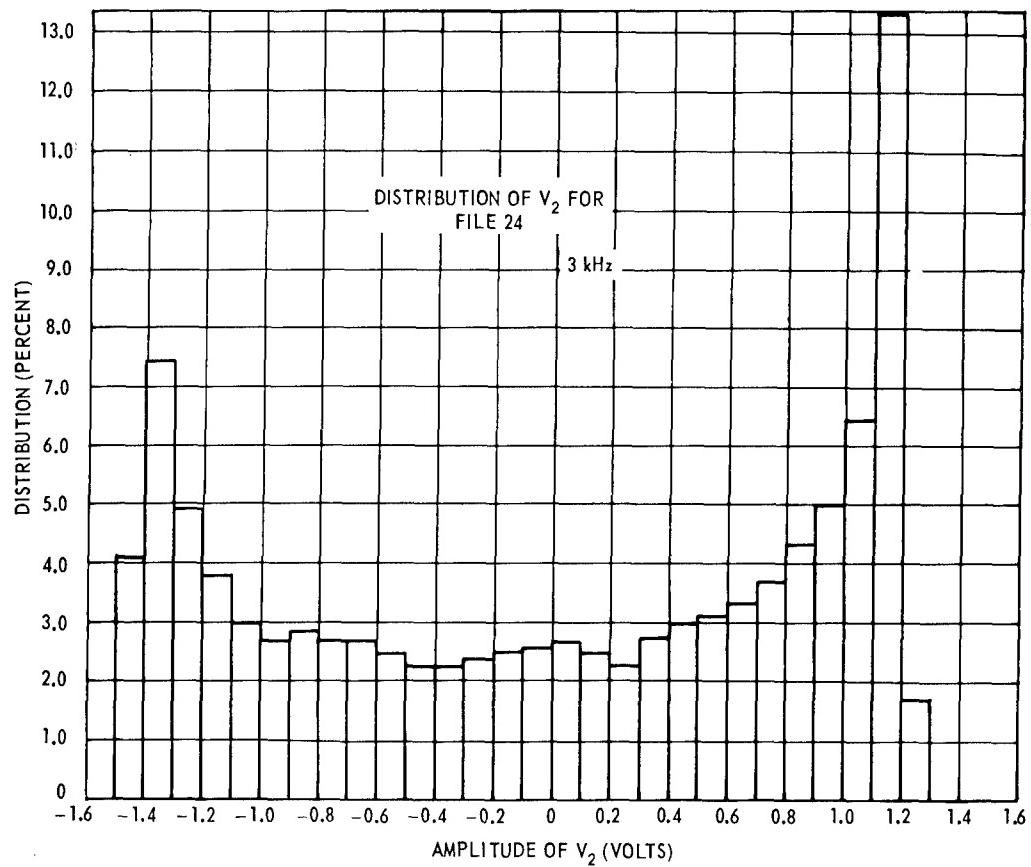
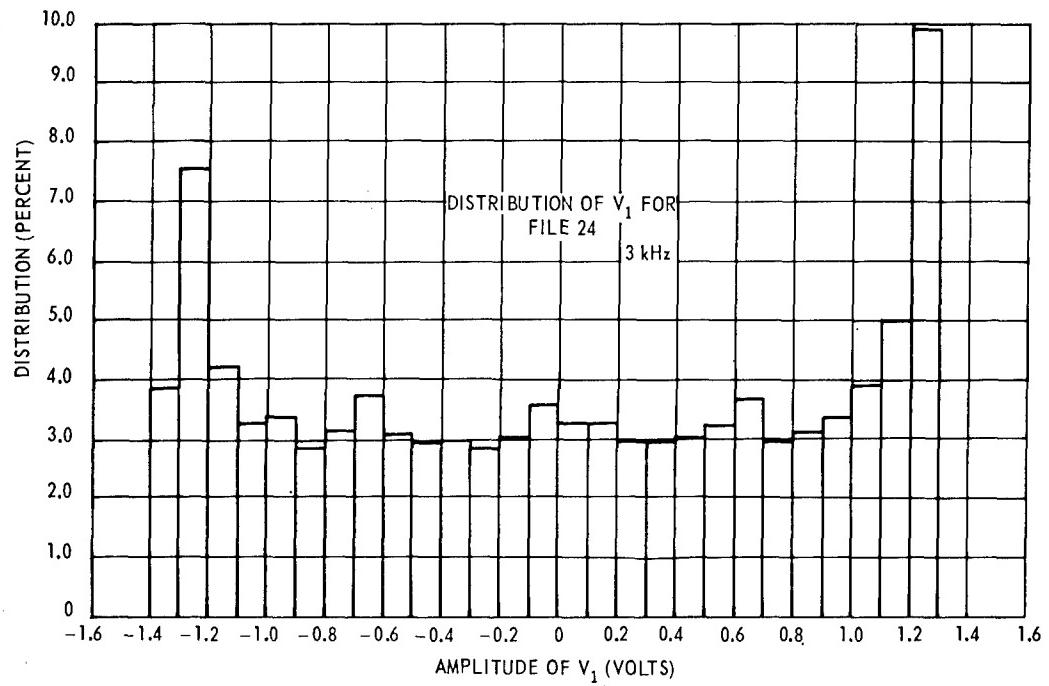
## APPENDIX C

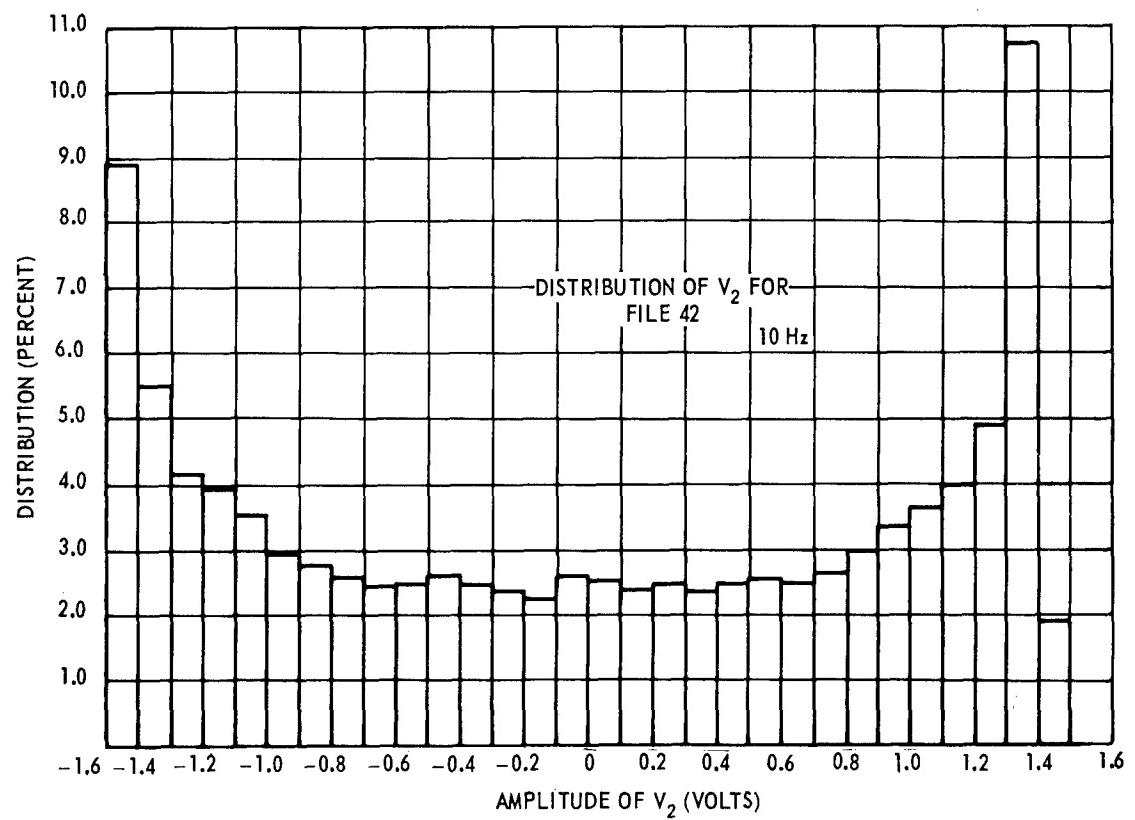
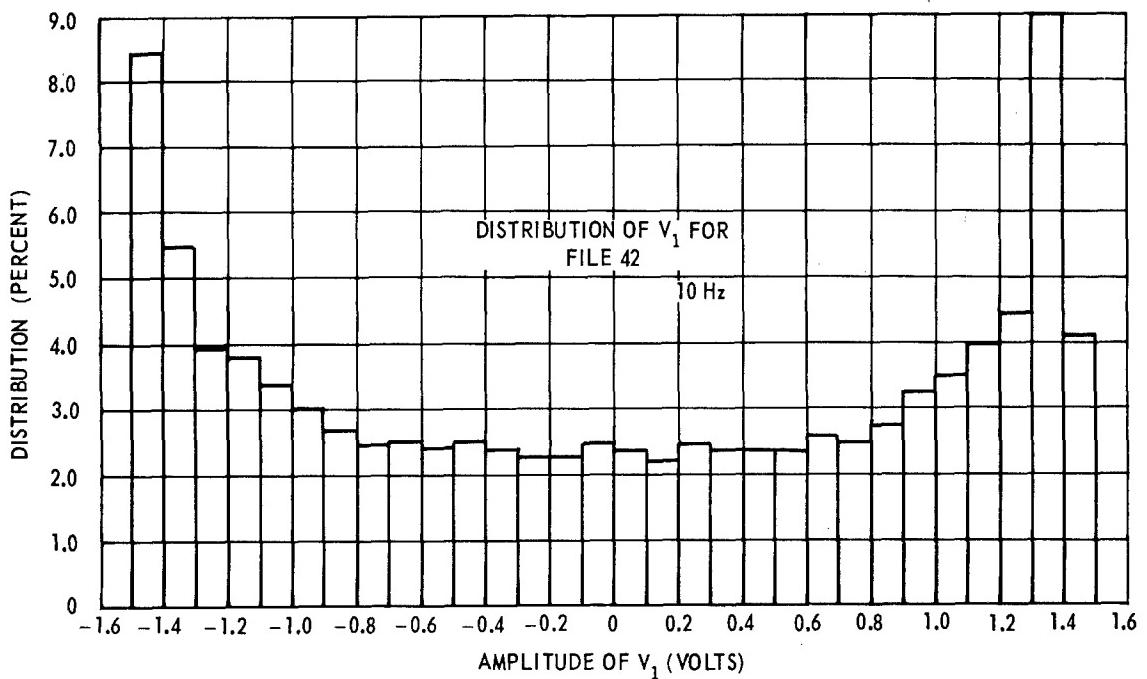
### DISTRIBUTIONS OF DYNAMIC DATA POINTS FROM DIGITAL EVALUATION BY CDC 6700 COMPUTER

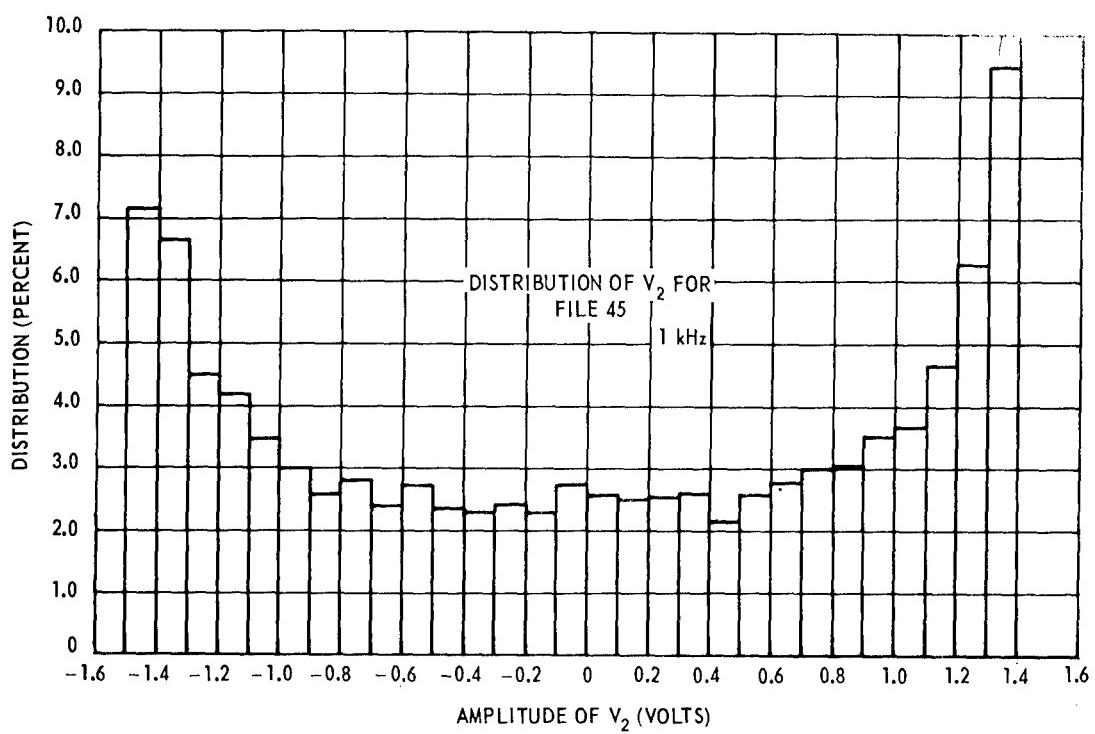
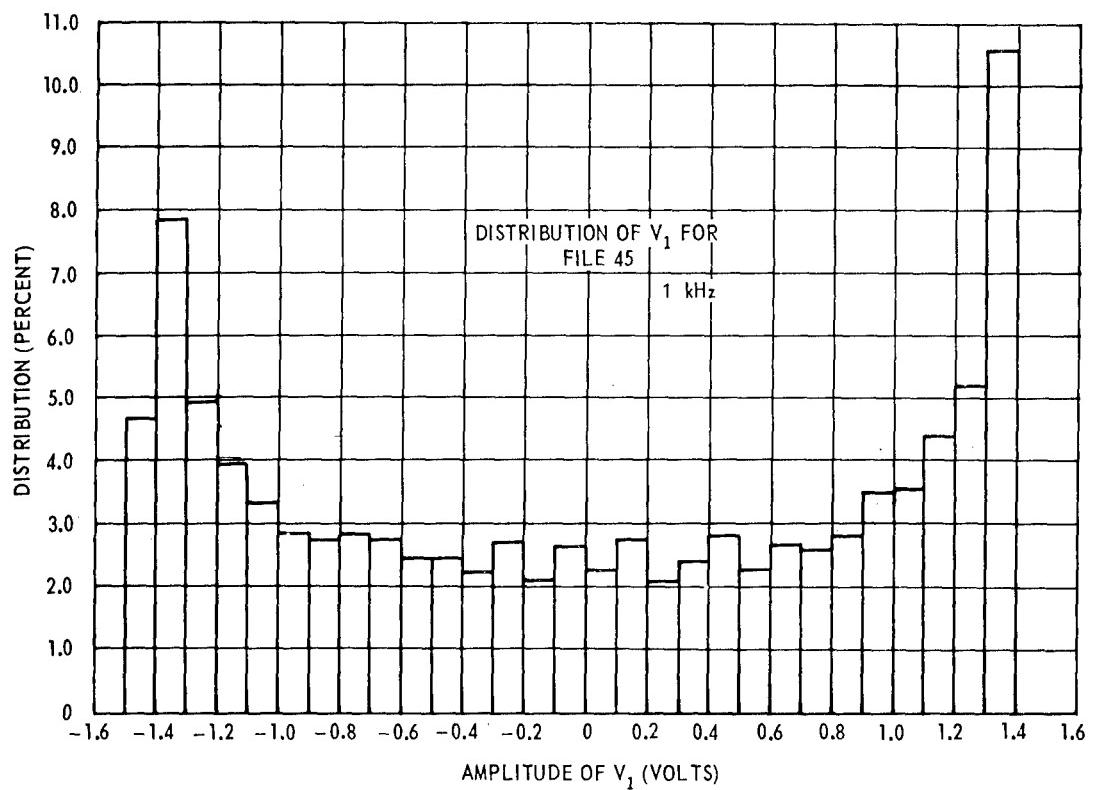
Distributions of dynamic data points from digital evaluation of files 18-24 and 42-48 by the CDC 6700 computer are shown on the following graphs. The distribution graphs are plotted from the TOTAL columns of the computer printout sheets. The slight distribution irregularity which can be noticed in many of the plots near  $-0.6\text{ V}$  correlates with the error curves of Figure 29.

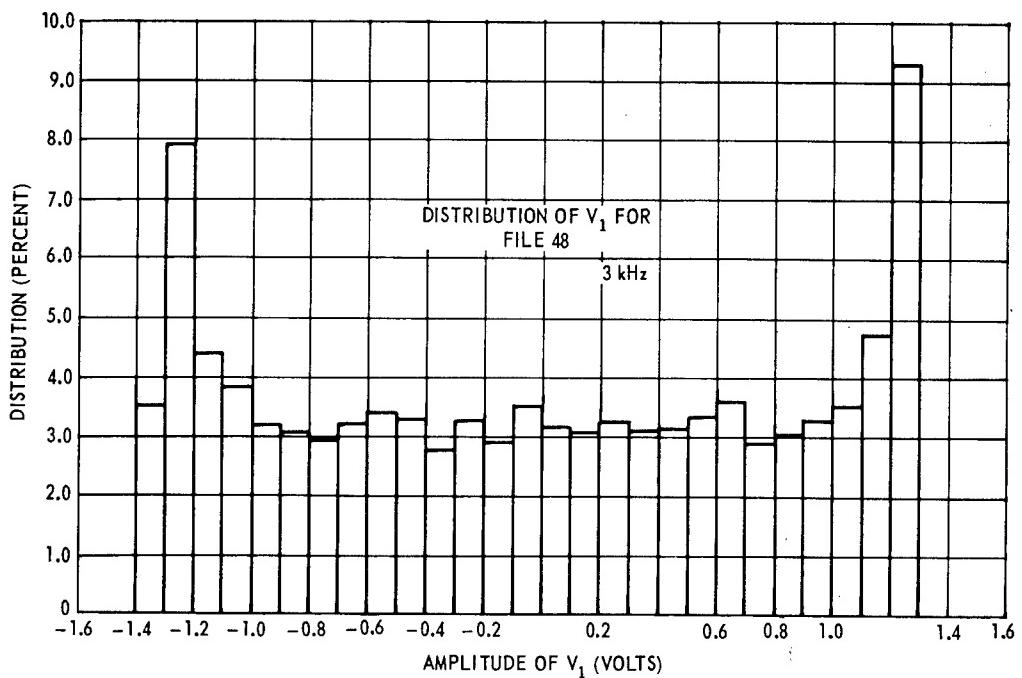












V1 FILE 18 TAPE DIT-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

INTERVALS	ENDPOINTS	TOTAL									
		.00	.01	.02	.03	.04	.05	.06	.07	.08	.09
-1.4 /	0.00003	5.30000	1.0417	.80729	1.64062	1.40625	1.09375	.80129	.75521	.736979	
-1.3 /	.57292	7.73112	.52183	.31250	.41667	.59896	.52683	.46042	.46875	.23637	4.60937
-1.2 /	.36458	.33633	.33854	.49479	.31250	.26042	.48271	.30963	.26042	.356771	3.56771
-1.1 /	.36458	.33854	.28646	.41667	.28646	.28646	.26142	.39063	.31250	3.25521	3.25521
-1.0 /	.33954	.28646	.33854	.31250	.28646	.20833	.41667	.18229	.23437	.41667	3.02083
- .9 /	.26042	.26042	.27437	.24337	.24337	.26042	.26042	.26042	.18229	.247396	2.47396
- .8 /	.26042	.29642	.19229	.26042	.15625	.29642	.13250	.20933	.13250	.36585	2.36979
- .7 /	.15625	.13250	.22433	.21233	.21233	.13250	.13250	.13250	.23437	.41667	2.34375
- .6 /	.19229	.20833	.26042	.23437	.16250	.20833	.10417	.38354	.23437	.23437	2.36979
- .5 /	.26042	.35442	.20833	.26042	.15625	.21250	.12083	.26042	.16250	.16250	2.47396
- .4 /	.28646	.14321	.13250	.28646	.15625	.28646	.10417	.28646	.13250	.26042	2.474792
- .3 /	.14321	.14321	.21933	.21933	.21933	.28646	.24337	.13250	.13250	.13250	2.29167
- .2 /	.21933	.26042	.26042	.26042	.15625	.26042	.15625	.26042	.15625	.26042	2.474792
- .1 /	.07912	.26042	.20833	.20833	.20833	.26042	.23437	.13250	.13250	.13250	2.474792
- .0 /	.26042	.28646	.28646	.28646	.20833	.18229	.18229	.18229	.18229	.18229	2.39583
C.0 /											
.1 /	.15625	.15625	.28646	.31250	.13250	.13250	.13250	.21833	.20933	.26042	2.13542
.2 /	.20833	.26042	.23437	.13021	.23437	.23437	.23437	.15625	.15625	.23437	2.23958
.3 /	.31250	.23437	.27437	.26042	.23437	.20833	.20833	.23437	.07812	.29167	2.29167
.4 /	.23437	.15625	.15625	.26042	.20833	.36458	.10417	.26042	.26042	.31250	2.47396
.5 /	.15625	.14321	.18229	.31250	.18229	.28646	.20833	.20833	.28646	.28646	2.39583
.6 /	.21933	.14321	.14321	.21933	.14321	.21933	.10417	.21933	.21933	.242187	2.42187
.7 /	.31250	.21933	.33854	.31250	.21933	.19229	.19229	.19229	.33854	.33854	2.78646
.8 /	.45642	.21933	.37854	.26042	.26042	.26042	.39063	.39063	.44271	.26042	2.99479
.9 /	.54687	.28646	.28646	.26042	.28646	.28646	.39063	.39063	.18229	.18229	3.09896
1.0 /	.36453	.49479	.36453	.49479	.49479	.49479	.52071	.52071	.31250	.33854	3.72356
1.1 /	.49479	.49479	.49479	.49479	.49479	.49479	.52071	.52071	.39063	.41667	4.08854
1.2 /	.49479	.49479	.54687	.54687	.54687	.54687	.52071	.52071	.49479	.49479	4.40104
1.3 /	.62551	.62551	.72917	.72917	.72917	.58986	.52071	.52071	.56887	.56887	10.49479
1.4 /	2.31171	1.30358	.07812	0.00000	0.00000	0.00000	1.22396	1.22396	1.51042	1.69371	0.00000

TOTAL DATA POINTS = 38

V2 FILE 18 TAPE DTT-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

V1 FILE 19 TAPE DIT-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

INTERVAL MIDPOINTS	.09	.08	.07	.06	.05	.04	.03	.02	.01	.00	TOTAL
-1.4 /	0.00000	0.00000	0.02604	0.07229	1.61458	1.05000	1.32812	1.25000	7.125	83333	8.51562
-1.3 /	-.72917	.78125	.44271	.80729	.52083	.46875	.57292	.49479	.46675	.59896	5.88542
-1.2 /	.31250	.39063	.33854	.52083	.36458	.39063	.39063	.52083	.46675	4.03646	
-1.1 /	.31250	.54687	.36458	.39063	.46875	.36458	.31250	.33854	.39063	.33854	3.82812
-1.0 /	.44271	.36458	.34458	.26042	.44271	.33854	.31250	.33854	.39063	.33854	3.59375
-0.9 /	.25833	.33854	.31155	.28646	.39063	.36458	.28646	.23437	.28646	.39063	3.02083
-0.8 /	.18229	.26042	.31250	.18229	.31250	.23437	.18229	.31250	.39063	.26042	2.65625
-0.7 /	.18229	.24833	.44271	.18229	.26042	.28646	.28646	.23437	.28646	.10417	2.36979
-0.6 /	.31155	.23437	.31155	.23437	.26042	.13021	.36458	.13021	.14229	.20833	2.57812
-0.5 /	.18229	.26042	.31250	.28646	.28646	.20833	.23437	.23437	.20833	.244792	
-0.4 /	.28646	.36458	.20833	.20833	.20833	.31250	.28646	.23437	.28646	.31250	2.55208
-0.3 /	.24833	.19417	.21546	.18229	.26042	.28646	.28646	.23437	.18229	.18229	2.26562
-0.2 /	.13621	.31250	.24833	.26042	.18229	.13021	.26042	.23437	.18229	.26042	2.21354
-0.1 /	.23437	.23437	.24833	.26042	.26042	.13021	.26042	.23437	.20833	.15625	2.31771
-0.0 /	.29696	.13229	.20566	.13021	.33854	.15625	.23437	.23437	.20833	.44271	2.63021
00											
0.0		.01	.02	.03	.04	.05	.06	.07	.08	.19	TOTAL
0.1		.31250	.24347	.20833	.18229	.13021	.13021	.13021	.13021	.20833	1.95312
0.2		.23437	.28646	.15625	.13021	.13021	.23437	.15625	.23437	.26042	2.23958
0.3		.18229	.31250	.33854	.28646	.28646	.18229	.28646	.23437	.20833	2.44792
0.4		.29696	.31250	.28646	.28646	.20833	.31250	.23437	.15625	.28646	2.26562
0.5		.18229	.26042	.25646	.28646	.18229	.39063	.23437	.15625	.15625	2.39583
0.6		.15625	.31250	.23437	.28646	.23437	.13021	.13021	.23437	.20833	2.44792
0.7		.13621	.41667	.31250	.23437	.23437	.23437	.28646	.26042	.26042	2.52604
0.8		.20833	.23437	.15625	.15625	.28646	.28646	.26042	.23437	.52033	2.65625
0.9		.26042	.31250	.31250	.41667	.41667	.31250	.33854	.23437	.26042	2.73437
1.0		.31250	.31250	.31250	.41667	.41667	.33854	.15625	.33854	.41667	3.09896
1.1		.39063	.36458	.26042	.26042	.39063	.52033	.26042	.36458	.31250	3.56771
1.2		.44271	.49479	.26042	.57292	.36458	.39063	.44271	.26042	.28646	3.77604
1.3		.59896	.44797	.41667	.41667	.41667	.46875	.28646	.46875	.41667	4.55729
1.4		1.71875	1.27604	.33854	0.00000	0.00000	0.00000	1.38021	1.17188	1.53646	9.08654
02										0.00000	0.00000

TOTAL DATA POINTS = 3840

V2 FILE 19 TAPE OUT-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

INTERVAL MIDPOINTS	.09	.08	.07	.06	.05	.04	.03	.02	.01	.00	TOTAL
-1.4 /	0.00003	0.00000	0.20901	0.64602	1.76080	1.6945	1.5909	1.25651	0.00000	0.00000	0.00000
-1.3 /	0.85232	5.1514	6.31953	7.77623	5.08356	5.0704	5.8704	5.3372	4.2087	5.93452	6.76343
-1.2 /	3.9876	5.0496	3.7340	3.2606	4.0012	5.505	4.0000	3.2606	3.9944	4.16762	4.11011
-1.1 /	5.0293	3.2674	5.5333	4.3088	3.7679	3.7408	3.9816	3.7340	5.2663	5.2965	3.40068
-1.0 /	3.7611	3.32199	4.65220	2.9324	3.3761	2.9323	4.5085	2.9527	3.3740	3.1793	3.25575
- .9 /	2.9183	3.4736	4.2688	2.4183	4.7832	3.2199	2.6632	3.4600	3.1793	2.1851	2.77751
- .8 /	3.1725	2.6724	1.9111	2.3912	2.9460	2.6652	2.3912	1.6439	4.7892	3.1920	1.6235
- .7 /	1.6151	3.4397	1.6773	1.8734	3.9605	2.4183	3.1880	1.6235	2.6188	2.1105	2.45755
- .6 /	1.9643	2.9256	2.6855	2.1444	4.2345	1.6568	2.1783	2.1444	2.6516	2.4251	2.51506
- .5 /	3.4329	2.15779	3.2164	3.9808	1.0959	2.1444	2.9256	1.8975	1.6168	3.7001	2.61583
- .4 /	3.1996	3.4664	1.6168	2.6584	2.9188	1.6439	1.0959	3.7069	3.9605	1.6439	2.59250
- .3 /	1.8975	3.4864	1.0500	1.6171	2.9121	1.8937	2.1579	3.9537	3.1980	3.4329	2.40275
- .2 /	1.5964	2.4251	2.1376	2.1512	3.1725	1.6371	1.6235	3.6933	1.3699	2.27119	2.27119
- .1 /	1.6235	1.3631	4.24643	1.8907	2.9460	1.8568	2.6720	1.3631	2.3980	2.9053	2.32598
-0.0 /	1.6371	2.1368	2.9392	3.1589	1.9111	2.9053	2.3980	3.1996	1.6439	0.00000	2.51641
0.0 /	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000
0.1 /	4.4881	16.58	34.329	16.00	16.642	21.12	21.240	21.240	21.240	21.512	13.553
0.2 /	2.6955	18.975	16.032	31.589	28.985	11.298	18.840	26.449	18.840	26.720	2.45582
0.3 /	2.6652	4.4610	1.9111	1.1163	2.3980	2.6516	2.7244	2.9256	1.8772	2.1512	2.45403
0.4 /	2.6516	2.3912	2.3912	2.4183	2.3980	2.1647	3.1880	2.3980	1.8840	1.8840	1.6032
0.5 /	2.1573	2.1548	2.21647	3.1725	4.2684	4.2684	1.8840	2.1240	2.9256	2.40614	2.40614
0.6 /	1.8974	2.1512	2.688	2.9256	2.4183	3.2777	3.4261	3.4261	1.8772	2.9888	2.45958
0.7 /	2.8985	1.6710	2.1647	3.2264	2.1444	1.6235	2.9152	1.6235	2.1512	2.51370	2.51370
0.8 /	2.1376	2.9527	2.1240	2.6788	3.2332	2.4116	3.4261	3.002	3.4465	2.80355	2.80355
0.9 /	1.8975	2.1240	3.2264	3.4465	1.6975	3.4668	3.7340	2.9256	3.4664	2.9324	2.90975
1.0 /	3.4668	3.2264	3.7242	2.9460	3.7543	2.3984	2.6923	2.3984	3.1860	3.7543	3.04945
1.1 /	4.2684	2.6991	3.1337	4.2752	5.2110	2.2190	3.4532	5.5637	3.4804	2.4251	3.66062
1.2 /	3.7611	3.9944	3.1996	4.0554	4.2616	5.5705	4.2820	3.7137	2.7195	4.2443	4.63063
1.3 /	5.6247	6.4331	4.2730	4.5223	4.2752	5.8512	5.5433	4.3704	7.1533	4.45356	4.70201
1.4 /	9.5622	1.15302	6.68987	7.4476	8.0363	8.841	1.2524	8.1719	1.5302	1.49835	9.53356
			3.37340	4.6875	0.7812	0.00000	0.02614	0.00000	0.00000	0.00000	3.05556

TOTAL DATA PCINTS = 3840

V1 FILE 20 TAPE DIT-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

INTERVAL MIDPOINTS	.09	.08	.07	.06	.05	.04	.03	.02	.01	.00	TOTAL
-1.4 /	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	7.33958
-1.3 /	.88542	.49479	.62500	.65104	.46075	.70312	.33854	.66104	.49479	.614583	
-1.2 /	.46875	.46875	.62500	.39163	.44271	.30563	.30563	.28646	.54687	.4.32292	
-1.1 /	.39063	.44271	.39063	.44271	.46875	.41667	.57292	.20833	.36458	.4.03646	
-1.0 /	.28646	.41667	.36458	.21250	.26042	.33854	.49079	.28646	.39063	.3.54167	
- .9 /	.26142	.26142	.13121	.39063	.26042	.28646	.41667	.28646	.23437	.2.34347	
- .8 /	.39063	.31250	.23437	.20833	.37854	.28646	.36458	.36458	.13021	.15625	.2.78646
- .7 /	.28646	.44271	.23437	.10417	.39063	.33054	.20833	.20833	.13021	.23437	.2.63021
- .6 /	.33054	.13021	.20833	.28646	.23437	.20833	.26042	.31250	.23437	.33054	.2.55208
- .5 /	.18229	.28646	.23437	.33054	.15625	.39063	.18229	.31250	.33054	.20833	.2.63021
- .4 /	.07812	.13229	.31250	.41667	.18229	.21437	.13021	.13021	.18229	.13021	.2.23354
- .3 /	.20833	.35237	.35237	.13021	.13021	.39063	.36458	.36458	.18229	.20833	.2.4792
- .2 /	.20833	.46875	.23437	.20833	.26042	.18229	.13021	.18229	.31250	.39063	.2.57812
- .1 /	.16625	.13229	.10417	.28646	.33054	.15625	.15625	.15625	.13021	.20833	.1.95312
-0.0 /	.26042	.15625	.28646	.33054	.18229	.26042	.18229	.18229	.23437	.44271	.2.50000
0.0											
*0.0 /											
*.1 /	.36458	.23437	.20833	.13021	.18229	.28646	.31250	.23437	.18229	.28646	.2.3542
*.2 /	.31250	.15625	.20833	.18229	.28646	.15625	.26042	.23437	.26042	.23437	
*.3 /	.23437	.05237	.13021	.33054	.23437	.20833	.15625	.20833	.41667	.20833	.2.66979
*.4 /	.15625	.23437	.13021	.28646	.23437	.20833	.15625	.15625	.28646	.28646	.2.43375
*.5 /	.39063	.23437	.20833	.15625	.26042	.31250	.20833	.20833	.07812	.07812	.2.13542
*.6 /	.26042	.20833	.20833	.26042	.20833	.31250	.26042	.26042	.23437	.23437	.2.8646
*.7 /	.28646	.15625	.15625	.20833	.31250	.28646	.18229	.18229	.13021	.41667	.2.57812
*.8 /	.33054	.39063	.23437	.20833	.31250	.28646	.33054	.33054	.28646	.28646	.2.4792
*.9 /	.21333	.15625	.13021	.23437	.20833	.39063	.49479	.49479	.20833	.33054	.3.15104
*1.0 /	.33054	.26042	.49479	.46875	.23437	.28646	.39063	.39063	.28646	.36458	.3.12500
*1.1 /	.33054	.44271	.46875	.31250	.26042	.33054	.41667	.41667	.26042	.36458	.3.47750
*1.2 /	.26042	.31250	.54687	.44271	.46875	.28646	.44271	.44271	.54687	.44271	.4.03646
*1.3 /	.57292	.67708	.71312	.91146	.88542	.54687	.41667	.57292	.52383	.54687	.4.71354
*1.4 /	1.40625	.21333	.026042	.0.00000	0.00000	0.00000	*91146	.0.00000	1.82292	1.82292	1.64062

TOTAL DATA POINTS = 3840

V2 FILE 20 TAPE DTT-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

TOTAL DATA POINTS = 3840

V1 FILE 21 TAPE OTT-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

TOTAL DATA POINTS = 3840

V2 FILE 21 TAPE 011-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

TOTAL DATA POINTS = 3840

V1 FILE 22 TAPE DIT-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

INTERVAL  
MIDPOINTS

		.07	.06	.05	.04	.03	.02	.01	.00	TOTAL
-1.4 /	0.00303	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.0417
-1.3 /	0.62503	1.71875	1.95313	1.35417	0.93750	0.72917	0.60729	0.5104	0.5203	5.9096
-1.2 /	0.57292	0.57292	*39063	*44271	*49479	*39063	*44271	*36458	*41667	9.89583
-1.1 /	0.39363	0.46875	0.46875	0.59896	0.36458	0.36458	*46875	*46875	0.455729	4.32292
-1.0 /	0.31250	0.57292	0.33854	0.46875	0.52083	0.28646	0.31250	*57292	0.36458	0.36458
-0.9 /	0.36458	0.18229	0.39163	0.36458	0.26042	0.23437	*28646	*20833	*28646	3.80021
-0.8 /	0.26042	0.26042	0.33854	0.26042	0.44271	0.3854	*41667	*33854	*28646	3.20125
-0.7 /	0.36458	0.1250	0.15625	0.39063	0.28646	0.20833	*28646	*39063	*3854	3.04687
-0.6 /	0.36458	0.33854	0.1250	0.23437	0.33854	0.39063	*39063	*33854	*33854	3.07292
-0.5 /	0.33854	0.15625	0.15625	0.1250	0.15625	0.23437	*26042	*23437	*23437	2.88229
-0.4 /	0.39063	0.23437	0.26042	0.28646	0.26042	0.13021	*36458	*26042	*26042	2.57812
-0.3 /	0.49479	0.18229	0.26042	0.28646	0.20833	0.18229	*18229	*15625	*23437	2.60417
-0.2 /	0.26042	0.15625	0.24737	0.26042	0.26042	0.28646	*26042	*26042	*26042	2.73437
-0.1 /	0.31250	0.31250	0.23437	0.20833	0.20833	0.39063	*31250	*23437	*23437	2.55258
-0.0 /	0.31250	0.23437	0.18229	0.18229	0.18229	0.18229	*18229	*18229	*18229	2.52604
0.0 /	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000
0.1 /	0.26042	0.26042	0.23437	0.20833	0.18229	0.16667	*18229	*26042	*26042	2.0833
0.2 /	0.23437	0.26042	0.26042	0.23437	0.23437	0.23437	0.13021	0.31250	0.31250	2.52604
0.3 /	0.23437	0.23437	0.39063	0.26042	0.26042	0.26042	0.26042	0.31250	0.31250	2.57812
0.4 /	0.18229	0.28646	0.28646	0.20833	0.20833	0.20833	0.20833	0.39063	0.39063	2.447792
0.5 /	0.18229	0.15625	0.15625	0.31250	0.31250	0.26042	0.26042	0.13021	0.13021	2.65625
0.6 /	0.20833	0.36458	0.23437	0.3854	0.3854	0.20833	0.20833	0.26042	0.26042	2.34375
0.7 /	0.18229	0.36458	0.26042	0.23437	0.23437	0.23437	0.18229	0.31250	0.31250	2.65625
0.8 /	0.41667	0.26042	0.36458	0.20833	0.20833	0.20833	0.20833	0.23437	0.23437	2.68229
0.9 /	0.23437	0.31250	0.23437	0.36458	0.41667	0.23437	0.23437	0.33854	0.33854	2.41667
1.0 /	0.65104	0.18229	0.44271	0.36458	0.28646	0.49479	0.49479	0.26042	0.26042	3.28125
1.1 /	0.62503	0.36458	0.50083	0.59896	0.54687	0.41667	0.41667	0.49479	0.49479	4.14062
1.2 /	0.52083	0.56497	0.31250	0.57292	0.52083	0.52083	0.52083	0.5104	0.5104	5.00000
1.3 /	0.85729	0.78125	1.22596	1.77783	2.08333	0.67708	0.10417	0.00000	0.00000	6.04167
1.4 /	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	7.44792

TOTAL DATA PCINTS = 3840

V2 FILE 22 TAPE DTI-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

INTERVAL MIDPOINTS	.09	.08	.07	.06	.05	.04	.03	.02	.01	.00	TOTAL
-1.4 / 0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	6.20063
-1.3 / .84961	*90413	1.04045	*45193	*54525	*77420	*69811	*38154	*32606	*71872	*6.69060	
-1.2 / .43159	*60913	*42620	*42601	*4L351	*42684	*48028	*29595	*42752	*58512	*4.61993	
-1.1 / .37436	*58512	*42987	*27601	*42616	*29595	*35075	*27662	*40012	*40012	*3.01049	
-1.0 / .32256	*45554	*37340	*27262	*48231	*29392	*39876	*30138	*37001	*31996	*3.53855	
-0.9 / .34844	*36933	*29663	*37408	*18840	*39808	*21918	*37340	*21579	*29731	*3.08024	
-0.8 / .31933	*31928	*29527	*31928	*21986	*24319	*31928	*26232	*31861	*29460	*2.91769	
-0.7 / .29595	*24251	*23844	*32267	*31996	*26584	*08558	*27559	*34736	*19111	*2.90022	
-0.6 / .24387	*32132	*151521	*29556	*24319	*32267	*18840	*29124	*26516	*18840	*2.67402	
-0.5 / .26923	*16332	*19043	*23844	*39994	*19043	*31589	*21183	*18907	*29168	*2.46297	
-0.4 / .32267	*21512	*21571	*29324	*21579	*21579	*31725	*18656	*24348	*13835	*2.15946	
-0.3 / .24726	*29121	*16303	*26788	*31733	*23912	*16371	*34312	*16303	*26855	*2.46704	
-0.2 / .24116	*23844	*16235	*31928	*34532	*37476	*24251	*18640	*21308	*26720	*2.52550	
-0.1 / .16433	*24251	*18840	*29188	*16032	*26991	*34465	*19111	*21677	*21647	*2.33141	
-0.0 / .19043	*31864	*18704	*26516	*24251	*18433	*18840	*24468	*24163	*66257	*2.212135	
0.0 /											
0.1 /											
0.2 / .26720	*14928	*37076	*21918	*18704	*24116	*13631	*21647	*21647	*16168	*1.88666	
0.3 / .18843	*31928	*32199	*29380	*24048	*26652	*18568	*29460	*19043	*18772	*2.53373	
0.4 / .24389	*23940	*16704	*31928	*18937	*18704	*29324	*16507	*27059	*26720	*2.0672	
0.5 / .13496	*13631	*27059	*16642	*24116	*18636	*29460	*29121	*33777	*31657	*2.36146	
0.6 / .31735	*21579	*24251	*21647	*21647	*24116	*03147	*3934	*29324	*32324	*2.55667	
0.7 / .26544	*29595	*26652	*19704	*24387	*29527	*29053	*31657	*34125	*32364	*2.87354	
0.8 / .29121	*24387	*24387	*31732	*37340	*47485	*31725	*34736	*42451	*26652	*2.90771	
0.9 / .21444	*42480	*31928	*29595	*23844	*37815	*34465	*29327	*34668	*44813	*3.99902	
1.0 / .22519	*42593	*57766	*37408	*45017	*53372	*45336	*34665	*42752	*42752	*3.370C9	
1.1 / .43244	*45220	*58648	*38618	*53317	*53372	*45336	*63117	*71601	*63924	*4.14468	
1.2 / .79441	*53507	*64147	*71604	*1.02949	*74273	*1.24091	*1.00593	*1.21694	*1.66165	*5.36458	
1.3 / .58382	1.66197	1.J9958	*44674	*13238	*14784	*02875	*0.00000	*0.00000	*0.00000	*9.9275	
1.4 / 0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	

TOTAL DATA POINTS = 3840

V1 FILE 23 TAPE DIT-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

TOTAL DATA PCINTS = 3840

V2 FILE 23 TAPE DTT-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

V1 FILE 24 TAPE OTT-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

TOTAL DATA POINTS = 3843

V2 FILE 24 TAPE DTI-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

TOTAL DATA POINTS = 3643

V1 FILE 42 TAPE DTT-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

TOTAL DATA POINTS = 3849

V2 FILE 42 TAPE DTT-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

INTERVAL MIDPOINTS	.09	.08	.07	.06	.05	.04	.03	.02	.01	.00	TOTAL
-1.4 / C.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.09160
-1.3 / •53204	•56519	•69268	•8362	1.62177	1.98337	1.0246	1.19900	0.91167	1.00083	0.26788	5.50700
-1.2 / •50544	•50836	•39741	•53711	•66393	•59055	•58580	•48435	•58848	•42880	•42880	4.16453
-1.1 / •34871	•37476	•45355	•42684	•27059	•55773	•40148	•45492	•35007	•35007	•47757	3.92714
-1.0 / •27559	•26855	•37272	•42443	•34871	•37340	•42548	•37476	•37001	•34939	•29392	3.52634
- .9 / •34339	•24251	•34600	•2672J	•35143	•35143	•37476	•31928	•34600	•34804	•29324	4.0085
- .8 / •29324	•29527	•24048	•29556	•34532	•24319	•2483	•21308	•21737	•21444	•21444	2.94325
- .7 / •24833	•26449	•18907	•18772	•21246	•42752	•19636	•34736	•26788	•29121	•29121	2.61583
- .6 / •21715	•23912	•13975	•29118	•29460	•18568	•26555	•26584	•31928	•16714	•16714	2.43557
- .5 / •34997	•24251	•29188	•13563	•29392	•29188	•21579	•21358	•24043	•21512	•21512	2.48427
- .4 / •39108	•21376	•21579	•18636	•34532	•16303	•26720	•16303	•31860	•21715	•21715	2.59047
- .3 / •29352	•21338	•16163	•29052	•21444	•24048	•18744	•31753	•29595	•23980	•23980	2.48582
- .2 / •29153	•16439	•18840	•29256	•18840	•39673	•21647	•08219	•21240	•34465	•34465	2.37671
- .1 / •21376	•18975	•2198J	•26514	•13496	•26788	•18636	•26652	•24116	•26652	•26652	2.27254
-0.0 / •21512	•21338	•21376	•26720	•31360	•16168	•16303	•28985	•16371	•58445	•58445	2.59047
	.00	.01	.02	.03	.04	.05	.06	.07	.08	.09	TOTAL
0.0 / •29121	•18636	•21444	•16168	•26449	•21579	•26516	•18772	•21444	•21444	•21444	1.97795
.1 / •19314	•24148	•29324	•18423	•23912	•37204	•15563	•26584	•36933	•11027	•11027	2.40139
.2 / •24149	•26445	•21116	•2318J	•21647	•34329	•21579	•23980	•21512	•31928	•31928	2.48630
.3 / •13699	•23980	•16371	•26584	•26584	•13902	•31725	•23912	•26788	•24048	•24048	2.37942
.4 / •24483	•39655	•16235	•32164	•26381	•21512	•32132	•26720	•23980	•16772	•16772	2.51099
.5 / •29356	•21105	•31028	•13767	•2398J	•31589	•32267	•18840	•31793	•256307	•256307	1.97795
.6 / •29113	•26991	•13496	•26652	•31936	•31928	•10959	•34600	•23912	•21115	•21115	2.51506
.7 / •34432	•21579	•2398J	•3186J	•26584	•26855	•32199	•21240	•21444	•29192	•29192	2.64323
.8 / •26855	•29527	•21579	•18704	•29392	•39944	•24116	•32064	•29188	•33808	•33808	2.98655
.9 / •21115	•31996	•42752	•34804	•19043	•37340	•44746	•24590	•26855	•46889	•46889	3.36670
1.0 / •32199	•42752	•47824	•34336	•19111	•4248J	•39944	•42684	•37543	•39008	•39008	3.65858
1.1 / •47553	•37408	•45424	•29799	•52829	•40012	•40080	•35211	•50173	•29867	•29867	4.01010
1.2 / •66528	•48887	•48435	•50768	•45288	•32335	•5841	•45627	•53372	•68861	•68861	4.93368
1.3 / •1.09728	•74983	•58919	•7137J	•1.13742	•90644	•1.31452	•1.51218	•1.51218	•1.53182	•1.53182	10.72875
1.4 / •69444	•0.08626	•0.02614	•0.02614	•0.02614	•0.00000	•0.00000	•0.00000	•0.00000	•0.00000	•0.00000	1.90403

TOTAL DATA POINTS = 3844

V1 FILE 43 TAPE DT-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

INTERVAL	MIDPOINTS	.09	.08	.07	.06	.05	.04	.03	.02	.01	.00	TOTAL
-1.4 /	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.48956
-1.3 /	0.75521	0.72917	0.69708	0.65208	0.60283	0.55208	0.50208	0.45208	0.40208	0.35208	0.30208	5.88542
-1.2 /	0.41667	0.26142	0.41667	0.62500	0.62500	0.62500	0.62500	0.62500	0.62500	0.62500	0.62500	5.7292
-1.1 /	0.31250	0.52083	0.31250	0.36458	0.36458	0.36458	0.36458	0.36458	0.36458	0.36458	0.36458	4.06250
-1.0 /	0.20833	0.44211	0.38554	0.36458	0.33854	0.33854	0.33854	0.33854	0.33854	0.33854	0.33854	3.69792
-0.9 /	0.18229	0.31250	0.33854	0.33854	0.26646	0.26646	0.26646	0.26646	0.26646	0.26646	0.26646	4.6667
-0.8 /	0.23933	0.36158	0.31250	0.33854	0.33854	0.33854	0.33854	0.33854	0.33854	0.33854	0.33854	3.43750
-0.7 /	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	2.99479
-0.6 /	0.23333	0.23333	0.23333	0.23333	0.23333	0.23333	0.23333	0.23333	0.23333	0.23333	0.23333	2.78646
-0.5 /	0.23442	0.26142	0.37854	0.31250	0.15625	0.15625	0.15625	0.15625	0.15625	0.15625	0.15625	2.52604
-0.4 /	0.36459	0.26142	0.15625	0.18229	0.18229	0.18229	0.18229	0.18229	0.18229	0.18229	0.18229	2.50010
-0.3 /	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	2.26562
-0.2 /	0.23437	0.26042	0.23437	0.31250	0.31250	0.31250	0.31250	0.31250	0.31250	0.31250	0.31250	2.42187
-0.1 /	0.18229	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	2.44792
-0.0 /	0.29646	0.28646	0.28646	0.28646	0.28646	0.28646	0.28646	0.28646	0.28646	0.28646	0.28646	2.78646
0.0 /	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000
0.1 /	0.10229	0.36458	0.65208	0.65208	0.31250	0.31250	0.31250	0.31250	0.31250	0.31250	0.31250	2.03125
0.2 /	0.28646	0.28646	0.15625	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	2.03125
0.3 /	0.18229	0.23437	0.23437	0.23437	0.15625	0.15625	0.15625	0.15625	0.15625	0.15625	0.15625	2.03125
0.4 /	0.23437	0.23437	0.23437	0.23437	0.18229	0.18229	0.18229	0.18229	0.18229	0.18229	0.18229	2.03125
0.5 /	0.31250	0.23437	0.23437	0.23437	0.15625	0.15625	0.15625	0.15625	0.15625	0.15625	0.15625	2.03125
0.6 /	0.26142	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	2.03125
0.7 /	0.23437	0.23437	0.23437	0.23437	0.18229	0.18229	0.18229	0.18229	0.18229	0.18229	0.18229	2.03125
0.8 /	0.23437	0.15625	0.15625	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	0.23437	2.03125
0.9 /	0.26042	0.33854	0.33854	0.33854	0.33854	0.33854	0.33854	0.33854	0.33854	0.33854	0.33854	2.03125
1.0 /	0.31250	0.33854	0.26142	0.26142	0.26142	0.26142	0.26142	0.26142	0.26142	0.26142	0.26142	2.03125
1.1 /	0.54687	0.28646	0.28646	0.49479	0.49479	0.49479	0.49479	0.49479	0.49479	0.49479	0.49479	3.41146
1.2 /	0.49479	0.44211	0.41667	0.28646	0.28646	0.46875	0.52083	0.52083	0.52083	0.52083	0.52083	3.88021
1.3 /	0.49479	0.7917	0.52083	0.80729	0.56687	0.80729	0.91146	0.91146	0.91146	0.91146	0.91146	4.60937
1.4 /	1.71875	1.64362	0.33854	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	8.85417

TOTAL DATA POINTS = 3840

V2 FILE 43 TAPE DTI-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

INTERVAL MIDPOINTS	.09	.08	.07	.06	.05	.04	.03	.02	.01	.00	TOTAL
-1.4 /	0.63000	0.60000	0.53573	0.58648	0.46392	1.59342	1.22911	1.12698	0.95717	0.88243	9.07525
-1.3 /	0.69675	0.77420	0.61559	0.61523	0.53304	0.44119	0.55840	0.24656	0.79617	5.86359	
-1.2 /	0.49399	0.42345	0.45356	0.40419	0.47824	0.42480	0.43362	0.50632	0.37360	0.32132	4.16829
-1.1 /	0.72564	0.43023	0.45223	0.46012	0.29256	0.37611	0.42616	0.45220	0.32267	0.32132	3.79422
-1.0 /	0.29184	0.21944	0.47757	0.32199	0.37376	0.26991	0.44949	0.37272	0.34871	0.34939	3.4493
-0.9 /	0.31725	0.26895	0.26923	0.39944	0.34600	0.37563	0.18947	0.26788	0.24116	0.37272	3.04674
-0.8 /	0.31793	0.29595	0.52897	0.26923	0.48975	0.23980	0.16235	0.37069	0.37543	0.18704	2.93715
-0.7 /	0.21444	0.21376	0.31395	0.21376	0.39741	0.13902	0.18975	0.39055	0.21647	0.31725	2.61187
-0.6 /	0.18772	0.31928	0.26923	0.08626	0.36865	0.34736	0.18500	0.31725	0.37611	0.13428	2.59115
-0.5 /	0.26991	0.21512	0.31657	0.21308	0.24116	0.13496	0.24048	0.16070	0.23912	0.21105	2.46450
-0.4 /	0.18844	0.31864	0.13563	0.37204	0.26855	0.29460	0.21376	0.23980	0.18704	0.31960	2.53703
-0.3 /	0.29324	0.26394	0.163L3	0.34668	0.1632	0.31996	0.21376	0.23980	0.24251	0.26652	2.51166
-0.2 /	0.31864	0.08423	0.21444	0.16168	0.26552	0.21172	0.34465	0.2112	0.34397	0.19111	2.35022
-0.1 /	0.23912	0.10756	0.24251	0.31657	0.15699	0.24116	0.29392	0.29553	0.10621	0.11163	2.00616
-0.0 /	0.29392	0.26788	0.26313	0.11163	0.36600	0.26516	0.23912	0.15896	0.13563	0.04049	2.48562
INTERVAL MIDPOINTS	.09	.08	.07	.06	.05	.04	.03	.02	.01	.00	TOTAL
0.0 /	0.31996	0.31725	0.34944	0.20969	0.24251	0.19043	0.18568	0.29526	0.18636	0.18975	2.18227
0.1 /	0.24183	0.28758	0.31657	0.21579	0.29188	0.11163	0.16168	0.31958	0.21172	0.29392	2.5031
0.2 /	0.34465	0.26534	0.29256	0.29324	0.24444	0.26923	0.18907	0.23759	0.24251	0.34668	2.5950
0.3 /	0.24329	0.13767	0.10824	0.13699	0.18636	0.24736	0.24319	0.18800	0.31793	0.29121	2.50827
0.4 /	0.37137	0.24448	0.24148	0.23912	0.29118	0.34125	0.37204	0.21607	0.18772	0.23777	2.43350
0.5 /	0.34668	0.26652	0.18635	0.21851	0.18975	0.34261	0.29392	0.21240	0.16959	0.31860	2.46694
0.6 /	0.34532	0.13531	0.16100	0.42345	0.25112	0.21725	0.24387	0.13418	0.37408	0.21308	2.55315
0.7 /	0.26584	0.42229	0.14174	0.23912	0.11434	0.29121	0.29128	0.23912	0.35007	0.67337	
0.8 /	0.42345	0.21244	0.18840	0.21444	0.37244	0.45085	0.18975	0.37244	0.21783	0.29460	2.77818
0.9 /	0.37137	0.24048	0.24048	0.40012	0.26727	0.26720	0.21647	0.40000	0.37340	0.26923	3.23378
1.0 /	0.32J64	0.39944	0.37137	0.26720	0.58377	0.37408	0.42480	0.32044	0.27127	0.34939	3.68259
1.1 /	0.27466	0.50225	0.37069	0.35143	0.55133	0.40419	0.42548	0.39808	0.42548	0.47824	4.16155
1.2 /	0.4C351	0.53236	0.37543	0.37204	0.559J8	0.43023	0.50632	0.4512	0.63924	0.33779	4.80754
1.3 /	0.61184	0.72211	0.76877	0.90644	0.84757	1.16606	1.09144	1.1818	1.49337	1.91501	10.6220
1.4 /	0.85205	0.65772	0.06393	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	1.66739

TOTAL DATA POINTS = 3840

V1 FILE 44 TAPE DTT-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

TOTAL DATA POINTS =

V2 FILE 44 TAPE DT-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

INTERVAL MIDPOINTS	.09	.08	.07	.06	.05	.04	.03	.02	.01	.00	TOTAL
-1.4 /	0.00309	6.03603	.32634	.07812	.88813	1.66707	1.58742	1.47502	1.08168	.93316	7.83664
-1.3 /	.90509	.64344	.59326	.74748	.55843	.48706	.45966	.71358	.37747	.58784	6.15506
-1.2 /	.58177	.35278	.47757	.37815	.35143	.50361	.47960	.48164	.45220	.26788	4.32861
-1.1 /	.43159	.50267	.50496	.47892	.37476	.21647	.50913	.24658	.37272	.42824	3.98600
-1.0 /	.34668	.39818	.35037	.37069	.45492	.29256	.42480	.27262	.16371	.37340	3.44754
-0.9 /	.37543	.26788	.39605	.13428	.45288	.32403	.16439	.39537	.34532	.29188	3.14752
-0.8 /	.29465	.21318	.19450	.21318	.24319	.34465	.26923	.34871	.24387	.31725	2.84112
-0.7 /	.23777	.45788	.24183	.34465	.21444	.26652	.26923	.34939	.18764	.34397	2.90771
-0.6 /	.23944	.26449	.42925	.42925	.21318	.31860	.29595	.26177	.21715	.24116	2.51031
-0.5 /	.21444	.31928	.26449	.29121	.28985	.14242	.29527	.29188	.19111	.29324	2.59318
-0.4 /	.18704	.29188	.29188	.34465	.34736	.18907	.18937	.39402	.24387	.10891	2.45850
-0.3 /	.26555	.34600	.29256	.16168	.29392	.21512	.19179	.29527	.21512	.18265	2.46365
-0.2 /	.23881	.31793	.16507	.21376	.39673	.13553	.26449	.26584	.24116	.23844	2.47884
-0.1 /	.19773	.26513	.13292	.16439	.26516	.24116	.26516	.18563	.18563	.26449	2.13759
-0.0 /	.18772	.24548	.26720	.29466	.18568	.10959	.21376	.24116	.29121	.53711	2.56850
	.00	.01	.02	.03	.04	.05	.06	.07	.08	.09	TOTAL
0.0 /											
.1 /	.26652	.21376	.26855	.39741	.13563	.26584	.24319	.24048	.21444	.18772	2.08483
.2 /	.13292	.18975	.26512	.23912	.21308	.29324	.23912	.31956	.29324	.21715	2.58775
.3 /	.21512	.11163	.31928	.18844	.24387	.24648	.21444	.28917	.21715	.23777	2.26983
.4 /	.15964	.26584	.23912	.29188	.13946	.18907	.18937	.21512	.21512	.29460	2.41157
.5 /	.05683	.23344	.29392	.29460	.28985	.29188	.32132	.31928	.29053	.18840	2.50285
.6 /	.18077	.35633	.21512	.21783	.19111	.31725	.26652	.29324	.21444	.21308	2.45483
.7 /	.21512	.21444	.19246	.44611	.15964	.37204	.11163	.40283	.24048	.29392	2.59793
.8 /	.23344	.53157	.27127	.26584	.26584	.21647	.37204	.31725	.24523	.39673	2.64865
.9 /	.29324	.29160	.29392	.42548	.26516	.50429	.29867	.32403	.39673	.24319	3.09068
1.0 /	.46012	.34338	.42277	.31996	.50157	.48628	.37340	.47852	.27127	.24523	3.79489
1.1 /	.50496	.52129	.37272	.50703	.27262	.37476	.45152	.45288	.48164	.37679	4.32319
1.2 /	.37476	.50105	.50105	.53144	.37476	.63721	.45152	.61523	.58716	.56247	5.23234
1.3 /	.77148	.77081	.1.00789	.80092	.85368	.1.32243	.1.32785	.1.43800	.1.69922	.94306	10.93614
1.4 /	.32444	.14038	.0.35208	.0.00003	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.51690

TOTAL DATA POINTS = 3840

V1 FILE 45 TAPE DTI-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

INTERNAL MIDPOINTS	.09	.08	.07	.06	.05	.04	.03	.02	.01	.00	TOTAL
-1.4 /	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	4.63542
-1.3 /	*.98956	*.98956	*.93750	*.85937	*.75521	*.62500	*.65104	*.62500	*.65104	*.62500	7.03854
-1.2 /	*.44271	*.49479	*.70312	*.44271	*.57292	*.39063	*.70312	*.39063	*.39063	*.39063	4.92187
-1.1 /	*.52183	*.33854	*.44271	*.41667	*.46375	*.36458	*.41667	*.31250	*.31250	*.31250	3.93229
-1.0 /	*.41667	*.41667	*.41667	*.36458	*.28646	*.26042	*.39063	*.26442	*.31250	*.31250	3.03333
-0.9 /	*.31250	*.28646	*.31250	*.28646	*.31250	*.18229	*.18229	*.20833	*.20833	*.20833	2.83854
-0.8 /	*.26442	*.26442	*.31250	*.31250	*.15025	*.23437	*.31250	*.31250	*.31250	*.31250	2.76042
-0.7 /	*.31250	*.1321	*.36458	*.28646	*.1321	*.41667	*.36458	*.13021	*.20833	*.20833	2.01250
-0.6 /	*.26442	*.26442	*.26442	*.26442	*.26442	*.31250	*.33854	*.15063	*.28646	*.28646	2.73437
-0.5 /	*.26442	*.18229	*.26442	*.13021	*.13021	*.33854	*.28646	*.18229	*.33854	*.33854	2.6042
-0.4 /	*.31250	*.26442	*.13021	*.28646	*.18229	*.10417	*.23437	*.49479	*.15025	*.15025	2.42187
-0.3 /	*.33954	*.15025	*.15025	*.20833	*.20833	*.26042	*.29646	*.28646	*.28646	*.28646	2.44792
-0.2 /	*.28646	*.23437	*.26442	*.26442	*.23437	*.33854	*.33854	*.28646	*.28646	*.28646	2.26562
-0.1 /	*.23437	*.26442	*.20833	*.23437	*.28646	*.26042	*.18229	*.20833	*.15025	*.15025	2.0833
-0.0 /	*.31250	*.23437	*.23437	*.23437	*.33854	*.26042	*.23437	*.18229	*.23437	*.23437	2.65625
0.0 /	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000

TOTAL DATA POINTS = 3840

V2 FILE 45 TAPE DTI-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

INTERVAL MIDPOINTS	.09	.08	.07	.06	.05	.04	.03	.02	.01	.00	TOTAL
-1.4 /	0.0600	0.0600	0.0600	0.0600	0.0600	0.0600	0.0600	0.0600	0.0600	0.0600	1.02405
-1.3 /	1.09348	0.75494	0.67546	0.64951	0.69946	0.48442	0.71940	0.45898	0.38142	0.64128	6.46246
-1.2 /	*58445	*45559	*35685	*55840	*56179	*40680	*47882	*53914	*26663	*27059	4.50317
-1.1 /	*50836	*52965	*35007	*42752	*50711	*21783	*53168	*29460	*39444	*42480	4.19095
-1.0 /	*37543	*27127	*40042	*34939	*37224	*31137	*34871	*24116	*34397	*34397	3.47222
- .9 /	*34668	*21579	*32044	*21579	*34668	*18704	*21376	*34804	*42752	*34261	2.96455
- .8 /	*24116	*18997	*34736	*16574	*29553	*34465	*19043	*29460	*20448	*26786	2.47189
- .7 /	*37272	*28979	*21773	*16371	*23777	*32335	*26991	*34193	*27227	*29731	2.78156
- .6 /	*26584	*21579	*21512	*18772	*13835	*42548	*34261	*19179	*21647	*18975	2.38352
- .5 /	*31923	*21738	*37137	*23777	*29527	*29392	*39537	*13952	*21048	*24116	2.71932
- .4 /	*21b47	*26729	*21172	*18975	*13496	*23749	*26652	*14309	*26449	*39808	2.32937
- .3 /	*13902	*34261	*13428	*31793	*24116	*24183	*26564	*16332	*24251	*21308	2.39856
- .2 /	*26788	*21444	*21512	*24193	*16235	*32132	*21579	*21444	*25152	*34600	2.41428
- .1 /	*21444	*18957	*18772	*29256	*21579	*29324	*21378	*18772	*16336	*31521	2.29519
-0.0 /	*24251	*31793	*21444	*21444	*13912	*29324	*39673	*23912	*29256	*40146	2.75146
	.00	.01	.02	.03	.04	.05	.06	.07	.08	.09	TOTAL
0.0 /											
*1 /	*21573	*31793	*23641	*24387	*21240	*18500	*31860	*29188	*26652	*38491	2.15752
*2 /	*29121	*28985	*18588	*40080	*19314	*21444	*36933	*11230	*3132	*16507	2.46772
*3 /	*18772	*31860	*13772	*31860	*16235	*31125	*23900	*26584	*26720	*29321	2.55493
*4 /	*21647	*10756	*29527	*24387	*21512	*16168	*21172	*37169	*16371	*16371	2.61515
*5 /	*24116	*31793	*21145	*26923	*19043	*37340	*26381	*34261	*13631	*26504	2.61176
*6 /	*14174	*26652	*47418	*21444	*26584	*29460	*21512	*34261	*29392	*20355	
*7 /	*23777	*29324	*21519	*37476	*31860	*29663	*21713	*34397	*39402	*29188	2.98448
*8 /	*21647	*24251	*37272	*45085	*24148	*24048	*35143	*23777	*38673	*31928	3.36871
*9 /	*31996	*32267	*37408	*29867	*31928	*45085	*32539	*34193	*53101	*27059	3.55442
1.0 /	*27127	*34844	*24465	*42036	*45224	*37611	*34610	*42820	*37343	*42616	3.68558
1.1 /	*53168	*30206	*53033	*35007	*45288	*50564	*58377	*42955	*45559	*53033	4.67190
1.2 /	*51039	*58173	*5596	*79007	*84486	*44367	*56044	*61663	*72279	*60913	6.28147
1.3 /	*56993	*98863	*96124	*1.50675	*1.66843	*1.55613	*1.28086	*63083	*25704	*0.3961	9.46854
1.4 /	*0.2604	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.02604

TOTAL DATA POINTS = 3840

V1 FILE #6 TAPE OTT-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

TOTAL DATA POINTS = 3840

V2 FILE 46 TAPE DTJ-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

TOTAL DATA POINTS = 3840

V1 FILE 47 TAPE DTT-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

TOTAL DATA POINTS = 3860

V2 FILE 47 TAPE OTT-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

TOTAL DATA POINTS = 3840

V1 FILE 48 TAPE DTT-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

TOTAL DATA PCINIS = 3840

V2 FILE 48 TAPE DTI-2  
PERCENTAGES OF TIME WITHIN VOLTAGE INTERVALS

INTERNAL MIDPOINTS	.19	.68	.07	.66	.05	.04	.03	.02	.01	.00	TOTAL
-1.4 / 0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000
-1.3 / 0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000
-1.2 / 0.67008	0.91146	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000
-1.1 / 0.42752	0.62812	0.61378	0.62134	0.43769	0.46706	0.43566	0.43159	0.63449	0.48570	0.51280	3.62956
-1.0 / 0.39944	0.48231	0.21851	0.37679	0.53440	0.34532	0.27262	0.14377	0.40469	0.42413	0.00000	0.00000
-0.9 / 0.39673	0.45215	0.42752	0.29663	0.19450	0.32267	0.27059	0.34668	0.33081	0.21300	0.00000	0.00000
-0.8 / 0.26584	0.29124	0.29124	0.37679	0.50622	0.18975	0.08965	0.29392	0.23376	0.26923	2.78700	0.00000
-0.7 / 0.37069	0.34532	0.24116	0.24116	0.24319	0.46080	0.27127	0.26652	0.21444	0.31928	2.91382	0.00000
-0.6 / 0.27195	0.19518	0.31657	0.27195	0.16371	0.24251	0.24251	0.34736	0.34603	0.28985	2.68758	0.00000
-0.5 / 0.32132	0.24555	0.19500	0.26584	0.26584	0.37069	0.29460	0.19111	0.21512	0.18772	0.22354	0.00000
-0.4 / 0.34465	0.26652	0.26923	0.18997	0.29527	0.26991	0.29121	0.21502	0.24483	0.19179	2.44290	0.00000
-0.3 / 0.24251	0.19246	0.50361	0.19643	0.11570	0.18734	0.24048	0.21783	0.32267	0.16371	2.37644	0.00000
-0.2 / 0.24387	0.26855	0.33886	0.34660	0.16313	0.23980	0.34736	0.21376	0.21115	0.19179	2.54951	0.00000
-0.1 / 0.32433	0.23844	0.168946	0.29324	0.26652	0.32471	0.31860	0.16100	0.34939	0.40690	2.85129	0.00000
0.0 / 0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000
0.1 / 0.21783	0.26720	0.19111	0.29392	0.32064	0.14038	0.26708	0.35357	0.13631	0.3069	0.18704	2.14912
0.2 / 0.32199	0.33364	0.29324	0.32235	0.29527	0.34804	0.26933	0.24116	0.31996	0.28117	0.13835	2.52794
0.3 / 0.42413	0.21647	0.19111	0.18975	0.16574	0.26923	0.29663	0.31996	0.34344	0.40215	2.61081	0.00000
0.4 / 0.34804	0.42616	0.11027	0.44949	0.34939	0.24455	0.18975	0.19111	0.34397	0.27398	3.32343	0.00000
0.5 / 0.47969	0.18772	0.32667	0.34668	0.42820	0.26991	0.34397	0.22054	0.51496	0.49954	3.59051	0.00000
0.6 / 0.37137	0.32453	0.18440	0.32064	0.47553	0.29595	0.24048	0.42209	0.50429	0.39537	0.39944	3.86143
0.7 / 0.42413	0.45424	0.31397	0.29663	0.31725	0.58105	0.50429	0.21783	0.68590	0.61117	0.63721	5.26842
0.8 / 0.44969	0.52965	0.53129	0.44610	0.39876	0.50429	0.68590	0.61117	0.55705	0.55569	0.84554	6.65473
0.9 / 0.53760	1.05320	1.15872	1.23752	1.59871	1.67887	2.22711	1.83716	1.3022	0.71872	1.32723	0.00000
1.0 / 0.20634	0.15656	0.01628	0.01695	0.02441	0.02848	0.13798	0.05086	0.02645	0.00746	0.42526	0.00000
1.1 / 0.01336	0.00100	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000
1.2 / 0.00064	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000
1.3 / 0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000
1.4 / 0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000	0.00000

TOTAL DATA POINTS = 3840

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13. ABSTRACT
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This report describes a new magnetic tape recording system for general instrumentation use. The system uses a new type of modulation format and offers excellent performance at low cost. The system concepts are explored from a subsystem or "block diagram" viewpoint, and extensions of these concepts are hypothesized. An actual prototype is described, its specifications and performance parameters given, and the results of its evaluation program presented. The history of the system from 1967 to 1971 is also included for completeness. The differential pulse width modulation (DPWM) concept is considered to represent a truly significant advance in modulation techniques.

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Security Classification

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Magnetic tape recorder						
Digital magnetic tape recorder						
Low-cost tape recorder						
Portable tape recorder						
Computer studies						
Tape recorder						
Instrumentation tape recorder						